# $\mathrm{AC}^{0}[p]$ Lower Bounds and NP-Hardness for Variants of MCSP 

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-_ Abstract

The Minimum Circuit Size Problem (MCSP) asks whether a (given) Boolean function has a circuit of at most a (given) size. Despite over a half-century of study, we know relatively little about the computational complexity of MCSP. We do know that questions about the complexity of MCSP have significant ramifications on longstanding open problems. In a recent development, Golovnev et al. [11] improve the status of unconditional lower bounds for MCSP, showing that MCSP $\notin \mathrm{AC}^{0}[p]$ for any prime $p$. While their results generalize to most "typical" circuit classes, it fails to generalize to the circuit minimization problem for depth- $d$ formulas, denoted ( $\mathrm{AC}_{d}^{0}$ )-MCSP. In particular, their result relies on a Lipchitz hypothesis that is unknown (and possibly false) in the case of ( $\mathrm{AC}_{d}^{0}$ )-MCSP. Despite this, we show that $\left(\mathrm{AC}_{d}^{0}\right)$-MCSP $\notin \mathrm{AC}^{0}[p]$ by proving even the failure of the Lipchitzness for $\mathrm{AC}_{d}^{0}$ formulas implies that MAJORITY $\leq_{t t}^{\mathrm{AC}^{0}}\left(\mathrm{AC}_{d}^{0}\right)-\mathrm{MCSP}$. Somewhat remarkably, our proof (in the case of non-Lipchitzness) uses completely different techniques than [11]. To our knowledge, this is the first MCSP reduction that uses modular properties of a function's circuit complexity.

We also define MOCSP, an oracle version of MCSP that takes as input a Boolean function $f$, a size threshold $s$, and oracle Boolean functions $f_{1}, \ldots, f_{t}$, and determines whether there is an oracle circuit of size at most $s$ that computes $f$ when given access to $f_{1}, \ldots, f_{t}$. We prove that MOCSP is NP-complete under non-uniform $\mathrm{AC}^{0}$ many-one reductions as well as (uniform) ZPP truth table reductions. We also observe that improving this ZPP reduction to a deterministic polynomial-time reduction requires showing EXP $\neq$ ZPP (using theorems of Hitchcock and Pavan [17] and Murray and Williams [22]). Optimistically, these MOCSP results could be a first step towards NP-hardness results for MCSP. At the very least, we believe MOCSP clarifies the barriers towards proving hardness for MCSP and provides a useful "testing ground" for questions about MCSP.

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## 1 Introduction

The Minimum Circuit Size Problem (MCSP) takes as input a Boolean function $f$ (represented by its truth table) and a size parameter $s$ and asks if there is a circuit of size at most $s$ computing $f$. Study of this problem began in the 1950s by complexity theorists in the Soviet Union [30], where MCSP was of such great interest that Levin is said to have delayed
publishing his initial NP-completeness results in hope of showing that MCSP is NP-complete. ${ }^{1}$ Interest in MCSP was revitalized when Kabanets and Cai [19] connected the problem with the natural proofs framework of Razborov and Rudich [27]. Since then, MCSP has been the subject of intense research. We begin by reviewing some of this work.

### 1.1 Known lower bounds, hardness, and non-hardness for MCSP

It is easy to see that MCSP is in NP (the circuit of size at most $s$ can be used as a witness), but, despite work by numerous researchers, the exact complexity of MCSP remains unknown.

Lower bounds and hardness results. We believe MCSP is not easy to compute. Kabanets and Cai [19] show that MCSP $\notin \mathrm{P}$ conditioned on a widely-believed cryptographic hypothesis, and Allender and Das [2] show that MCSP is hard for SZK under BPP-Turing reductions.

Unconditionally, we know lower bounds against MCSP for restricted classes of circuits. Hirahara and Santhanam [15] show that MCSP requires nearly quadratic sized DeMorgan formulas, and Allender et al. [1] prove that MCSP $\notin \mathrm{AC}^{0}$. In a recent paper, Golovnev et al. [11] improve the latter result, showing that MCSP requires exponential-sized $\mathrm{AC}^{0}[p]$ circuits by proving MAJORITY $\in\left(\mathrm{AC}^{0}\right)^{\text {MCSP }}$. The MAJORITY hardness result of [11] generalizes to the circuit minimization problem for many circuit classes, however, the techniques fail in the case of constant depth formulas.

Under weak reductions, we know MCSP is hard for some subclasses of P. Oliveira and Santhanam [25] prove that MCSP is hard for DET under TC ${ }^{0}$ truth table reductions, and Golovnev et al. [11] use the results of [25] to show that $N C^{1} \subseteq\left(A C^{0}\right)^{M C S P}$. Surprisingly, we know stronger results for the "program" variant of MCSP, MKTP. Allender and Hirahara [3] show that MKTP is hard for DET under NC ${ }^{0}$ many-one reductions, and Hirahara and Santhanam [15] show average-case lower bounds for MKTP against AC $^{0}[p]$.

The most natural question is whether MCSP is NP-complete. As of yet, we have not managed to uncover even strong supporting evidence for, or against, MCSP being NPcomplete. We do know that the circuit minimization problem is NP-complete for some restricted classes of circuits: DNF circuits by Masek [20] and OR $\circ$ AND $\circ \mathrm{MOD}_{m}$ circuits by Hirahara, Oliveira, and Santhanam [14]. Impagliazzo, Kabanets, and Volkovich [18] show that if there exist Indistinguishability Obfuscators against randomized polynomial-time algorithms, then MCSP $\in$ ZPP $\Longleftrightarrow$ NP $=$ ZPP.

Known non-hardness results. The unconditional non-hardness results for MCSP rule out NP-hardness under certain types of reductions. For example, Hirahara and Watanabe [16] show that "oracle-independent reductions" cannot show that MCSP is hard for either a class larger than $P$ under polynomial-time Turing reductions or a class larger than $A M \cap$ coAM under BPP reductions with one query to MCSP. Moreover, while most NP-complete problems are complete under rather weak reductions such as $\operatorname{TIME}\left[n^{o(1)}\right]$ or $\mathrm{AC}^{0}$ many-one reductions, Murray and Williams [22] prove that MCSP is not NP-hard under TIME $\left[n^{49}\right]$ reductions, and Allender, Ilango, and Vafa [5] show that a super-linear approximations of MCSP cannot be NP-hard under even non-uniform $\mathrm{AC}^{0}$ many-one reductions.

Conditioned on a widely-believed cryptographic hypothesis, Allender and Hirahara [3] show that a very weak approximation of MCSP is NP-intermediate.

[^0]
### 1.2 Implications of lower bounds and hardness for MCSP

While we have not managed to establish the complexity of MCSP, a series of works, beginning with Kabanets and Cai [19], connect the computational complexity of MCSP and its variants to longstanding open questions in the field.

Separations of complexity classes. Several works ([17], [22], [2], [19]) show that MCSP being NP-hard, under various notions of reducibility, implies unknown class separations. For example, Hitchcock and Pavan [17] and Murray and Williams [22] show that if MCSP is NP-hard under polynomial-time truth-table reductions, then ZPP $\neq$ EXP, a major open problem. ${ }^{2}$

Worst-case versus average-case complexity for NP. Using tools developed by Nisan and Wigderson [23] and Carmosino, Impagliazzo, Kabanets, and Kolokova [9], Hirahara [13] gives a "worst-case to average-case" reduction for NP conditioned on a certain approximation to MCSP being NP-hard. Thus, if one could show this approximation to MCSP is NP-hard, the worst-case and average-case complexity of NP would be equivalent.

Circuit Lower Bounds. Recent work by Oliveira, Pich, and Santhanam ([26] and [24]) ${ }^{3}$ explores a phenomenon they term "hardness magnification," whereby even weak circuit lower bounds on certain computational problems imply strong lower bounds on other problems. For example, [26] shows that if MCSP cannot be solved on average with no error by linear-size formulas, then NP does not have polynomial-size formulas. [24] shows that if a certain approximation to MCSP cannot be computed by circuits of size $n^{1+\epsilon}$, then NP does not have polynomial-sized circuits.

### 1.3 Our Contributions

In this work, we focus on hardness results for variants of MCSP, in particular establishing an $\mathrm{AC}^{0}[p]$ lower bound and an NP-hardness result.

## MAJORITY-hardness for $\left(\mathrm{AC}_{d}^{0}\right)$-MCSP

As mentioned previously, Golovnev et al. [11] proves that MAJORITY $\in\left(\mathrm{AC}^{0}\right)^{\mathrm{MCSP}}$. Using similar techniques, they also show that, for restricted classes of circuits $\mathcal{C}$ such as formulas and constant depth circuits, the $\mathcal{C}$-circuit minimization problem, denoted $(\mathcal{C})$-MCSP, is hard for MAJORITY under AC $^{0}$ reductions. For these MAJORITY reductions to work, [11] requires that the size of the minimum $\mathcal{C}$-circuit on truth tables of length $n$ is roughly $\left(n^{.49}\right)$-Lipchitz.

This Lipchitzness hypothesis is unknown (and perhaps even false) in the class of depth-d formulas, which we denote $\mathrm{AC}_{d}^{0}{ }^{4}$ Despite this, we prove MAJORITY-hardness for $\left(\mathrm{AC}_{d}^{0}\right)$-MCSP by giving a MAJORITY reduction that works in the case that Lipchitzness fails. Applying the lower bounds of Razborov [27] and Smolensky [29] then gives an $\mathrm{AC}^{0}[p]$ lower bound for $\left(\mathrm{AC}_{d}^{0}\right)$-MCSP.

[^1]$\mathrm{AC}^{0}[p]$ for any prime $p$.
Remarkably, the techniques used for this MAJORITY reduction (in the case of nonLipchitzness) are entirely different than the ones used by [11] for general MCSP. Indeed, the non-Lipchitz case reduction we present is of a very different flavor than, to our knowledge, all known MCSP hardness results. As far as the author knows, it is the only MCSP hardness result that does not easily generalize to an approximation of MCSP. This is because the key step in the reduction is determining, exactly, a Boolean function's circuit complexity modulo a certain prime.

- Open Question 1.2. Can one extend Theorem 1.1 to an approximation of MCSP?

We also remark that our notion of size for $\mathrm{AC}_{d}^{0}$ formulas is critical for Theorem 1.1. We define the size of an $\mathrm{AC}_{d}^{0}$ formula to be the number of input leaves. While this is the standard definition of formula size, we make heavy use of elementary direct product theorems known, specifically, for this notion of formula size. It is not clear to us how to generalize Theorem 1.1 to the case when the size of an $\mathrm{AC}_{d}^{0}$ formula is, say, the number of gates or the number of wires.

## NP-Hardness of oracle MCSP (MOCSP)

Some work has been done trying to approach the NP-hardness of MCSP "from below," that is, proving that the circuit minimization problem is NP-hard for restricted classes of circuits. As mentioned previously, we know that (DNF)-MCSP [20] and (OR $\circ$ AND $\left.\circ \mathrm{MOD}_{m}\right)$-MCSP [14] are NP-hard.

Instead, we attempt to approach MCSP from "above." We formulate the Minimum Oracle Circuit Size Problem, denoted MOCSP, that takes as input a truth table $T$, a size parameter $s \in \mathbb{N}$, and auxiliary truth tables $T_{1}, \ldots, T_{t}$ and asks whether there is an oracle circuit of size at most $s$ that computes $T$ when given access to $T_{1}, \ldots, T_{t}$. It is easy to see that MOCSP $\in \mathrm{NP}$ (the oracle circuit of size $s$ acts as a witness).

We note that this is not the first time someone has considered an "oracle version" of MCSP. Allender et al. [1] and Allender, Holden, and Kabanets [4] consider the problem of minimizing oracle circuits for a fixed oracle A. We will denote this problem MCSP ${ }^{\text {A }}$. An important result for this problem that [1] proves is that MCSP ${ }^{\text {QBF }}$ is complete for PSPACE under ZPP reductions. MOCSP differs from MCSP ${ }^{A}$ in that the oracle circuit gets access to a finite number of Boolean functions, not a language, and the functions the oracle circuit has access to are inputs to the problem.

In our view, MOCSP has two advantages over MCSP ${ }^{A}$. First, MOCSP $\in N P$ while the complexity of MCSP ${ }^{A}$ depends on the oracle A. Second, there is an easy reduction from MCSP to MOCSP, simply provide no oracle truth tables. Therefore, we can use MOCSP as a testing ground for hardness results we conjecture for MCSP. Thus, the most natural question is whether we can prove that MOCSP is NP-hard. We prove that MOCSP is indeed NP-hard under non-uniform $A C^{0}$ reductions and under uniform randomized reductions.

- Theorem 1.3. $\quad \mathrm{NP} \leq{ }_{m}^{\mathrm{AC}^{0}} \mathrm{MOCSP}$
- NP $\leq_{m}^{R P}$ MOCSP
- NP $\leq_{t t}^{\text {ZPP }}$ MOCSP

These NP-hardness results are all proved by giving a reduction from approximating $r$ bounded set cover to MOCSP. It is worth noting that the NP-hardness results of (DNF)-MCSP $[20]$ and $\left(\mathrm{OR} \circ \mathrm{AND} \circ \mathrm{MOD}_{m}\right)$-MCSP [14] are also proved via set cover problems.

Given that we can show MOCSP is NP-hard under randomized reductions, one might even begin to hope that we can prove hardness under, say, polynomial-time truth table reductions. Unfortunately, this seems difficult. Essentially the same proofs Murray and Williams [22] or Hitchcock and Pavan [17] use to show that MCSP being NP-hard under polynomial-time truth table reductions implies EXP $\neq$ ZPP also works for MOCSP.

- Theorem 1.4 (Essentially proven in [17] and [22]). If $\mathrm{NP} \leq_{t t}^{\mathrm{P}} \mathrm{MOCSP}$, then EXP $\neq \mathrm{ZPP}$.

Thus, improving our ZPP reduction to a $P$ reduction requires separating EXP from ZPP, a longstanding open problem. For completeness, we give the MOCSP version of Murray and Williams' proof in Appendix B.

Even so, we expect that the ground truth is that MOCSP is NP-hard under, at least, polynomial-time Turing reductions.

- Conjecture 1.5. NP $\leq_{T}^{P} M O C S P$.

We give some details on why we believe Conjecture 1.5 near the end of Section 4. Even so, we believe proving such a hardness result is beyond current techniques. Perhaps one could even prove there are some barriers.

## A perspective on MOCSP and some questions.

In light of the fact that hardness for MCSP beyond SZK under even non-uniform reductions is unknown, we found these MOCSP hardness results to be quite surprising. To an optimist, NP-hardness results for MOCSP could even be a first step towards proving hardness for MCSP. Indeed, a PSPACE-hardness result was first proved by Buhrman and Torenvliet [8] for an "oracle" version of space-bounded Kolmogorov complexity before Allender et al. [1] showed PSPACE-hardness for the non-oracle version about four years later. ${ }^{5}$

Even if stronger hardness results for MCSP remain out of reach, MOCSP could still yield valuable insights about MCSP. For instance, it would be interesting to see which of the barriers and non-hardness results known for MCSP carry over to MOCSP.

- Open Question 1.6. Can one show that other barriers or non-hardness results that hold for MCSP also hold for MOCSP?

As an example of the insight given by answers to this question, consider Murray and Williams' [22] result that proving MCSP is NP-hard under polynomial-time many-one reductions implies EXP $\neq$ ZPP. A natural question one might ask is whether we can improve this theorem to show that MCSP being NP-hard under randomized reductions implies unknown class separations. As we note in Theorem 1.4, however, Murray and Williams' proof carries over to MOCSP, and Theorem 1.3 shows MOCSP is indeed NP-hard under randomized reductions. Thus, any improvement of Murray and Williams' result to randomized reductions likely requires a fact about MCSP that we do not know for MOCSP.

In another direction, our results seem to imply that proving hardness for MOCSP is easier than proving hardness for MCSP. Indeed, since MCSP easily reduces to MOCSP, any hardness result that is true for MCSP must also be true for MOCSP. Therefore, we can use MOCSP as a testing ground for hardness results we conjecture about MCSP. For example, Hirahara's [13] worst-case to average-case reduction for NP can be based on a certain approximation of MCSP being NP-hard, which would imply that a certain approximation of MOCSP is

[^2]also NP-hard. Given that we can prove the NP-hardness of MOCSP under uniform ZPP reductions and non-uniform $A C^{0}$ reductions, we ask if one can prove something similar for the approximation version of MOCSP.

- Open Question 1.7. Can one prove that, for some $\epsilon>0$, approximating MOCSP on $n$-inputs to a factor of $n^{\epsilon}$ is NP-hard under, say, $\mathrm{P} /$ poly reductions? Conversely, can one prove that there is any barrier to showing such a hardness result?

We note that the techniques we use to prove NP-hardness results for MOCSP seem to break down completely in the case of even super-constant approximation, so answering this question will likely require new ideas.

### 1.4 Proof Overviews

In this section, we give fairly detailed overviews of our proofs. In doing this, we will often state results without filling in low-level details. To make clear to the reader when we are doing this, we mark such sentences with an italicized we observe.

## Majority Hardness for $\left(\mathrm{AC}_{d}^{0}\right)$-MCSP

Recall, $\mathrm{AC}_{d}^{0}$ is the class of depth- $d$ formulas. We also define $\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}$ and $\mathrm{OR} \circ \mathrm{AC}_{d-1}^{0}$ be the classes of $\mathrm{AC}_{d}^{0}$ formulas with a top AND and top OR gate respectively. For $\mathcal{F} \in$ $\left\{\mathrm{AC}_{d}^{0}, \mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}, \mathrm{OR} \circ \mathrm{AC}_{d-1}^{0}\right\}$ and a truth table $T$, we let $\mathrm{CC}_{\mathcal{F}}(T)$ denote the size of the minimum $\mathcal{F}$-formula computing $T$ where the size of a formula is the number of input leaves.

Our analysis proceeds by considering each $n \in \mathbb{N}$ and splitting into cases depending on whether $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}$ is Lipchitz on truth tables of length around $n$. In more detail, fix some sufficiently large $n$. Let $q=\Theta\left(n^{2}\right)$ be a power of two. We divide into cases depending on whether there exists an $m \in\left\{q^{10}, q^{50}\right\}$ such that $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}$ is $\left(m^{.25}\right)$-Lipchitz on truth tables of length $m$.

## Case 1: Lipchitzness holds for some $m$.

If there does exist an $m \in\left\{q^{10}, q^{50}\right\}$ such that $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}$ is $\left(m^{25}\right)$-Lipchitz on truth tables of length $m$, then the techniques of [11] yield an $\mathrm{AC}^{0}$ truth table reduction from MAJORITY on $n$-bits to $\left(\mathrm{AC}_{d}^{0}\right)$-MCSP on $m$-bits. For completeness, we include a self-contained proof of this case in Appendix A.

## Case 2: Lipchitzness fails.

Assume that for all $m \in\left\{q^{10}, q^{50}\right\} \mathrm{CC}_{\mathrm{AC}_{d}^{0}}$ is not $\left(m^{.25}\right)$-Lipchitz on truth tables of length $m$. Let $u=q^{10}$ and $v=q^{50}$.

Lipchitzness failing $\Longrightarrow$ functions easier to compute with a top AND gate. We observe, as a straight forward consequence of Lipchitzness failing, that there exists a truth table of length $u$ that has an optimal formula with large top fan-in and and a truth table of length $v$ that is easier to compute with a top AND gate:

1. There exists a Boolean function $f^{u}$ that takes $\log u$ inputs and an $\mathrm{AC}_{d}^{0}$ formula $\phi^{u}$ such that $\phi^{u}$ is an optimal $\mathrm{AC}_{d}^{0}$ formula for $f^{u}$ and $\phi^{u}=\phi_{1}^{u} \wedge \cdots \wedge \phi_{t}^{u}$ for some $t \geq n$ and some $\phi_{1}^{u}, \ldots, \phi_{t}^{u} \in \mathrm{AC}_{d-1}^{0}$.
2. There exists a Boolean function $f^{v}$ that takes $\log v$ inputs such that $\mathrm{CC}_{\mathrm{OR} \circ \mathrm{AC}_{d-1}^{0}}\left(f^{v}\right)>$ $\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f^{v}\right)+u \log u$.
We will make use of $f^{u}$ and $\phi^{u}$ to reduce MAJORITY to $\mathrm{CC}_{\text {AND } \circ \mathrm{AC}_{d-1}^{0}}$ and we will use $f^{v}$ to reduce $\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}$ to $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}$.

Using $\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}$ and optimal subformulas of $\phi^{u}$ to compute a dot product. The heart of our MAJORITY reduction is a fairly elementary observation about optimal ( $\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}$ ) formulas. Recall, $\phi^{u}=\phi_{1}^{u} \wedge \cdots \wedge \phi_{t}^{u}$ is an optimal $\left(\mathrm{AC}_{d}^{0}\right)$ formula for $f^{u}$ and, hence, also an optimal $\left(\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}\right)$ formula for $f^{u}$. We observe that for any $A \subseteq[t]$, the $\left(\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}\right)$ ) optimality of $\phi^{u}$ implies that $\bigwedge_{i \in A} \phi_{i}^{u}$ is also an optimal (AND $\circ \mathrm{AC}_{d-1}^{0}$ ) formula for the function it computes.

Introducing some notation, for a string $x \in\{0,1\}^{n}$, we let $f_{x}^{u}$ be the function given by $\bigwedge_{i \in O_{x}} \phi_{i}^{u}$ where $O_{x} \subseteq[n]$ are the bits in $x$ that are one. Using the above observation about subformulas being optimal, we have that $\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f_{x}^{u}\right)=\sum_{i \in O_{x}}\left|\phi_{i}^{u}\right| .{ }^{6}$ Thus, one can think of $\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f_{x}^{u}\right)$ as computing the dot product between $x$ and the vector $\langle | \phi_{1}^{u}\left|, \ldots,\left|\phi_{n}^{u}\right|\right\rangle$.

Note that that the definition of $f_{x}^{u}$ depends on the labeling of $\phi_{1}^{u}, \ldots, \phi_{t}^{u}$, in particular the choice of which $\phi_{i}^{u}$ have $i \leq n$. We will later choose an labeling of the $\phi_{i}^{u}$ that is convenient.

## Computing MAJORITY (non-uniformly) using $\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}$. Our goal is to compute

 MAJORITY on a string $x \in\{0,1\}^{n}$ using the above "dot product" observation. Before we show how to do this, we give some intuition on how we came up with the idea.Instead of trying to compute MAJORITY, suppose we relaxed the problem to computing PARITY given access to the integer produced by the dot product $x \cdot\langle | \phi_{1}^{u}\left|, \ldots,\left|\phi_{n}^{u}\right|\right\rangle$. Well, if it so happened that all the entries in the vector $\langle | \phi_{1}^{u}\left|, \ldots,\left|\phi_{n}^{u}\right|\right\rangle$ were odd, then it is clear that the integer produced by $x \cdot\langle | \phi_{1}^{u}\left|, \ldots,\left|\phi_{n}^{u}\right|\right\rangle$ is odd if and only if $x$ has an odd number of ones. Our approach for MAJORITY is a generalization of this.

Let $p=O(n)$ be prime greater than $n$. We observe, via an averaging argument, that there exists integers $k \geq 0$ and $1 \leq r \leq p-1$ such that (after relabeling the $\phi_{i}^{u}$ )

$$
\left|\phi_{1}^{u}\right| / p^{k} \equiv \cdots \equiv\left|\phi_{n}^{u}\right| / p^{k} \equiv r \quad \bmod p .
$$

Thus, we can determine the weight $w$ of $x$ (and hence compute MAJORITY of $x$ ) by computing the value of

$$
\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f_{x}^{u}\right) / p^{k}=\sum_{i \in O_{x}}\left|\phi_{i}^{u}\right| / p^{k} \equiv r w \quad \bmod p
$$

and multiplying by the inverse of $r$ modulo $p .{ }^{7}$

Reducing computing $\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}$ to computing $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}$. Ultimately, we want to compute MAJORITY using $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}$ not $\mathrm{CC}_{\mathrm{ANDoAC}}^{d-1} 0$. By the above procedure, it suffices to show how to compute $\mathrm{CC}_{\mathrm{AND} \mathrm{\circ AC}_{d-1}^{0}}\left(f_{x}^{u}\right)$ using $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}$.

[^3]We can make such a computation as follows. Recall that $f^{v}$ is a function satisfying

$$
\mathrm{CC}_{{\mathrm{OR} \circ \mathrm{AC}_{d-1}^{0}}\left(f^{v}\right)>\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f^{v}\right)+u \log u .}
$$

whose existence is guaranteed by the failure of Lipchitzness. Take the direct product of $f_{x}^{u}$ with $f^{v}$ to obtain a function $g_{x}(y, z)=f_{x}^{u}(y) \wedge f^{v}(z)$. Since the difference between computing $f^{v}$ with a top AND gate and a top OR gate is larger than $u \log u$ (which is the maximum complexity of $f_{x}^{u}$, we observe ${ }^{8}$ any optimal $\mathrm{AC}_{d}^{0}$ formula for $g_{x}$ must have a top AND gate, so

$$
\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(g_{x}\right)=\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(g_{x}\right) .
$$

Then, we observe that

$$
\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(g_{x}\right)=\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f_{x}^{u}\right)+\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f^{v}\right)
$$

Hence, if we are non-uniformly given the value of $\operatorname{CC}_{{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f^{v}\right) \text {, we can subtract }}$ $\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f^{v}\right)$ from $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(g_{x}\right)$ to find $\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f_{x}^{u}\right)$.

## NP-hardness of Oracle MCSP (MOCSP)

We define the size of an oracle circuit to be the total number of AND, OR, and oracle gates. The Minimum Oracle Circuit Size Problem, MOCSP, takes as input a truth table $T$, a threshold $s \in \mathbb{N}$, and auxiliary truth tables $T_{1}, \ldots T_{t}$ and outputs whether there is an oracle circuit of size at most $s$ that computes $T$ when given oracle access to $T_{1}, \ldots, T_{t}$. We denote the output of MOCSP on such an input as $\operatorname{MOCSP}\left(T, s ; T_{1}, \ldots, T_{t}\right)$. We denote the minimum size of any oracle circuit computing $T$ when given access to $T_{1}, \ldots, T_{t}$ as $\mathrm{CC}^{T_{1}, \ldots, T_{t}}(T)$.

We prove that MOCSP is NP-hard under various reductions by giving a reduction from 4 -approximating $r$-bounded set cover, denoted 4 - ${\text { Set } \text { Cover }_{r} \text {, to MOCSP. As a reminder, }}^{\text {, }}$ 4 -Set Cover $_{r}$ is the promise problem takes as input sets $S_{1}, \ldots, S_{t} \subseteq[n]$ of cardinality at most $r$ whose union is $[n]$ as well as an integer $c \in[n]$ and requires outputting YES when $c \geq \ell$ and NO when $c<\ell / 4$ where $\ell$ is the optimal cover size, i.e.

$$
\ell=\min \left\{|I|: I \subseteq[t] \text { and } \bigcup_{i \in I} S_{i}=[n]\right\}
$$

For sufficiently large $r, 4$ - SetCover $_{r}$ is known to be NP-hard (see Theorem 2.6).

Informal idea. We begin by giving a high-level overview of the reduction to orient the reader. (It will be very informal, but we are building to a more detailed description.) Say we are given sets $S_{1}, \ldots, S_{t} \subseteq[n]$ of cardinality at most $r$ whose union is [ $n$ ]. One can think of each of these sets $S_{i}$ as "seeing" a small portion of the ground set [ $n$ ]. For some carefully chosen truth table $T$ of length $m \geq n$, we let each set $S_{1}, \ldots, S_{t}$ induce truth tables $T_{S_{1}}, \ldots, T_{S_{t}}$ respectively where each truth table $T_{S_{i}}$ "sees" roughly the same part of $T$ as $S_{i}$ "sees" of $[n]$. Finally, we ask how hard it is for a circuit to compute $T$ given oracle access to $T_{1}, \ldots, T_{t}$, and we show that, if $T$ has a certain property, then the answer to this question is the answer to the set cover problem up to a constant factor.

We now illustrate the algorithm in more detail. Fix sets $S_{1}, \ldots, S_{t} \subseteq[n]$ of cardinality at most $r$ whose union is $[n]$ and fix a truth table $T$ of length $m$. Assume the optimal cover size of $[n]$ by $S_{1}, \ldots, S_{t}$ is $\ell$.

[^4]The truth tables induced by $S_{1}, \ldots, S_{t}$ and $T$. We rigorously define the truth tables $T_{S_{1}}, \ldots, T_{S_{t}}$ of length $m$ induced by $S_{1}, \ldots, S_{t}$ and $T$. First, we fix a partition of $[m$ ] into $n$ sets. It does not really matter what partition we choose as long as the sets are roughly the same size and the partition is easily computable, but, for concreteness, let $P_{1}, \ldots, P_{n} \subseteq[m]$ be the partition of $[m]$ given by $P_{i}=\{j \in[m]: j \equiv i \bmod n\}$.

We can then use this partition to "lift" any subset of $[n]$ into a subset of $[m]$ as follows. For a subset $S$ of $[n]$, let $S^{m}$ denote the subset of $[m]$ given by $S^{m}=\bigcup_{i \in S} P_{i}$.

Next, for a subset $P \subseteq[m]$, we let $T_{P}$ be the truth table of length $m$ that "sees" $T$ on the elements of $P$ and zeroes everywhere else, that is the $i$ th bit of $T_{P}$ is

$$
\begin{cases}\text { the } i \text { th bit of } T & , \text { if } i \in P \\ 0 & , \text { otherwise }\end{cases}
$$

Finally, we define the truth table $T_{S_{i}}$ induced by $S_{i}$ to be the truth table $T_{S_{i}^{\mathcal{P}}}$ given by the above notation (we are dropping the $m$ superscript for concision).
$\mathrm{CC}^{T_{S_{1}}, \ldots, T_{S_{t}}}(T)$ is at most $2 \ell$. Suppose, without loss of generality, that $S_{1} \cup \cdots \cup S_{\ell}$ is an optimal cover of $[n]$. Then, by construction, the function computed by $T_{S_{1}} \vee \cdots \vee T_{S_{\ell}}$ is $T$. This is an oracle circuit of size $2 \ell-1$, so $\mathrm{CC}^{T_{S_{1}}, \ldots, T_{S_{t}}}(T) \leq 2 \ell$.

If $T$ is $(r n)$-irritable, then $\mathrm{CC}^{T_{S_{1}}, \ldots, T_{S_{t}}}(T)>\ell / 2$. Recall the notation defined previously that, for a set $P \subseteq[m], T_{P}$ denotes the string of length $m$ that equals $T$ on the bits in $P$ and is zero everywhere else. Also recall that we fixed a partition $P_{1}, \ldots, P_{n}$ of $[m]$. We say that $T$ is $(s)$-irritable if for all $i \in[n]$ we have that

$$
\mathrm{CC}^{T_{P_{1}}, \ldots, T_{P_{i-1}}, T_{P_{i+1}}, \ldots, T_{P_{n}}}(T)>s
$$

Informally, $T$ being ( $r n$ )-irritable means that if you take away access to any particular $T_{P_{i}}$ oracle, then computing $T$ requires an oracle circuit of size greater than $r n$, which is a $(r / 2)$-factor jump over the trivial $2 n$-sized oracle circuit given by $T_{P_{1}} \vee \cdots \vee T_{P_{n}}$ if one had full oracle access.

Now assume $T$ is $(r n)$-irritable. We need to show that $\mathrm{CC}^{T_{S_{1}}, \ldots, T_{S_{t}}}(T)>\ell / 2$. For contradiction, suppose that $C$ is an oracle circuit computing $T$ with at most $\ell / 2$ gates. Then $C$ uses at most $q \leq \ell / 2$ unique oracle gates. Without loss of generality, assume $C$ uses as oracles only $T_{S_{1}}, \ldots, T_{S_{q}}$. Next, note that for any $i \in[q], T_{S_{i}}$ can, by construction, be computed by the oracle circuit $\bigvee_{j \in S_{i}} T_{P_{j}}$. Moreover, this is an oracle circuit for $T_{S_{i}}$ of size at most $2 r$ since $\left|S_{i}\right| \leq r$. Thus, replacing each $T_{S_{i}}$ oracle gate in $C$ with the oracle circuit $\bigvee_{j \in S_{i}} T_{P_{j}}$, we can transform $C$ into an oracle circuit $D$ of size at most $r \cdot|C| \leq r \ell$ such that $D$ computes $T$ when given access to the oracles in the set $O=\left\{T_{P_{j}}: j \in S_{1} \cup \cdots \cup S_{q}\right\}$. However, since $q \leq \ell / 2$ is less than the optimal cover size, $\left|S_{1} \cup \cdots \cup S_{q}\right|<n$ and so $|O|<n$, so $O$ is missing $T_{P_{i^{\star}}}$ for some $i^{\star} \in[n]$. But then $D$ is an oracle circuit of size at most $r \ell \leq r n$ that computes $T$ when given access to $T_{P_{1}}, \ldots, T_{P_{n}}$ without using $T_{P_{i}^{\star}}$ as an oracle gate, which contradicts that $T$ is $(r n)$-irritable.

RP, ZPP and $\mathrm{AC}^{0}$ reductions. At this point, we have shown that one can compute whether $S_{1}, \ldots, S_{t}$ admits a $c$-cover (up to a 4 -approximation) by outputting $\operatorname{MOCSP}\left(T, 2 c ; T_{S_{1}}, \ldots, T_{S_{t}}\right)$ for some $T$ that is $(r n)$-irritable.

We observe by a counting argument that a truth table $T$ of length $m \geq n^{3}$ picked uniformly at random is $(r n)$-irritable with high probability. Thus, picking a random truth table $T$ of
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length $\Theta\left(n^{3}\right)$ and outputting $\operatorname{MOCSP}\left(T, 2 c ; T_{S_{1}}, \ldots, T_{S_{t}}\right)$ gives an RP many-one reduction from 4-SetCover ${ }_{r}$ to MOCSP (note that we get one-sided error because irritability was only required for the $\ell / 2$ lower bound and not required for the $2 \ell$ upper bound). Additionally, since we can check if a random $T$ is $(r n)$-irritable using an oracle to MOCSP, we observe that 4 -SetCover ${ }_{r} \leq_{t t}^{\text {ZPP }}$ MOCSP. Finally, we observe that there is an $\mathrm{AC}^{0}$ circuit $C$ such that $C\left(T, c, S_{1}, \ldots, S_{t}\right)=\left(T, 2 c ; T_{S_{1}}, \ldots, T_{S_{t}}\right)$. Therefore, by non-uniformly hardcoding an (rn)-irritable truth table $T$ into $C$, we get that 4 -SetCover $r_{r}$ reduces to MOCSP under (non-uniform) $A C^{0}$ many-one reductions.

### 1.5 Paper Organization

In Section 2 we fix notation and review precise definitions. In Section 3 we prove that MAJORITY reduces to $\left(\mathrm{AC}_{d}^{0}\right)$-MCSP, and in Section 4 we prove our MOCSP results.

## 2 Preliminaries

For an integer $n$, we let $[n]$ denote the set $\{1, \ldots, n\}$. For a binary string $x \in\{0,1\}^{*}$, the weight of $x$, denoted $w t(x)$, is the number ones in $x$. We identify a Boolean function $f:\{0,1\}^{n} \rightarrow\{0,1\}$ with its truth table $T \in\{0,1\}^{2^{n}}$ and often use them interchangeably.

We let $\log$ denote the base-2 logarithm and ln represent the base-e logarithm. For functions $f$ and $g$, we say $f=\tilde{O}(g)$ if there exists a $c$ such that $f(x) \leq \log ^{c}(g(x)) g(x)$ for all sufficiently large $x$. We say that $f=\tilde{\Omega}(g)$ if $g=\tilde{O}(f)$.

We say a function $f:\{0,1\}^{n} \rightarrow \mathbb{R}$ is $c$-Lipchitz if for all $x, y \in\{0,1\}^{n}$ that differ in at most one bit, $|f(x)-f(y)| \leq c$.

## Complexity classes and reductions

We assume the reader is familiar with the standard complexity classes such as $A^{0}, P, Z P P, R P, N P, E$ and the notion of Turing machines. For background on these, we refer to Arora and Barak's excellent textbook [7]. For us, $\mathrm{AC}^{0}$ always refers to non-uniform $\mathrm{AC}^{0}$.

We review the types of reductions we use in case the reader is not familiar with randomized reductions, truth table reductions, or our notation.

Many-one reductions. We will make use of the follow notions of many-one reduction.

- $L \leq_{m}^{\mathrm{AC}^{0}} L^{\prime}$ if there is a non-uniform (polynomial-sized) $\mathrm{AC}^{0}$ circuit $C$ such that $x \in$ $L \Longleftrightarrow C(x) \in L^{\prime}$.
- $L \leq_{m}^{\mathrm{P}} L^{\prime}$ if there is a polynomial-time Turing machine $M$ such that $x \in L \Longleftrightarrow M(x) \in$ $L^{\prime}$ 。
- $L \leq_{m}^{\mathrm{RP}} L^{\prime}$ if there is a polynomial-time probabilistic Turing machine $M$ taking in a
"random" auxiliary input $r$ such that

$$
x \in L \Longrightarrow \forall r M(x, r) \in L^{\prime}, \text { and }
$$

$$
x \notin L \Longrightarrow \operatorname{Pr}_{r}\left[M(x, r) \in L^{\prime}\right] \geq 2 / 3
$$

and $|r|$ is polynomial in the length of $x$.

Truth table reductions. We will also make use of the following notions of truth table reductions.

- We say an oracle circuit $C$ is a truth table oracle circuit if there is no directed path between oracle gates in $C$.
- $L \leq_{t t}^{\mathrm{AC}^{0}} L^{\prime}$ if there is a non-uniform (polynomial-sized) $\mathrm{AC}^{0}$ truth table oracle circuit $C$ such that $C$ computes $L$ when given oracle access to $L^{\prime}$.
- $L \leq_{t t}^{\text {ZPP }} L^{\prime}$ if $L$ can be computed with zero-error by a polynomial-time probabilistic oracle Turing machine $M$ with oracle access to $L^{\prime}$ with the caveat that all of $M$ 's oracle queries must be answered simultaneously (i.e. so no oracle query can depend on another oracle query). On any single input, $M$ is allowed to output "don't know" with probability at most $1 / 2$.


## $\mathrm{AC}_{d}^{0}$ formulas, $\left(\mathrm{AC}_{d}^{0}\right)-\mathrm{MCSP}$, and $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}$

For an integer $d \geq 2$, we let $\mathrm{AC}_{d}^{0}$ denote the class of depth- $d$ formulas that use AND and OR gates with unbounded fan-in and fan-out 1 and that takes as "input leaves" the bits of a binary string and the negation of each of those bits.

For an $\mathrm{AC}_{d}^{0}$ formula $\phi$, we define the size of $\phi$, denoted $|\phi|$, to be the total number of input leaves $\phi$ uses. For a Boolean function $f$, we let $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}(f)$ be the size of the smallest $\mathrm{AC}_{d}^{0}$ formula computing $f$.

- Definition 2.1 (Minimum Circuit Size Problem for constant depth formulas). ( $\left.\mathrm{AC}_{d}^{0}\right)$-MCSP, is the language given by
$\left\{(T, s) \in\{0,1\}^{\star} \times \mathbb{N}: T\right.$ is the truth table of a Boolean function, and $\left.\mathrm{CC}_{\mathrm{AC}_{d}^{0}}(T) \leq s\right\}$.
We will also make use of the classes of formulas $\mathrm{OR} \circ \mathrm{AC}_{d-1}^{0}$ and $\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}$, defined as the subclasses of $\mathrm{AC}_{d}^{0}$ formulas with a top OR gate and a top AND gate respectively. For $\mathcal{C} \in\left\{\mathrm{OR} \circ \mathrm{AC}_{d-1}^{0}, \mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}\right\}$, we define We define $\mathrm{CC}_{\mathcal{C}}$ and $(\mathcal{C})$-MCSP analogous to $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}$ and $\left(\mathrm{AC}_{d}^{0}\right)$-MCSP.

We also require the following elementary lemmas regarding $\mathrm{AC}_{d}^{0}$ formulas.

- Lemma 2.2. Let $f$ be a Boolean function. Then $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}(f)=\mathrm{CC}_{\mathrm{AC}_{d}^{0}}(\neg f)$.

Proof. One can use DeMorgan's laws to turn any $\mathrm{AC}_{d}^{0}$ formula for $f$ of size $s$ into an $\mathrm{AC}_{d}^{0}$ formula for $\neg f$ of size $s$.

We note that our specific notion of $\mathrm{AC}_{d}^{0}$ formula size is crucial for the next lemma.

- Lemma 2.3 (Direct product theorem for formulas). Let $f:\{0,1\}^{n} \rightarrow\{0,1\}$ and $g$ : $\{0,1\}^{m} \rightarrow\{0,1\}$ be Boolean functions that are both not the constant zero function. Define $h:\{0,1\}^{n} \times\{0,1\}^{m} \rightarrow\{0,1\}$ by $h(x, y)=f(x) \wedge g(y)$. Then
$\mathrm{CC}_{{\mathrm{AND} 口 \mathrm{AC}_{d-1}^{0}}^{0}}(h)=\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}(f)+\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}(g)$, and
$\mathrm{CC}_{\mathrm{OR}_{\circ} \mathrm{AC}_{d-1}^{0}}(h) \geq \mathrm{CC}_{\mathrm{OR}_{\mathrm{AC}}^{d-1}}^{0}(f)+\mathrm{CC}_{{\mathrm{OR} \circ \mathrm{AC}_{d-1}^{0}}^{0}}(g)$.
Proof. It is easy to see that

On the other hand, since $f$ is not the constant 0 function, it has a 1 -valued input $x_{0}$. Then, $h\left(x_{0}, y\right)$ computes $g(y)$. Thus, if $\phi$ is an $\mathrm{OR} \circ \mathrm{AC}_{d-1}^{0}$ formula for $h$, then $\phi$ has at least
$\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}}^{d-1} 00$ ( $(g) y$-input leaves. A similar argument shows that $\phi$ has at least $\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}(f)$ $x$-input leaves. Hence

$$
\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}(h) \geq \mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}(f)+\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}(g)
$$

A similar argument shows that

$$
\mathrm{CC}_{\mathrm{OR}^{2} \mathrm{AC}_{d-1}^{0}}(h) \geq \mathrm{CC}_{{\mathrm{OR} \circ \mathrm{AC}_{d-1}^{0}}}(f)+\mathrm{CC}_{{\mathrm{OR} \circ \mathrm{AC}_{d-1}^{0}}(g) .} .
$$

## Oracle Circuits and Oracle MCSP: MOCSP

An oracle circuit $C$ is made up of NOT gates, fan-in two AND and OR gates, and oracle gates $g_{1}, \ldots, g_{t}$ with fan-in $i_{1}, \ldots, i_{t}$ respectively for some integers $t, i_{1}, \ldots, i_{t} \geq 1$. When given functions $f_{1}:\{0,1\}^{i_{1}} \rightarrow\{0,1\}, \ldots, f_{t}:\{0,1\}^{i_{t}} \rightarrow\{0,1\}$, we let $C^{f_{1}, \ldots, f_{t}}(x)$ be the value obtained when evaluating $C$ on input $x$ by using the $f_{1}, \ldots, f_{t}$ functions as the outputs of the $g_{1}, \ldots, g_{t}$ gates respectively.

We define the size of an oracle circuit $C$, denoted $|C|$, to be the sum of the number of OR gates, the number of AND gates, and the number of oracle gates in $C$.

For Boolean functions $f, f_{1}, \ldots, f_{t}$, we let $\mathrm{CC}^{f_{1}, \ldots, f_{t}}(f)$ be the size of the smallest oracle circuit that computes $f$ when given access to $f_{1}, \ldots, f_{t}$. Analogous to MCSP, we define the following.

- Definition 2.4 (The Minimum Oracle Circuit Size Problem). The Minimum Oracle Circuit Size Problem, denoted MOCSP, takes as as input a truth table T, a threshold $s \in \mathbb{N}$, and oracle truth tables $T_{1}, \ldots, T_{t}$ and outputs whether $\mathrm{CC}^{T_{1}, \ldots, T_{t}}(T) \leq s$. The output of MOCSP on such an input is written as $\operatorname{MOCSP}\left(T, s ; T_{1}, \ldots, T_{t}\right)$.


## $r$-Bounded Set Cover

We will make use of the following well known NP-complete problem.

- Definition 2.5 ( $r$-Bounded Set Cover). $r$-Bounded Set Cover, denoted SetCover ${ }_{r}$, is the problem that takes as input a tuple $\left(n, c, S_{1}, \ldots, S_{t}\right)$, where $n \in \mathbb{N}$ is a universe size, $c \in \mathbb{N}$ is a proposed cover size $1 \leq c \leq n$, and $S_{1}, \ldots, S_{t} \subseteq[n]$ are sets of cardinality at most $r$ whose union is [ $n$ ], and outputs whether $c \geq \ell$ where $\ell$ is the optimal cover size given by

$$
\ell=\min \left\{|I|: I \subseteq[t] \text { and } \cup_{i \in I} S_{i}=[n]\right\} .
$$

We will also make use of the following restricted version of set cover. Let SetCover ${ }_{r, n, t}$ denote $r$-bounded set cover on $t$ subsets of $[n]$. We encode inputs to SetCover ${ }_{r, n, t}$ as the tuple $\left(c, S_{1}, \ldots, S_{t}\right)$ (with $n$ implicit) where $c$ is represented in binary and the set $S_{i}$, for each $i \in[t]$, is represented by a bit string of length $r\lceil\log (n+1)]$ that is a concatenated list of the elements of $S_{i}$ in binary, padded with zeroes if $\left|S_{i}\right|<r$ (note that zero is not an element of [ $n$ ], so padding with zeroes is not ambiguous).

We will use that SetCover $r_{r}$ is NP-hard even to approximate to a roughly $\ln r$ factor.

- Theorem 2.6 (Feige [10] and Trevisan [31]). Let $r$ be a sufficiently large constant, and let $L$ be a language. If for every instance $x=\left(n, c, S_{1}, \ldots, S_{t}\right)$ of SetCover $_{r}$, we have that both that
- $c \geq \ell$ implies $x \in L$, and
- $c \leq \ell /(\ln r-O(\ln \ln r))$ implies $x \notin L$,
where $\ell$ is the optimal cover size, then $L$ is NP-hard under polynomial-time many-one reductions.


## 3 MAJORITY $\leq \leq_{t t}^{\mathrm{AC}^{0}}\left(\mathrm{AC}_{d}^{0}\right)$-MCSP

Let $d \geq 2$. Our goal in this section is to prove the following theorem.

- Theorem 3.1. MAJORITY $\leq_{t t}^{\mathrm{AC}^{0}}\left(\mathrm{AC}_{d}^{0}\right)$-MCSP.

We will do this by showing that for all sufficiently large $n \in \mathbb{N}$, there exists an $\mathrm{AC}^{0}$ truth table oracle circuit that computes MAJORITY on $n$-bits when given access to $\left(\mathrm{AC}_{d}^{0}\right)$-MCSP.

Fix some $n$, and let $q$ be the least power of two such that $n \leq \sqrt{q} / 2$. We will split our analysis into cases depending on whether there exists an $m \in\left\{q^{10}, q^{50}\right\}$ such that $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}$ is $\left(m^{.25}\right)$-Lipchitz on inputs of length $m$.

### 3.1 Case 1: Lipchitzness Holds

If Lipchitzness holds, then the desired $\left(\mathrm{AC}_{d}^{0}\right)$-MCSP oracle circuit $C$ exists for computing MAJORITY on $n$-inputs by the work of Golovnev et al. [11]. At a high-level, $C$ works by using the input string to sample a random variable whose circuit complexity spikes (in expectation) depending on the weight of the input and using Lipchitzness to show that this spike happens with such high probability that it can be derandomized using non-uniformity.

For completeness, we give a self-contained proof of this case in Appendix A.

### 3.2 Case 2: Lipchitzness fails

Assume that for all $m \in\left\{q^{10}, q^{50}\right\}, \mathrm{CC}_{\mathrm{AC}_{d}^{0}}$ is not $\left(m^{.25}\right)$-Lipchitz on inputs of length $m$. Thus, for all $m \in\left\{q^{10}, q^{50}\right\}$ there exist functions $f^{m}, h^{m}:\{0,1\}^{\log m} \rightarrow\{0,1\}$ that differ only on a single input $z^{m} \in\{0,1\}^{\log m}$ such that $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(h^{m}\right)-\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(f^{m}\right)>m^{.25}$.

We assume, without loss of generality, that for all $m \in\left\{q^{10}, q^{50}\right\}, f^{m}\left(z^{m}\right)=0$ and $h^{m}\left(z^{m}\right)=1$. (If this is not the case, then replace $f^{m}$ and $h^{m}$ by $\neg f^{m}$ and $\neg h^{m}$ respectively and apply Lemma 2.2.)

First, we show that the failure of Lipchitzness implies the existence of functions that are much easier to compute by formulas with an AND gate on top. For $m \in\left\{q^{10}, q^{50}\right\}$ let $\mathbb{1}_{z^{m}}:\{0,1\}^{\log m} \rightarrow\{0,1\}$ denote the indicator function that accepts just the string $z^{m}$.

- Proposition 3.2. Let $m \in\left\{q^{10}, q^{50}\right\}$. For sufficiently large $n, \mathrm{CC}_{\mathrm{OR} \circ \mathrm{AC}_{d-1}^{0}}\left(f^{m}\right) \geq$ $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(f^{m}\right)+m^{24}$, and so any optimal $\mathrm{AC}_{d}^{0}$ formula for $f^{m}$ has an AND gate on top.
Proof. Suppose $\phi$ is an $\mathrm{OR} \circ \mathrm{AC}_{d-1}^{0}$ formula computing $f^{m}$, that is, $f^{m}$ is computed by $\phi=\phi_{1} \vee \cdots \vee \phi_{t}$ for some $\mathrm{AC}_{d-1}^{0}$ formulas $\phi_{1}, \ldots, \phi_{t}$. Then
$\mathbb{1}_{z^{m}} \vee \phi_{1} \vee \cdots \vee \phi_{t}$
computes $h^{m}$. Since $\mathbb{1}_{z^{m}}$ can be computed by a single AND gate of formula size $\log m$, this shows that $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(h^{m}\right) \leq|\phi|+\log m$. Combining this with the fact that $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(h^{m}\right)-$ $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(f^{m}\right) \geq m^{.25}$ gives the desired result.

At this point, we will need to refer to both $q^{10}$ and $q^{50}$ individually, so for convenience let $u=q^{10}$ and $v=q^{50}$.

Let $\phi^{u}$ be an optimal $\mathrm{AC}_{d}^{0}$ formula for $f^{u}$. By Proposition 3.2, for sufficiently large $n$, we know that $\phi^{u}=\phi_{1}^{u} \wedge \cdots \wedge \phi_{t}^{u}$ for some $\mathrm{AC}_{d-1}^{0}$ formulas $\phi_{1}^{u}, \ldots, \phi_{t}^{u}$. Moreover, we can assume, without loss of generality, that the top gate of $\phi_{i}^{u}$ is OR for all $i \in[t]$. (If some $\phi_{i}^{u}$ has an AND gate on top, then this AND can be carried out by the AND gate on top of $\phi^{u}$ without increasing the size of the formula.)

Our next Proposition shows that $\phi^{u}$ has high top fan-in.
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- Proposition 3.3. For sufficiently large n,
$t \geq u^{24}$.
Proof. We divide into cases depending on $d$.

Case 1: $d \geq 3$. Realize that

$$
\left(\phi_{1}^{u} \vee \mathbb{1}_{z^{u}}\right) \wedge \cdots \wedge\left(\phi_{t}^{u} \vee \mathbb{1}_{z^{u}}\right)
$$

computes $h^{u}$. Since $\mathbb{1}_{z^{u}}$ can be computed by a single AND gate of formula size $\log u$ and the top gate of each $\phi_{i}^{u}$ is an OR gate and $d \geq 3$, this yields a depth- $d$ formula for $h^{u}$ of size $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(f^{u}\right)+t \log u$. Since $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(h^{u}\right)-\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(f^{u}\right) \geq u^{.25}$, the desired bound on $t$ follows.

Case 2: $d=2$. Let $\mathbb{1}_{z^{u}, j}:\{0,1\}^{\log u} \rightarrow\{0,1\}$ be the function that accepts a string $x$ if and only if the $j$ th bit of $x$ equals the $j$ th bit of $z^{u}$. Observe that, since $\bigwedge_{i \in[t]} \phi_{i}^{u}$ computes $f^{u}$, we have that

$$
\bigwedge_{i \in[\mid] \in[\log z]}\left(\phi_{i}^{u} \vee \mathbb{1}_{z^{u}, j}\right)
$$

computes $h^{u}$. Since $\mathbb{1}_{z^{u}, j}$ is computed by a single input leaf and $\phi_{i}^{u}$ has an OR gate on top, this yields a depth- 2 formula for $h^{u}$ of size $\left(\left|\phi^{u}\right|+1\right) \log u$. Since $\phi^{u}$ is an optimal CNF, each clause $\phi_{i}^{u}$ of $\phi^{u}$ is the OR of at most $\log u$ input leaves. In other words, $\left|\phi_{i}^{u}\right| \leq \log u$. Therefore, we have that

$$
\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(h^{u}\right) \leq\left(\left|\phi^{u}\right|+1\right) \log u \leq\left(\sum_{i \in[t]}\left|\phi_{i}^{u}\right|+1\right) \log u \leq(t \log u+1) \log u=t \log ^{2} u+\log u
$$

On the other hand, we know by assumption that

$$
\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(h^{u}\right)>\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(f^{u}\right)+u^{.25} \geq u^{.25}
$$

Combining these two inequalities gives us the desired bound on $t$.

Let $p$ be smallest prime greater than $n$. (Note that $p \leq 2 n$ by Betrand's postulate, also known as Chebyshev's theorem. See [12] for a proof.) We say that an integer $j$ is $(k, r)$-good for integers $k \geq 0$ and $1 \leq r \leq p-1$ if $p^{k}$ divides $j$ and $j / p^{k} \equiv r \bmod p$. In other words, an integer $j$ is $(k, r)$-good for $k \geq 0$ and $r \in[p-1]$ if the $k$ th largest entry of the base- $p$ representation of the integer $j$ equals $r$ and all previous entries equal zero. From this "base- $p$ " perspective, it is clear that all positive integers $j$ are $(k, r)$-good for some $k \geq 0$ and $r \in[p-1]$.

We show that, for some $k$ and $r$, a large subset of the $\left|\phi_{i}^{u}\right|$ are $(k, r)$-good.

- Proposition 3.4. For all sufficiently large $n$, there exist integers $k \geq 0$ and $1 \leq r \leq p-1$ and a set $S \subseteq[t]$ of cardinality $n$ such that, for all $i \in S$, the integer $\left|\phi_{i}^{u}\right|$ is ( $k, r$ )-good.

Proof. We do this by an averaging argument. First, we show that each $\left|\phi_{i}^{u}\right|$ is $(k, r)$-good for a $k$ not too large.
$\triangleright$ Claim 3.5. For all $i \in[t],\left|\phi_{i}^{u}\right|$ is $(k, r)$-good for some $0 \leq k \leq \log (u \log u)+1$ and some $r \in[p-1]$.

Proof. Fix some $i \in[t] .\left|\phi_{i}^{u}\right|$ is a positive integer, so $\left|\phi_{i}^{u}\right|$ is $(k, r)$-good for some $k \geq 0$ and some $r \in[p-1]$. We still need to upper bound this $k$. Note that the size of $\left|\phi_{i}^{u}\right|$ is at most by $u \log u$ since $\phi^{u}$ is optimal for $f^{u}$ and $f^{u}$ can be computed by a DNF of size $u \log u$. Thus, for $p^{k}$ to divide $\left|\phi_{i}^{u}\right|$, we must have that $k \leq \log _{p}(u \log u)+1 \leq \log (u \log u)+1$. $\quad \triangleleft$

Since for all $i \in[t]$ we have shown that $\left|\phi_{i}^{u}\right|$ is $(k, r)$ good for some $0 \leq k \leq \log (u \log u)+1$ and some $r \in[p-1]$, a standard averaging argument implies that there exists a set $S \subseteq[t]$ of cardinality at least

$$
\frac{t}{(\log (u \log u)+1)(p-1)}
$$

such that for all $i \in S,\left|\phi_{i}^{u}\right|$ is $(k, r)$-good for some fixed $k \geq 0$ and $1 \leq r \leq p-1$. For sufficiently large $n$, we have that

$$
\frac{t}{(\log (u \log u)+1)(p-1)} \geq \frac{u^{.24}}{4 n \log u} \geq n
$$

using that $u=q^{10} \geq n^{10}$. We then can truncate $S$ so that it has only $n$ elements as desired.

Assume that $n$ is large enough that all the sufficiently large hypotheses in Propositions $3.2,3.3$, and 3.4 apply. For convenience, relabel $\phi_{1}, \ldots, \phi_{t}$ so that the set $S$ guaranteed by Proposition 3.4 is just $S=[n]$. Fix $k \geq 0$ and $r \in[p-1]$ to be the values such that for all $i \in S=[n],\left|\phi_{i}^{u}\right|$ is $(k, r)$-good.

Introducing notation, for a set $A \subseteq[n]$, let $f_{A}^{u}$ be the function computed by $\bigwedge_{i \in A} \phi_{i}^{u}$.

- Lemma 3.6. Let $A \subseteq[n]$. Then $\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f_{A}^{u}\right)=\sum_{i \in A}\left|\phi_{i}^{u}\right|$.

Proof. By construction, we have that $\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f_{A}^{u}\right) \leq \sum_{i \in A}\left|\phi_{i}\right|$. Suppose for contradiction that $\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f_{A}^{u}\right)<\sum_{i \in A}\left|\phi_{i}^{u}\right|$.

Let $\theta_{1} \wedge \cdots \wedge \theta_{\ell}$ be a minimum-sized ( $\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}$ ) formula for $f_{A}^{u}$. By assumption, we have that $\sum_{j=1}^{\ell}\left|\theta_{j}\right|<\sum_{i \in A}\left|\phi_{i}^{u}\right|$. We can thus replace the $\bigwedge_{i \in A} \phi_{i}^{u}$ in the optimal formula for $f^{u}$ with $\theta_{1} \wedge \cdots \wedge \theta_{\ell}$ and get a smaller formula. In more detail, we have that

$$
f^{u}=f_{A}^{u} \wedge\left(\bigwedge_{i \in[t] \backslash A} \phi_{i}^{u}\right)=\left(\theta_{1} \wedge \cdots \wedge \theta_{\ell}\right) \wedge\left(\bigwedge_{i \in[t] \backslash A} \phi_{i}^{u}\right)
$$

which is a formula of size

$$
\sum_{j=1}^{\ell}\left|\theta_{j}\right|+\sum_{i \in[t] \backslash A}\left|\phi_{i}^{u}\right|<\sum_{i \in A}\left|\phi_{i}^{u}\right|+\sum_{i \in[t] \backslash A}\left|\phi_{i}^{u}\right|=\sum_{i=1}^{t}\left|\phi_{i}^{u}\right|=\left|\phi^{u}\right|
$$

which contradicts the optimality of $\phi^{u}$ for $f^{u}$.
For a string $x \in\{0,1\}^{n}$, let $f_{x}^{u}$ be shorthand for $f_{A_{x}}^{u}$ where $A_{x} \subseteq[n]$ is the set of indices where $x$ is one.

- Proposition 3.7. Let $x \in\{0,1\}^{n}$. Then $x$ has weight $w$ if and only if the integer $\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}}^{d-1} 00\left(f_{x}^{u}\right)$ is $(k, r w)$-good.

Proof. By Lemma 3.6 and the fact that $\left|\phi_{i}^{u}\right|$ is $(k, r)$-good for all $i \in[n]$, we have that

$$
\frac{\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f_{x}^{u}\right)}{p^{k}}=\frac{\sum_{i \in A_{x}}\left|\phi_{i}^{u}\right|}{p^{k}} \equiv w \cdot r \bmod p
$$

where $A_{x} \subseteq[n]$ are bits of $x$ that are ones. The "only if" part of the statement is guaranteed by the fact that $1 \leq r \leq p-1$ has a multiplicative inverse modulo $p$ since $p$ is prime.
$\mathrm{AC}^{0}[p]$ Lower Bounds and NP-Hardness for Variants of MCSP

- Theorem 3.8. Assume $n$ is sufficiently large. Then there is a depth- $8 \mathrm{AC}^{0}$ truth table oracle circuit $C$ with $O\left(n^{250}\right)$ wires such that $C^{\left(\mathrm{AC}_{d}^{0}\right)-\mathrm{MCSP}}$ computes MAJORITY on $n$-bits.

Proof. It suffices to show that for every $w \in[n]$, there exists a depth-7 $\mathrm{AC}^{0}$ oracle circuit $C_{w}$ with $O\left(n^{249}\right)$ wires such that $C_{w}^{\left(\mathrm{AC}_{d}^{0}\right)-\operatorname{MCSP}}(x)=1 \Longleftrightarrow \mathrm{wt}(x)=w$. Then MAJORITY $(x)=$ $\bigvee_{w \geq n / 2} C_{w}(x)$.
$\overline{\text { Fix some }} w \in[n]$. The circuit $C_{w}$ works as follows. On input $x \in\{0,1\}^{n}$, first check if $x$ is the all zeroes string. If so, then reject. Otherwise, compute the truth table of the direct product function $g_{x}:\{0,1\}^{\log u} \times\{0,1\}^{\log v} \rightarrow\{0,1\}$ given by $g_{x}(y, z)=f_{x}^{u}(y) \wedge f^{v}(z)$. Compute $s=\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(g_{x}\right)$ in binary using oracle access to $\left(\mathrm{AC}_{d}^{0}\right)$-MCSP. Finally accept if the integer $s$ has the property that $s-\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(f^{v}\right)$ is $(k, r w)$-good. Reject otherwise.

We now verify this yields a (non-uniform) $A C^{0}$ truth table oracle circuit. We can check if $x$ is the all zeroes string with a single OR gate. This requires one level of depth and $O(n)$ wires. Next, realize the $j$ th bit in the truth table of $g_{x}$ is either zero for all $x$ or equal to

$$
f_{x}^{u}(j)=\bigvee_{i \in[n]: \phi_{i}^{u}(j)=1} x_{i}
$$

where $x_{i}$ denotes the $i$ th bit of $x$. Thus, using non-uniformity, we can compute the truth table of $g_{x}$ with $O(n u v)=O\left(n q^{60}\right)=O\left(n^{121}\right)$ wires and depth-one. Next, we can compute $s=$ $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(g_{x}\right)$ in binary with $O(u v \log (u v))$ calls to $\left(\mathrm{AC}_{d}^{0}\right)$ - MCSP using the fact that $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(g_{x}\right) \leq$ $u v \log (u v)$ by the DNF bound and the fact that

$$
\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(g_{x}\right)=s \Longleftrightarrow\left(\mathrm{AC}_{d}^{0}\right)-\operatorname{MCSP}\left(g_{x}, s\right)=1 \text { and }\left(\mathrm{AC}_{d}^{0}\right)-\operatorname{MCSP}\left(g_{x}, s-1\right)=0
$$

This takes at most $\tilde{O}\left((u v)^{2}\right)=O\left(n^{241}\right)$ wires, an additional three layers of depth, and $2 u v \log (u v)$ oracle calls that all do not depend on each other. Finally, the DNF upper bound guarantees that $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(g_{x}\right) \leq u v \log (u v) \leq n^{61}$, so the length of the integer $s=\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(g_{x}\right)$ in binary is at most $61 \log n$. Therefore we can check if $s$ has the property that $s-\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(f^{v}\right)$ is $(k, r w)$-good using a DNF with at most $n^{62}$ wires and at most an additional two layers of depth. Combining all this yields a $\mathrm{AC}^{0}$ circuit of depth- 7 with at most $O\left(n^{241}\right)$ wires and no directed path between oracle gates.

Next, we argue for correctness. Clearly, $C_{w}$ rejects the all zero string, so assume $x \neq 0^{n}$. By Proposition 3.7, it suffices to show that, for $s=\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(g_{x}\right)$,

$$
s-\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(f^{v}\right)=\mathrm{CC}_{\mathrm{AND} \mathrm{\circ AC}_{d-1}^{0}}\left(f_{x}^{u}\right)
$$

We confirm that neither $f_{x}^{u}$ nor $f^{v}$ is the constant zero function, so that we can use the direct product theorems in Lemma 2.3.
$\triangleright$ Claim 3.9. Neither $f_{x}^{u}$ nor $f^{v}$ is the constant zero function.
Proof. If $f^{v}$ were the constant zero function, then $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(h^{v}\right) \leq \log v$ by DNF computation which contradicts that

$$
\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(h^{v}\right)-\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(f^{v}\right) \geq v^{\cdot 25} .
$$

Next, let $i \in[n]$ be a bit of $x$ that is not zero. (Recall, we assumed that $x \neq 0^{n}$.) Then $f_{x}^{u}$ has accepts every input that that $\phi_{i}^{u}$ accepts. For contradiction, suppose that $\phi_{i}^{u}$ had no ones. Then we can remove $\phi_{i}^{u}$ from the optimal formula $\phi^{u}=\phi_{1}^{u} \wedge \ldots \phi_{t}^{u}$ for $f^{u}$ and get a smaller formula for $f^{u}$ which contradicts the optimality of $\phi^{u}$.

Next we show that the optimal $\mathrm{AC}_{d}^{0}$ formula for $g_{x}$ has an AND gate on top.

Proof. Let $\Delta=\mathrm{CC}_{\mathrm{OR} \mathrm{\circ AC}_{d-1}^{0}}\left(g_{x}\right)-\mathrm{CC}_{\mathrm{AND} \mathrm{\circ AC}_{d-1}^{0}}\left(g_{x}\right)$. We need to show $\Delta>0$.
$\Delta \geq \mathrm{CC}_{\mathrm{OR} \mathrm{\circ AC}_{d-1}^{0}}\left(f^{v}\right)-\mathrm{CC}_{{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f^{v}\right)+\mathrm{CC}_{\mathrm{OR} \mathrm{\circ AC}_{d-1}^{0}}\left(f_{x}^{u}\right) \quad \text { (by Lemma 2.3) }}$ $-\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f_{x}^{u}\right)$
$\geq(v)^{24}+\mathrm{CC}_{{\mathrm{OR} \circ \mathrm{AC}_{d-1}^{0}}\left(f_{x}^{u}\right)-\mathrm{CC}_{\mathrm{AND} \mathrm{\circ AC}_{d-1}^{0}}\left(f_{x}^{u}\right) \geq \quad \text { (by Proposition 3.2) }}$
$\geq(v)^{24}-u \log u \quad$ (by DNF bound on $f_{x}^{u}$ )
$\geq n^{50 \cdot 24}-n^{10} \log \left(n^{10}\right) \quad$ (by definition of $u$ and $v$ )
$>0 \quad$ (for sufficiently large $n$ )

Using the claim we have that

$$
\begin{aligned}
s-\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(f^{v}\right) & =\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(g_{x}\right)-\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(f^{v}\right) & & \text { (by definition) } \\
& =\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(g_{x}\right)-\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(f^{v}\right) & & \text { (by Claim 3.10) } \\
& =\mathrm{CC}_{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f_{x}^{u}\right)+\mathrm{CC}_{{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f^{v}\right)-\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(f^{v}\right)} & & \text { (by Lemma 2.3) } \\
& =\mathrm{CC}_{{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f_{x}^{u}\right)+\mathrm{CC}_{\left.{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f^{v}\right)-\mathrm{CC}_{{\mathrm{AND} \circ \mathrm{AC}_{d-1}^{0}}\left(f^{v}\right)}\right)} \text { (by Proposition 3.2) }} & =\mathrm{CC}_{\mathrm{AND} \mathrm{\circ AC}_{d-1}^{0}}\left(f_{x}^{u}\right) &
\end{aligned}
$$

as desired.

- Remark 3.11. We remark that the only time we use the failure of Lipchitzness in Case 2 is to show the existence of functions like $f^{u}$ with high top fan-in and functions like $f^{v}$ with a large difference between top AND gate and top OR gate complexity. Using known PARITY lower bounds and depth-hierarchy theorems for $\mathrm{AC}^{0}$ circuits, we can unconditionally prove the existence of $f^{u}$ and $f^{v}$ respectively but with slightly worse parameters that would yield a quasi-polynomial reduction (at least in the $d \geq 3$ case) rather than the polynomial reduction we present.


## 4 On the NP-hardness of MOCSP

First, we introduce some useful notation and definitions. For a truth table $T$ of length $m$ and a set $P \subseteq[m]$, let $T_{P}$ be the truth table of length $m$ where the $j$ th bit of $T_{P}$ equals

$$
\begin{cases}\text { the } j \text { th bit of } T & , \text { if } j \in P \\ 0 & , \text { otherwise }\end{cases}
$$

Next, we say a truth table $T$ of length $m$ is $(s)$-irritable on a partition $\mathcal{P}=\left(P_{1}, \ldots, P_{n}\right)$ of [ $m$ ] if for all $i \in[n]$

$$
\mathrm{CC}^{T_{P_{1}}}, \ldots, T_{P_{i-1}}, T_{P_{i+1}}, \ldots, T_{P_{n}}(T)>s
$$

Finally, for a partition $\mathcal{P}=\left(P_{1}, \ldots, P_{n}\right)$ of $[m]$ and any set $S \subseteq[n]$, we define the $\mathcal{P}$-lift of $S$, denoted $S^{\mathcal{P}}$, to be the subset of $[m]$ given by

$$
S^{P}=\bigcup_{i \in S} P_{i}
$$

$\mathrm{AC}^{0}[p]$ Lower Bounds and NP-Hardness for Variants of MCSP

Our first theorem shows that one can use an irritable truth table and MOCSP to approximate $r$-bounded set cover.

- Theorem 4.1. Let $S_{1}, \ldots, S_{t} \subseteq[n]$ be sets of cardinality at most $r$ that cover $[n]$. Let $T$ be a truth table of length $m$, and let $\mathcal{P}=\left(P_{1}, \ldots, P_{n}\right)$ be a partition of $[m]$. Then
- $\mathrm{CC}^{T_{S_{1}^{P}}, \ldots, T_{S_{t}^{\mathcal{P}}}}(T) \leq 2 \ell$, and
- $\mathrm{CC}^{T_{S_{1}^{P}}, \ldots, T_{S_{t}}}(T)>\ell / 2$ if $T$ is $(r n)$-irritable on $\mathcal{P}$
where $\ell$ is size of the optimal cover of $[n]$ by $S_{1}, \ldots, S_{t}$.
Proof. We split this proof into two claims to make clear that our two "without loss of generality" assumptions do not conflict with each other.
$\triangleright$ Claim 4.2. $\quad \mathrm{CC}^{T_{S_{1}^{\mathcal{P}}}, \ldots, T_{S_{t}^{\mathcal{P}}}}(T) \leq 2 \ell$
Proof. Without loss of generality, assume that the optimal cover size $\ell$ is witnessed by $S_{1} \cup$ $\cdots \cup S_{\ell}=[n]$. Then, by construction, the function computed by oracle circuit $T_{S_{1}^{\mathcal{P}}} \vee \cdots \vee T_{S_{\ell}^{\mathcal{P}}}$ of size $2 \ell-1$ is $T$. In more detail,

$$
\bigvee_{i \in[\ell]} T_{S_{i}^{P}}=\bigvee_{i \in[\ell]} \bigvee_{j \in S_{i}} T_{P_{j}}=\bigvee_{j \in S_{1} \cup \cdots \cup S_{\ell}} T_{P_{j}}=\bigvee_{j \in[n]} T_{P_{j}}=T
$$

Therefore $\mathrm{CC}^{T_{S_{1}^{\mathcal{P}}}, \ldots, T_{S_{t}^{\mathcal{P}}}}(T) \leq 2 \ell$.
$\triangleright$ Claim 4.3. If $T$ is $(r n)$-irritable on $\mathcal{P}$, then $\mathrm{CC}^{T_{S_{1}^{\mathcal{P}}}, \ldots, T_{S_{t}^{\mathcal{P}}}}>\ell / 2$.
Proof. For contradiction, suppose there is an oracle circuit $D$ with at most $\ell / 2$ gates such that $D^{T_{S_{1}^{\mathcal{P}}}, \ldots, T_{S_{t}^{P}}}$ computes $T$. Since $D$ has at most $q \leq \ell / 2$ unique oracle gates, assume, without loss of generality, that $D^{T_{S_{1}^{\mathcal{P}}}, \ldots, T_{S_{q}^{\mathcal{P}}}}$ computes $T$.

Recall, by the definition of $T_{S_{i}^{\mathcal{P}}}$, we have that $T_{S_{i}^{\mathcal{P}}}=\bigvee_{j \in S_{i}} T_{P_{j}}$. Note that $\bigvee_{j \in S_{i}} T_{P_{j}}$ is an oracle circuit of size at most $2 r$ since $\left|S_{i}\right| \leq r$. Thus, by replacing each $T_{S_{i}^{P}}$ oracle gate in $D$ with the oracle circuit $\bigvee_{j \in S_{i}} T_{P_{j}}$, we can transform $D$ into an oracle circuit $E$ of size at most $2 r|C| \leq r \ell$ that computes $T$ when given access to the oracles in the set $O=\left\{T_{P_{j}}: j \in S_{1} \cup \cdots \cup S_{q}\right\}$. However, since the optimal cover of $n$ is of size $\ell$ and $q \leq \ell / 2<\ell$, it follows that $\left|S_{1} \cup \cdots \cup S_{q}\right|<n$ and hence $|O|<n$. Thus, there is an element $i^{\star} \in[n]$ such that $T_{P_{i} \star} \notin O$. Therefore, the circuit $E$ witnesses that

$$
\mathrm{CC}^{T_{P_{1}}, \ldots, T_{P_{i^{\star}-1}}, T_{P_{i^{\star}+1}}, \ldots, T_{P_{n}}}(T) \leq r \ell \leq r n
$$

which contradicts that $T$ is $(r n)$-irritable on $\mathcal{P}$.

Moreover, given a truth table $T$ and the partition $\mathcal{P}$ on which $T$ is sufficiently irritable, we show it is easy to build a constant-depth circuit that approximates $r$-bounded set cover using MOCSP.

- Theorem 4.4. Let $r \in \mathbb{N}$. There exists an polynomial-time algorithm $A$ such that if $\mathcal{P}=\left(P_{1}, \ldots, P_{n}\right)$ is a partition of $[m]$, and $T$ is a truth table of length $m$, then

1. $A\left(0^{n}, 0^{t}, T, \mathcal{P}\right)$ outputs a depth $-2 \mathrm{AC}^{0}$ circuit $C_{n, t, T, \mathcal{P}}$ with $O$ (mnrt) wires,
2. MOCSP $\circ C_{n, t, T, \mathcal{P}}$ accepts all YES instances of SetCover $_{r, n, t}$, and
 $\mathcal{P}$.

Proof. First, we show that there is a polynomial-time algorithm $A$ that outputs a small depth-2 circuit computing a specific function. Then we show that this specific function is helpful in computing SetCover $r, n, t$.
$\triangleright$ Claim 4.5. There is a polynomial-time algorithm $A$ such that $A\left(0^{n}, 0^{t}, T, \mathcal{P}\right)$ outputs a depth-2 AC ${ }^{0}$ circuit $C_{n, t, T, \mathcal{P}}$ with $O(m n r t)$ wires satisfying

$$
C_{n, t, T, \mathcal{P}}\left(c, S_{1}, \ldots, S_{t}\right)=\left(T, 2 c ; T_{S_{1}^{\mathcal{P}}}, \ldots, T_{S_{t}^{\mathcal{P}}}\right)
$$

for any instance $\left(c, S_{1}, \ldots, S_{t}\right)$ of SetCover ${ }_{r, n, t}$.
Proof. On input $\left(0^{n}, 0^{t}, T, \mathcal{P}\right), A$ builds the circuit $C_{n, t, T, \mathcal{P}}$ as follows. First, $A$ will hardwire $C_{n, t, T, \mathcal{P}}$ to output $T$. This requires $O(m)$ wires and depth one. Next, $A$ adds circuitry to $C_{n, t, T, \mathcal{P}}$ that outputs $2 c$ by adding an extra zero to the binary expansion of $c$. This uses $O(\log n)$ wires and depth one.

Finally, $A$ adds circuitry to $C_{n, t, T, \mathcal{P}}$ that outputs $T_{S_{1}^{\mathcal{P}}}, \ldots, T_{S_{t}^{\mathcal{P}}}$ as follows. Observe that for any $i \in[t]$ and $j \in[m]$, the $j$ th bit of $T_{S_{i}^{\mathcal{P}}}$ is one if and only if $S_{i}$ contains the unique element $k_{j} \in[n]$ such that $j \in P_{k_{j}}$. Thus, since $A$ has access to $\mathcal{P}, A$ can calculate $k_{j}$ for all $j \in[m]$ and then add circuitry to $C_{n, t, T, \mathcal{P}}$ that calculates the $j$ th bit of $T_{S_{i}^{\mathcal{P}}}$ by ORing over all the elements of $S_{i}$ and using an AND to check if any one of those elements is $k_{j}$. This requires $O(m n r t)$ wires and depth-two.

Therefore, $C_{n, t, T, \mathcal{P}}$ is a depth-2 $\mathrm{AC}^{0}$ circuit with $O(m n r t)$ wires as desired. Moreover, it is clear from this description that $A$ runs in polynomial-time.

It remains to show that the algorithm $A$ given in Claim 4.5 satisfies (2) and (3). Let $\left(c, S_{1}, \ldots, S_{t}\right)$ be an instance of SetCover ${ }_{r, n, t}$. Let $\ell$ be the minimum size of any cover of $[n]$ by $S_{1}, \ldots, S_{t}$.

First, we show (2) holds. Suppose $c \geq \ell$. Then Theorem 4.1 implies that

$$
C C^{T_{S_{1}^{\mathcal{P}}}, \ldots, T_{S_{t}^{\mathcal{P}}}}(T) \leq 2 \ell \leq 2 c
$$

so
$\operatorname{MOCSP}\left(C_{n, t, T, \mathcal{P}}\left(c, S_{1}, \ldots, S_{t}\right)\right)=\operatorname{MOCSP}\left(T, 2 c ; T_{S_{1}^{\mathcal{P}}}, \ldots, T_{S_{t}^{\mathcal{P}}}\right)=\mathrm{YES}$
as desired.
Finally, we show (3) holds. Suppose $c<\ell / 4$ and $T$ is $(r n)$-irritable. Then Theorem 4.1 implies that

$$
C C^{T_{S_{1}^{\mathcal{P}}}, \ldots, T_{S_{t}^{\mathcal{P}}}}(T)>\ell / 2 \geq 2 c
$$

so

$$
\operatorname{MOCSP}\left(C_{n, t, T, \mathcal{P}}\left(c, S_{1}, \ldots, S_{t}\right)\right)=\operatorname{MOCSP}\left(T, 2 c ; T_{S_{1}^{\mathcal{P}}}, \ldots, T_{S_{t}^{\mathcal{P}}}\right)=\operatorname{NO}
$$

Hence (3) holds.
Of course to make use of Theorem 4.4, we need to actually find truth tables $T$ and partitions $\mathcal{P}$ on which $T$ is sufficiently irritable. Fortunately, such $T$ and $\mathcal{P}$ are abundant. We show that, with high probability, any choice of $\mathcal{P}$ and a random choice of a truth table $T$ suffices.
$\mathrm{AC}^{0}[p]$ Lower Bounds and NP-Hardness for Variants of MCSP

- Lemma 4.6. Let $n, r \in \mathbb{N}$. Let $m$ be the least power of two greater than $n^{3}$. Let $\mathcal{P}=$ $\left(P_{1}, \ldots, P_{n}\right)$ be any partition of $[m]$ such that $\left|P_{i}\right| \geq m / n-1$ for all $i \in[n]$. Pick a truth table $T \in\{0,1\}^{m}$ uniformly at random. Then $T$ is $(r n)$-irritable on $\mathcal{P}$ except with probability $2^{-\Omega\left(n^{2}\right)}$.

Proof. We prove this by bounding the probability that, for some fixed $i^{\star}$ and some fixed oracle circuit $C$,

$$
C^{T_{P_{1}}, \ldots, T_{P_{i^{\star}-1}}, T_{P_{i^{\star}+1}}, \ldots, T_{P_{n}}} \text { computes } T
$$

and then union bounding over all $i^{\star}$ and all oracle circuits of size at most $r n$.
Realize that the function computed by $C^{T_{P_{1}}, \ldots, T_{P_{i^{\star}-1}}, T_{P_{i^{\star}+1}}, \ldots, T_{P_{n}}}$ does not depend on any of the bits in $T$ that lie in $P_{i^{\star}}$. Therefore, since $\left|P_{i^{\star}}\right| \geq m / n-1$, this means that the probability that $\mathcal{C}^{T_{P_{1}}, \ldots, T_{P_{i^{\star}-1}}, T_{P_{i^{\star}+1}}, \ldots, T_{P_{n}}}$ computes $T$ is at most $2^{-m / n+1} \leq 2^{-n^{2}+1}$.

Now, we union bound. Clearly, there are at most $n$ choices of $i^{\star}$. Next, we need to count the number of $C$ of size at most $r n$. For concision, let $s=r n$. We bound the number of oracle circuits of size $s$, allowing for identity gates to catch circuits of smaller size. For each of the $s$ gates, there are $4+n$ gate types to chose from (AND, OR, NOT, identity, and the $n$ oracle gates). Then, for each of the $s$ gates, we have to choose the at most $\log (m)$ wires that feed into that gate and there are at most $(s+\log (m))$ choices for where each of these wires comes from. Hence, we get a bound of

$$
(4+n)^{s}(s+\log (m))^{s \log (m)}
$$

whose logarithm is

$$
s \log (4+n)+s \log (m) \log (s+\log (m))=\tilde{O}(r n)
$$

Thus, probability that $T$ fails to be $(r n)$-irritable on $\mathcal{P}$ is at most
$n 2^{-n^{2}+\tilde{O}(r n)} \leq 2^{-\Omega\left(n^{2}\right)}$.

Thus, using a random choice of $T$ gives us an NP-hardness result under RP-reductions.

- Corollary 4.7. NP $\leq_{m}^{\mathrm{RP}} \mathrm{MOCSP}$.

Proof. Let $r$ be sufficiently large that computing a 4 -approximation to $r$-bounded set cover is NP-hard (such an $r$ exists by Theorem 2.6). We will reduce giving a 4-approximation of $r$-bounded set cover to MOCSP.

The reduction $R$ works as follows. On an instance $\left(c, S_{1}, \ldots, S_{t}\right)$ of SetCover $_{r, n, t}, R$ first computes the integer $m$ that is the least power of two greater than $n^{3}$. Next, $R$ computes the partition $\mathcal{P}=\left(P_{1}, \ldots, P_{n}\right)$ of $[m]$ where for all $i \in[n]$
$P_{i}=\{j \in[m]: j \equiv i \bmod n\}$.
Then, $R$ picks a truth table $T$ of length $m$ uniformly at random. After that, $R$ runs the algorithm $A$ from Theorem 4.4 on the input $\left(0^{n}, 0^{t}, T, \mathcal{P}\right)$ to obtain the circuit $C_{n, t, T, \mathcal{P}}$. Finally, $R$ outputs $\operatorname{MOCSP}\left(C_{n, t, T, \mathcal{P}}\left(c, S_{1}, \ldots, S_{t}\right)\right)$.

Now, we argue for correctness. Theorem 4.4 guarantees that $C_{n, t, T, \mathcal{P}}$ correctly answers all YES instances of SetCover $r, n, t$, so $R$ also correctly answers all YES instances of SetCover $_{r, n, t}$.

On the other hand, observe that our construction of $\mathcal{P}$ guarantees that $\left|P_{i}\right| \geq m / n-1$ for all $i \in[n]$, so Lemma 4.6 implies that $T$ is $(r n)$-irritable on $\mathcal{P}$ with high probability. Therefore, Theorem 4.1 further implies that with high probability $C_{n, t, T, \mathcal{P}}$ (and hence $R$ ) computes a 4 -approximation to $r$-bounded set cover.

Using more queries to MOCSP, we can improve the RP reduction to a ZPP reduction by checking if the randomly chosen $T$ is indeed $(r n)$-irritable on $\mathcal{P}$.

- Corollary 4.8. NP $\leq_{t t}^{\mathrm{ZPP}} \mathrm{MOCSP}$.

Proof. Run the same reduction as in the proof of Corollary 4.7 except check whether $T$ is $(r n)$-irritable on $\mathcal{P}$ using the MOCSP oracle. This can be done at the same time the as the MOCSP oracle answers $\operatorname{MOCSP}\left(C_{n, t, T, \mathcal{P}}\left(c, S_{1}, \ldots, S_{t}\right)\right)$. If $T$ is indeed $(r n)$-irritable on $\mathcal{P}$, then we know the output given by $\operatorname{MOCSP}\left(C_{n, t, T, \mathcal{P}}\left(c, S_{1}, \ldots, S_{t}\right)\right)$ is correct using Theorem 4.1. Otherwise, output "don't know."

We can also use non-uniform bits to provide the reduction with a truth table $T$ and a partition $\mathcal{P}$ such that $T$ is sufficiently irritable on P . This yields an $\mathrm{AC}^{0}$ many-one reduction.

- Corollary 4.9. MOCSP is NP-hard under (non-uniform) $\mathrm{AC}^{0}$ many-one reductions.

Proof. Let $r$ be large enough that computing a 4 -approximation to $r$-bounded set cover is NP-hard. It suffices to show that for all sufficiently large $n$ and $t$, there is an $\mathrm{AC}^{0}$ circuit $C$ such that MOCSP $\circ C$ computes a 4 -approximation to SetCover $r, n, t$.

By Lemma 4.6 for sufficiently large $n$, there exists a truth table $T$ of length $O\left(n^{3}\right)$ and a partition $\mathcal{P}=\left(P_{1}, \ldots, P_{n}\right)$ of $[m]$ such that $T$ is $(r n)$-irritable on $\mathcal{P}$. Thus, letting $A$ be the algorithm from Theorem 4.4, $A\left(0^{n}, 0^{t}, T, \mathcal{P}\right)$ outputs a depth- $2 \mathrm{AC}^{0}$ circuit $C$ such that MOCSP $\circ C$ computes a 4 -approximation to SetCover $_{r, n, t}$, as desired.

At this point, one might begin to speculate whether we can prove that MOCSP is NPhard under deterministic polynomial-time reductions. Unfortunately, this seems difficult. This is because Murray and Williams' [22] and Hitchcock and Pavan's [17] result that NP $\leq_{t t}^{P}$ MCSP $\Longrightarrow$ EXP $\neq$ ZPP also holds for MOCSP with essentially the same proof. For completeness, we give the MOCSP version of Murray and Williams' proof in Appendix B.

- Theorem 4.10 (Essentially proved in [22] and [17]). If NP $\leq_{t t}^{\mathrm{P}}$ MOCSP, then EXP $\neq \mathrm{ZPP}$.

Still, it seems plausible to us that MOCSP is hard for NP under Turing reductions. Indeed, Theorem 4.4 implies that, to prove a $P$-Turing reduction, it suffices to show that that there is a polynomial time algorithm $B$, with oracle access to MOCSP, such that for all large $n, B\left(0^{n}\right)$ outputs a truth table $T$ and a partition $\mathcal{P}=\left(P_{1}, \ldots, P_{n}\right)$ such that $T$ is $(r n)$-irritable on $\mathcal{P}$. We stress that $B$ has access to MOCSP, so $B$ can actually check whether $T$ is $(r n)$-irritable on $\mathcal{P}$ and make adjustments accordingly.

- Conjecture 4.11. NP $\leq_{T}^{P}$ MOCSP.

Maybe it is even possible to prove that such a $B$ exists if $\mathrm{E} \nsubseteq$ i.o-SIZE $\left[2^{O(n)}\right]$.

- Open Question 4.12. Can one show that $\mathrm{E} \nsubseteq$ i.o-SIZE $\left[2^{O(n)}\right]$ implies $\mathrm{NP} \leq_{T}^{\mathrm{P}} \mathrm{MOCSP}$ ?

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## A MAJORITY reduces to $\left(\mathrm{AC}_{d}^{0}\right)$-MCSP when Lipchitzness holds

Our goal in this section is to find a small $\left(\mathrm{AC}_{d}^{0}\right)$-MCSP-oracle circuit that computes MAJORITY on $n$-bits for sufficiently large $n$. We can do this using the techniques of Golovnev et al. [11]. In order to make our proof relatively self-contained, we differ slightly from the presentation in [11]. In particular, our presentation follows a method for computing MAJORITY that is described in Shaltiel and Viola [28].

At a high-level, this procedure works by using the input string to sample a random variable whose circuit complexity spikes depending on the weight of the input and then using Lipchitzness to prove that this spike occurs with high enough probability that we can derandomize using non-uniformity.

Continuing the notation from Section 3, assume that there is an $m \in\left\{q^{10}, q^{50}\right\}$ such that $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}$ is $\left(m^{25}\right)$-Lipchitz on inputs of length $m$.

We define the random variable $T_{p, m} \in\{0,1\}^{m}$ where each bit in $T_{p, m}$ is independently chosen to be one with probability $p$ and zero with probability $1-p$.

- Lemma A.1. $\mathbb{E}\left[\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(T_{p, m}\right)\right]=\tilde{O}(p m)$ if $p \geq m^{-1 / 3}$

Proof. By Hoeffding's inequality we have that the probability that $T_{p, m}$ has greater than $k$ ones is at most $\exp \left(-2 \epsilon^{2} m\right)$. Via computation by DNF, if a truth table $T \in\{0,1\}^{m}$ has at most $k$ ones, $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}(T)=k \log m=\tilde{O}(k)$. Similarly, we have that $\max \{C(T): T \in$ $\left.\{0,1\}^{m}\right\}=\tilde{O}(m)$. Hence, we get that

$$
\mathbb{E}\left[\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(T_{p, m}\right)\right]=\tilde{O}(k)+\tilde{O}\left(\exp \left(-2 \epsilon^{2} m\right) m\right)=\tilde{O}\left(p m+p m \epsilon+\exp \left(-2 \epsilon^{2} m\right) m\right)
$$

If we set $\epsilon=\sqrt{\frac{\ln m}{2 m}}$, then we have

$$
\mathbb{E}\left[\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(T_{p, m}\right)\right] \leq \tilde{O}(p m+p \sqrt{m} \ln m+1) \leq \tilde{O}(p m+\sqrt{m} \ln m)
$$

Finally, if $p \geq 1 / m^{1 / 3}$, we have $\mathbb{E}\left[T_{p, m}\right]=\tilde{O}(p m)$ as desired
We will make use of the following concentration inequality.

- Theorem A. 2 (McDiarmid's "bounded differences inequality" [21]). Let $f:\{0,1\}^{n} \rightarrow \mathbb{R}$ be $c$-Lipchitz. Let $X_{1}, \ldots, X_{n}$ be independent random variables with values in $\{0,1\}$. Let $\mu=\mathbb{E}_{X_{1}, \ldots, X_{n}}\left[f\left(X_{1}, \ldots, X_{n}\right)\right]$. Then

$$
\operatorname{Pr}\left[\left|f\left(X_{1}, \ldots, X_{n}\right)-\mu\right| \geq \epsilon\right] \leq 2 \exp \left(-\frac{\epsilon^{2}}{n c^{2}}\right)
$$

$\mathrm{AC}^{0}[p]$ Lower Bounds and NP-Hardness for Variants of MCSP

For $t \in \mathbb{N}$ and $w_{1} \neq w_{2} \in[t]$, we say a Boolean function $f:\{0,1\}^{t} \rightarrow\{0,1\}$ computes WTDIS $_{t}\left[w_{1}, w_{2}\right]$ if $\operatorname{wt}(x)=w_{1}$ implies $f(x)=1$ and $\mathrm{wt}(x)=w_{2}$ implies $f(x)=0$. (WTDIS is short for weight distinguishing.)

- Theorem A.3. If $n$ is sufficiently large, then for all $1 \leq b \leq \sqrt{q} / 2$, there exists a (nonuniform) $\mathrm{NC}^{0}$ oracle circuit $C$ with at most $O\left(n^{100}\right)$ wires such that $C^{\left(\mathrm{AC}_{d}^{0}\right)-\mathrm{MCSP}}$ computes $\mathrm{WTDIS}_{q}\left[w_{1}, w_{1}+b\right]$ for some $w_{1} \geq \sqrt{q} / 2$. Moreover, $C$ has a single gate.

Proof. For $w \in[q]$, let $p_{w}=\frac{w}{2 q}$. Let $w_{0}$ be the largest integer less than $\sqrt{q}$ such that $q-w_{0}$ is a multiple of $b$. (Note that $w_{0} \geq \sqrt{q}-b \geq \sqrt{q} / 2$ ).

By Lemma A.1, we have that

$$
\mathbb{E}\left[\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(T_{p_{w_{0}}, m}\right)\right]=\tilde{O}\left(\frac{\sqrt{q}}{q} m\right)=\tilde{O}(m / \sqrt{q})
$$

On the other hand, since $p_{q}=1 / 2, T_{p_{q}, m}$ is just a binary string of length $m$ picked uniformly at random, so the formula size lower bounds of Shannon and Riordan imply

$$
\mathbb{E}\left[\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(T_{p_{q}, m}\right)\right]=\underset{x \in\{0,1\}^{m}}{\mathbb{E}}[C(x)]=\tilde{\Omega}(m)
$$

(note that an $\mathrm{AC}_{d}^{0}$ formula of size $s$ implies an unrestricted formula of size $s$ ). Hence, by an averaging argument there exists a $w_{1} \geq w_{0} \geq \sqrt{q} / 2$ such that

$$
\mathbb{E}\left[\mathrm{CC}_{\mathrm{AC}_{d}^{0}}\left(T_{p_{w_{1}+b}, m}\right)\right]-\mathbb{E}\left[\mathrm{CC}\left(T_{p_{w_{1}}, m}\right)\right] \geq \frac{\tilde{\Omega}(m)-\tilde{O}(m / \sqrt{q})}{q}=\tilde{\Omega}(m / q)
$$

Let $t=\frac{\mathbb{E}\left[T_{p_{w_{1}+b}, m}\right]+\mathbb{E}\left[T_{p_{w_{1}}, m}\right]}{2}$. Then we have that $\mathbb{E}\left[T_{p_{w_{1}+b}, m}\right]-t=\tilde{\Omega}(m / q)$ and $t-$ $\mathbb{E}\left[T_{p_{w_{1}}, m}\right]=\tilde{\Omega}(m / q)$.

We now outline a probabilistic oracle circuit $D$ that we will later make into a deterministic $\mathrm{NC}^{0}$ circuit. $D$ takes as input a string $x \in\{0,1\}^{n}$ and takes as its random "inputs" strings $u_{1}, \ldots, u_{m} \in\{0,1\}^{\log q}$ and $v_{1}, \ldots, v_{m} \in\{0,1\}$. The reduction then computes the string $y:=y_{1} \ldots y_{m}$ where $y_{i}$ is zero if $v_{i}$ is zero and $y_{i}$ is the $u_{i}$ th bit of $x$ if $v_{i}$ is one (recall, $q$ is a power of two $)$. $D$ then outputs $\left(\mathrm{AC}_{d}^{0}\right)-\mathrm{MCSP}(y, t)$.

We now argue for correctness with high probability. Realize each $y_{i}$ is independent with probability $\frac{\mathrm{wt}(x)}{2 n}$ of being 1 . Hence, $y$ is just the random variable $T_{p_{w}, m}$ where $w=\mathrm{wt}(x)$. Hence, if $\mathbf{w t}(x)=w_{1}$, then

$$
\operatorname{Pr}[R(x) \neq 1]=\operatorname{Pr}\left[\mathcal{C}\left(T_{p_{w_{1}}, m}\right)>t\right]
$$

Recall that $t-\mathbb{E}\left[T_{p_{w_{1}}, m}\right]=\tilde{\Omega}(m / q)$ and, by assumption, $\mathrm{CC}_{\mathrm{AC}_{d}^{0}}$ on inputs of length $m$ is ( $m^{.25}$ )-Lipchitz, so by Theorem A.2, we have that this probability is bounded by

$$
2 \exp \left(-2 \frac{\tilde{\Omega}\left(m^{2}\right)}{\tilde{O}\left(q^{2} m^{1.5}\right)}\right) \leq \exp \left(-2 \frac{\tilde{\Omega}\left(q^{\cdot 5 \cdot 10}\right)}{\tilde{O}\left(q^{2}\right)}\right)=O\left(\exp \left(-q^{3}\right)\right)
$$

using the fact that $m \geq q^{10}$. A similar analysis shows that the probability $D$ errs if $\operatorname{wt}(x)=w_{1}+b$ is at most $O\left(\exp \left(-q^{3}\right)\right)$. This completes the analysis of $D$.

We now argue that this reduction can be derandomized using non-uniformity. For each input of weight either $w_{1}$ or $w_{1}+b$, we have shown the fraction of random strings which err on that input is $O\left(\exp \left(-q^{3}\right)\right)$. Hence, the fraction of random seeds which err on at least one input of weight $w_{1}$ or $w_{1}+b$ is at most

$$
2^{q} O\left(\exp \left(-q^{3}\right)\right)<1
$$

for large enough $n$. Thus, there exists some fixed $u_{1}, \ldots, u_{m}$ and $v_{1}, \ldots, v_{m}$ which work on all inputs of length $q$. Once we are (non-uniformly) given these $u_{1}, \ldots, u_{m}$ and $v_{1}, \ldots, v_{m}$ which work on all inputs, we can turn $D$ into an $\mathrm{NC}^{0}$ oracle circuit $C$ which has just a single gate (an oracle gate) whose inputs are the fixed number $t$ and the string $y$ where each bit of $y$ is either a fixed bit of $x$ or zero. This yields a $\mathrm{NC}^{0}$ oracle circuit with $O(m)=O\left(q^{50}\right)=O\left(n^{100}\right)$ wires.

- Corollary A.4. If $n$ is sufficiently large, then for all distinct $w_{1}, w_{2} \in[n]$ there is an $\mathrm{NC}^{0}$ oracle circuit $C$ with at most two gates and $O\left(n^{100}\right)$ wires such that $C^{\left(\mathrm{AC}_{d}^{0}\right)-\mathrm{MCSP}}$ computes $\mathrm{WTDIS}_{n}\left[w_{1}, w_{2}\right]$.

Proof. Fix some $w_{1} \neq w_{2}$. Without loss of generality assume $w_{1}<w_{2}$ (if this is not the case, then swap the names of $w_{1}$ and $w_{2}$ in this proof and add a NOT gate to the top of $C$ ). Let $b=w_{2}-w_{1}$. Recall $q$ is the least power of two such that $n \leq \sqrt{q} / 2$. Note that $q=\Theta\left(n^{2}\right)$ and $b \leq n \leq \sqrt{q} / 2$. Theorem A. 3 guarantees there exists an $\mathrm{NC}^{0}$ oracle circuit $D$ of size $O\left(n^{20}\right)$ such that $D^{\text {MCSP }}$ computes $\mathrm{WTDIS}_{q}\left[w_{3}, w_{3}+b\right]$ for some $w_{3} \geq \sqrt{q} / 2 \geq n$. Finally, let $C$ be the oracle circuit that on input $x$ outputs $D(y)$ where $y=1^{w_{3}-w_{1}} 0^{q-n-w_{3}+w_{1}} x$. The correctness of this output is guaranteed by the fact that $\mathrm{wt}(y)=w_{3}$ if and only if $\mathrm{wt}(x)=w_{1}$ and $\operatorname{wt}(y)=w_{3}+b$ if and only if $\operatorname{wt}(x)=w_{2}$.

- Corollary A.5. If $n$ is sufficiently large, then there exists a depth-4 $\mathrm{AC}^{0}$ truth table oracle circuit $C$ with $O\left(n^{102}\right)$ wires such that $C^{\left(\mathrm{AC}_{d}^{0}\right)-\mathrm{MCSP}}$ computes MAJORITY on strings on length $n$.

Proof. It suffices to show that, for all $w \in[n]$, one can check if a string $x \in\{0,1\}^{n}$ has weight $w$ using a depth $-3 \mathrm{AC}^{0}$ truth-table oracle circuit $C_{w}$ of size $O\left(n^{101}\right)$. If one is able to do this, then MAJORITY is computed by $\bigvee_{w \geq n / 2} C_{w}(x)$.

For $w \in[n]$, let $\mathrm{wt}_{w}:\{0,1\}^{n} \rightarrow\{0,1\}$ be the Boolean function that outputs one if and only if its input is a string of weight $w$. Now fix some $w \in[n]$. We claim that

$$
\mathrm{wt}_{w}(x)=\bigwedge_{w^{\prime} \in[n]: w \neq w^{\prime}} \mathrm{WTDIS}_{n}\left[w, w^{\prime}\right]
$$

If $x$ has weight $w$, then $\operatorname{WTDIS}_{n}\left[w, w^{\prime}\right](x)=1$ for all $w^{\prime} \neq w$, so

$$
\mathrm{wt}_{w^{\prime}}(x)=1=\bigwedge_{w^{\prime} \in[n]: w \neq w^{\prime}} \mathrm{WTDIS}_{n}\left[w, w^{\prime}\right] .
$$

On the other hand, if $x$ has weight $w^{\prime} \neq w$, then $\operatorname{WTDIS}_{n}\left[w, w^{\prime}\right](x)=0$, so

$$
\mathrm{wt}_{w}(x)=0=\bigwedge_{w^{\prime} \in[n]: w \neq w^{\prime}} \mathrm{WTDIS}_{n}\left[w, w^{\prime}\right] .
$$

Finally, by Corollary A. 4 we have that $\bigwedge_{w \in[n]: w \neq w^{\prime}} \operatorname{WTDIS}_{n}\left[w^{\prime}, w\right]$ is computable by a depth-3 AC ${ }^{0}$ truth table oracle circuit with $O\left(n^{101}\right)$ wires.

B $N P \leq_{t t}^{P}$ MOCSP implies EXP $\neq$ ZPP
The proof of this result follows essentially exactly from Murray and Williams's [22] proof for MCSP. For completeness, we replicate the proof here (even using their words and structure).

- Proposition B.1. If $\mathrm{NP} \leq_{t t}^{\mathrm{P}} \mathrm{MOCSP}$, then $\mathrm{EXP} \subseteq \mathrm{P} /$ poly implies $\mathrm{EXP}=\mathrm{NEXP}$.
$\mathrm{AC}^{0}[p]$ Lower Bounds and NP-Hardness for Variants of MCSP

Proof. Assume NP $\leq_{t t}^{\mathrm{P}} \mathrm{MOCSP}$ and $\operatorname{EXP} \subseteq \mathrm{P} /$ poly. Let $L \in \operatorname{NTIME}\left(2^{n^{c}}\right)$ for some $c \geq 1$. It suffices to show that $L \in$ EXP.

We pad $L$ into the $L^{\prime}=\left\{x 01^{2^{|x|^{c}}}: x \in L\right\}$. Note that $L^{\prime} \in N P$. Hence there is a polynomial-time truth table reduction from $L^{\prime}$ to MOCSP. Composing the reduction from $L$ to $L^{\prime}$ with the reduction from $L^{\prime}$ to MOCSP, we get a $2^{c^{\prime} n^{c}}$-time truth table reduction $R$ from $n$-bit instances of $L$ to $2^{c^{\prime} n^{c}}$-bit instances of MOCSP for some constant $c^{\prime}$.

Let $Q(x)$ denote the concatenated string of all MOCSP queries produced by $R$ in order on input $x$. Define the language

$$
B I T S_{Q}:=\{(x, i): \text { the } i \text { th bit of } Q(x) \text { is } 1\}
$$

$B I T S_{Q}$ is clearly in EXP. Since EXP $\subseteq \mathrm{P} /$ poly, for some $d \geq 1$ there is a circuit family $C_{n}$ of size at most $n^{d}+d$ computing $B I T S_{Q}$ on $n$-bit inputs.

Thus, on a given instance $x$, we have $\mathrm{CC}(Q(x)) \leq s(|x|)$ where $s(|x|):=\left(|x|+2 c^{\prime}|x|^{c}\right)^{d}+d$. Therefore, every MOCSP query $\left(T, s^{\prime}, T_{1}, \ldots, T_{t}\right)$ produced by the reduction $R$ on input $x$ satisfies

$$
\mathrm{CC}^{T_{1}, \ldots, T_{t}}(T) \leq \mathrm{CC}(T) \leq e \cdot \mathrm{CC}(Q(x)) \leq e \cdot s(|x|)
$$

for some constant $e$ since $T$ is a substring of $Q(x)$ (see Lemma 2.2 in [22] for a proof of this substring fact). This leads to the following exponential time algorithm for $L$ :

On input $x$, run the exponential-time reduction $R(x)$ by using the following procedure for answering each MOCSP oracle query $\left(T, s^{\prime} ; T_{1}, \ldots, T_{t}\right)$. If $s^{\prime}>e \cdot s(|x|)$, then respond YES to the query. Otherwise, cycle through every oracle circuit $E$ of size at most $s^{\prime}$. If $E^{T_{1}, \ldots, T_{t}}$ computes $T$, then respond YES. If no such $E$ is found, then respond NO.

It suffices to show the procedure for answering MOCSP oracle queries runs in exponential time. Let $n=|x|$. First, we need to count the number of oracle circuits $E$ on $\left(\log |T| \leq c^{\prime} n^{c}\right)$-inputs with size at most $s(n)$ As shown in Lemma 4.6, the logarithm of the number of oracle circuit of size at most $s(|x|)$ on $\left(c^{\prime} n^{c}\right)$-inputs with $t$ oracle functions is at most

$$
O\left(s(n) \log (4+t)+s(|x|) \log \left(c^{\prime} n^{c}\right) \log \left(s(|x|)+\log \left(c^{\prime} n^{c}\right)\right)\right)
$$

Since $t \leq 2^{c^{\prime} n^{c}}$ and $s$ is polynomial in $n$, it is easy to see that the number of such circuits $E$ is at most exponential. Second, one can check if an oracle circuit $E$ satisfies $E^{T_{1}, \ldots, T_{t}}$ computes $T$ in time polynomial in $\left(|E|+|T|+\left|T_{1}\right|+\cdots+\left|T_{t}\right|\right)$ and hence exponential in $n$. As a result, $L \in \mathrm{EXP}$, completing the proof.

- Theorem B.2. If $\mathrm{NP} \leq_{t t}^{\mathrm{P}} \mathrm{MOCSP}$, then $\mathrm{EXP} \neq \mathrm{NP} \cap \mathrm{P} /$ poly. Consequently, EXP $\neq \mathrm{ZPP}$. Proof. For contradiction, suppose NP $\leq_{t t}^{P}$ MOCSP and EXP $=N P \cap P /$ poly. Then by Proposition B. 1 NEXP $\subseteq$ EXP $\subseteq$ NP contradicting the nondeterministic time hierarchy theorem [32].


[^0]:    1 [6] cites a personal communication from Levin regarding this story.

[^1]:    ${ }^{2}$ [22] only shows the result under many-one reductions, but their techniques easily generalize to the truth table case. [17] explicitly proves the truth table result using a different approach than [22].
    ${ }^{3}$ Pich is an author on [24] but not [26]
    ${ }^{4}$ We will always use the notation $\mathrm{AC}_{d}^{0}$ to refer to depth- $d$ formulas and never depth- $d$ circuits.

[^2]:    5 The conference versions of [8] and [1] are four years apart.

[^3]:    ${ }^{6}$ Recall, our notion of formula size is the number of input leaves.
    ${ }^{7}$ In case the reader is unsure of whether the last parts of this procedure are implementable in $\mathrm{AC}^{0}$, realize that the output of $\mathrm{CC}_{\mathrm{ANDOAC}_{d-1}^{0}}\left(f_{x}^{u}\right)$ is a binary string of length $O(\log n)$ and that any function on a string of length $O(\log n)$ can be computed by a polynomial-sized DNF. See the proof in Section 3 for more details.

[^4]:    8 both the "we observe" statements in this paragraph are consequences of standard direct product theorems for formulas.

[^5]:    - References

    1 Eric Allender, Harry Buhrman, Michal Koucký, Dieter van Melkebeek, and Detlef Ronneburger. Power from random strings. SIAM J. Comput., 35(6):1467-1493, 2006.
    2 Eric Allender and Bireswar Das. Zero knowledge and circuit minimization. Information and Computation, 256:2-8, 2017.
    3 Eric Allender and Shuichi Hirahara. New insights on the (non-)hardness of circuit minimization and related problems. In Symposium on Mathematical Foundations of Computer Science (MFCS), pages 54:1-54:14, 2017.

