# A very simple function that requires exponential size nondeterministic graph-driven read-once branching programs 

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#### Abstract

Branching programs are a well-established computation model for boolean functions, especially read-once branching programs (BP1s) have been studied intensively. A very simple function $f$ in $n^{2}$ variables is exhibited such that both the function $f$ and its negation $\neg f$ can be computed by $\Sigma_{p}^{3}$-circuits, the function $f$ has nondeterministic BP 1 s (with one nondeterministic node) of linear size and $\neg f$ has size $O\left(n^{4}\right)$ for oblivious nondeterministic BP1s but $f$ requires nondeterministic graph-driven BP1s of size $2^{\Omega(n)}$. This answers an open question stated by Jukna, Razborov, Savický, and Wegener [13].


## 1 Introduction

Besides boolean circuits and formulae branching programs (BPs), sometimes also called binary decision diagrams (BDDs), are one of the standard representations for boolean functions. (For a history of results on branching programs see, e.g., the monograph of Wegener [18]).

Definition 1. A branching program (BP) or binary decision diagram (BDD) on the variable set $X_{n}=\left\{x_{1}, \ldots, x_{n}\right\}$ is a directed acyclic graph with one source and two sinks labeled by the constants 0 and 1. Each non-sink node (or decision node) is labeled by a boolean variable and has two outgoing edges, one labeled by 0 and the other by 1. A nondeterministic branching program ( $\mathrm{V}-\mathrm{BP}$ for short) is a branching program with some additional unlabeled nodes, called nondeterministic nodes, which have out-degree 2.

An input $a \in\{0,1\}^{n}$ activates all edges consistent with $a$, i.e., the edges labeled by $a_{i}$ which leave nodes labeled by $x_{i}$ and all unlabeled edges. A computation path for an input $a$ in $a \operatorname{BP} G$ is a path of edges activated by the input a that leads from the source to a sink. A computation path for an input a that leads to the 1-sink is called accepting path for $a$.

Let $B_{n}$ denote the set of all boolean functions $f:\{0,1\}^{n} \rightarrow\{0,1\}$. The BP $G$ represents a function $f \in B_{n}$ for which $f(a)=1$ iff there exists an accepting path for the input $a$.

A parity branching program (or $\oplus-\mathrm{BP}$ for short) is syntactically a nondeterministic branching program but instead of the usual existential nondeterminism the parity acceptance mode is used. An input a is accepted iff the number of its accepting paths is odd.

The size of a branching program $G$ is the number of its nodes and is denoted by $|G|$. The branching program size of a boolean function $f$ is the size of the smallest BP representing $f$. The length of a branching program is the maximum length of a path.

[^0]In order to learn more about the power of branching programs, various restricted models have been investigated intensively and several interesting restricted types of BPs could be analyzed quite successfully (for the latest breakthrough for semantic superlinear length BPs see [1],[2], and [3], where using a subtle combinatorial reasoning superpolynomial lower bounds were obtained).

Besides this complexity theoretical viewpoint people have used branching programs in applications. Bryant [9] introduced ordered binary decision diagrams (OBDDs) which are up to now the most popular representation for formal circuit verification.

Definition 2. An OBDD is a branching program with a variable ordering given by a permutation $\pi$ on the variable set. On each path from the source to the sinks, the variables at the nodes have to appear in the order prescribed by $\pi$ (where some variables may be left out). $A \pi$-OBDD is an OBDD ordered according to $\pi$.

Unfortunately, several important and also quite simple functions have exponential OBDD size. Therefore, more general representations with good algorithmic behavior are necessary. Generalizing the concept of variable orderings to graph orderings Gergov and Meinel $[10,11]$ and Sieling and Wegener [16] have shown independently how deterministic read-once branching programs can be used for verification.

Definition 3. A graph ordering is a branching program with a single sink, where on each path from the source to the sink all variables appear exactly once. Let $\omega \in\{\vee, \oplus\}$. An $\omega$-nondeterministic graph-driven BP1 is an $\omega$-nondeterministic $\mathrm{BP} 1 G$ for which there exists a graph ordering $G_{0}$ with the following property: If for an input a, a variable $x_{i}$ appears on a computation path of $a$ in $G$ before the variable $x_{j}$, then $x_{i}$ also appears on the unique computation path of a in $G_{0}$ before $x_{j}$.

In the following if nothing else is mentioned nondeterministic graph-driven BP1s means $\vee$-nondeterministic graph-driven BP1s .

The main idea is that in graph-driven BP1s according to a fixed graph ordering, for each input the variables are tested in the same ordering, whereas (different from OBDDs) for different inputs different orderings may be used. It is easy to see that any deterministic BP1 is in fact a BP1 for a suitably chosen graph ordering. For nondeterministic BP1s graph orderings do not exist in general. If we generalize OBDDs to nondeterministic OBDDs we gain the possibility of nondeterministic guesses, but the variable ordering remains the same on all computation paths. For read-once branching programs the situation is different. It is possible to guess nondeterministically and moreover, for each input arbitrary orderings of the variables are allowed.

The concept of graph-ordered branching programs has turned out to be also useful in other settings, see e.g. [14] and [17]. Gergov and Meinel [10] were the first ones who suggested parity graph-driven BP1s as a data structure for boolean functions. Another reason for investigating parity graph-driven BP1s is that until now exponential lower bounds on the size of parity read-once branching programs for explicitly defined boolean functions are unknown. One step towards the proof of such bounds might be to investigate BP models inbetween deterministic and parity BP1s. Nondeterministic and parity graph-driven BP1s have been investigated more intensively in [4], [8], and [7].
Definition 4. $\Sigma_{p}^{d}$, $\Pi_{p}^{d}$ be the classes of functions that can be computed by polynomial size depth-d circuits over the de Morgan basis $\{\wedge, \vee, \neg\}$ (negations are allowed only at
the input variables and do not contribute to the depth) that have $\vee($ respectively, $\wedge)$ as output gate.

The following general question has been widely studied for various computational models:

Suppose that both a computational problem $f$ and its complement $\neg f$ posses an efficient nondeterministic computation in some model. Does this imply that $f$ can also be computed efficiently and deterministically in the same model?

This question is often called the P versus $\mathrm{NP} \cap c o-\mathrm{NP}$ question and for boolean (nonuniform) complexity, polynomial size instead of polynomial time is investigated.

In [13] an explicitly defined boolean function is presented that is in $\Sigma_{p}^{3}$ and in $\mathrm{NP} \cap \mathrm{co-NP}$ for read-once branching programs but has exponential deterministic readonce branching program size. Another function belongs to the smaller class $\Sigma_{p}^{3} \cap \Pi_{p}^{3}$, but the separation is only quasipolynomial. Jukna, Razborov, Savický, and Wegener (1999) asked whether the class $\Sigma_{p}^{3} \cap \Pi_{p}^{3}$ contains a function separating NP $\cap$ co-NP from quasipolynomial size in the context of read-once branching programs. Here we answer the question in the affirmative. Moreover, we prove that not only the deterministic BP1 size but even the nondeterministic graph-driven BP1s size of the selected function is exponential. In [5] a lower bound method for nondeterministic graph-driven read-once branching programs is presented and the first exponential lower bound for an explicitly defined boolean function has been proved. Since the lower bound technique is very general we cannot apply the method directly to our function. Therefore, we have to improve the lower bound method.

## 2 The separation result

The theory of communication complexity is a powerful tool for proving lower bounds on the size of restricted nondeterministic oblivious BPs (a BP is called oblivious if the nodes can be partitioned into levels such that edges point only from lower to higher levels and all internal decision nodes of one level are labeled by the same variable). (See, e.g., [12] and [15] for the theory of communication complexity.) In [7] and [5] it has been shown how this tool can be used for proving large lower bounds on the size of so-called well-structured $\omega$-nondeterministic $\mathrm{BP} 1 \mathrm{~s}, \omega \in\{\vee, \oplus\}$, repectively, of nondeterministic graph-driven BP1s.

For the ease of notations we assume w.l.o.g. that $n$ is an even number. The function $f_{n}$ is defined on $n \times n$ boolean matrices $X$ on the variable set $X_{n}=\left\{x_{1,1}, \ldots, x_{n, n}\right\}$ and outputs 1 iff the matrix $X$ contains exactly one 1-entry in each row or exactly $n-1$ columns with exactly one 1-entry and $n-1$ 1-entries altogether. The technique described in [5] is very general and cannot be applied directly in order to prove an exponential lower bound on the size of nondeterministic BP1s representing $f_{n}$. Therefore, we have to improve the method. First, we need some further notation.

Definition 5. Let $f$ be a boolean function defined on the variables in $X_{n}=\left\{x_{1}, \ldots, x_{n}\right\}$. $A$ set $A(f)=\left\{\left(\alpha_{1}, \beta_{1}\right),\left(\alpha_{2}, \beta_{2}\right), \ldots,\left(\alpha_{k}, \beta_{k}\right)\right\}$, $\alpha_{i} \in\{0,1\}^{n^{\prime}}$ and $\beta_{i} \in\{0,1\}^{n-n^{\prime}}$, is called $a$ strong 1-fooling set if
i) $f\left(\alpha_{i}, \beta_{i}\right)=1$ for all $i \in\{1, \ldots, k\}$, and
ii) $i, j \in\{1, \ldots, k\}$ and $i \neq j$ implies that $f\left(\alpha_{i}, \beta_{j}\right\}=0$ and $f\left(\alpha_{j}, \beta_{i}\right\}=0$.

For a subset $Z \subseteq X_{n}$ we denote by $\mathcal{A}(Z)$ the set of all possible assigments to the variables in $Z$. Let $G_{0}$ be a graph ordering and $v$ a node in $G_{0}$. Let $X_{v}$ be the set of variables tested on a path from the source to $v$ (excluding the variable which is the label of $v)$ and $\mathcal{A}_{v} \subseteq \mathcal{A}\left(X_{v}\right)$ a set of partial assignments which lead in $G_{0}$ from the source to $v$. Using well-known facts from communication complexity the following is easy to prove. Let $v_{1}, \ldots, v_{k}$ be nodes in $G_{0}$, where $X_{v_{1}}=\ldots=X_{v_{k}}, \mathcal{A}_{v}=\cup_{1 \leq j \leq k} \mathcal{A}_{v_{j}}$, and $X_{v}:=X_{v_{1}}$. If there exists a strong 1-fooling set $A(f)=\left\{\left(\alpha_{1}, \beta_{1}\right),\left(\alpha_{2}, \beta_{2}\right), \ldots,\left(\alpha_{\left|\mathcal{A}_{v}\right|}, \beta_{\left|\mathcal{A}_{v}\right|}\right)\right\}, \alpha_{i} \in$ $\mathcal{A}_{v}$, and $\beta_{i} \in \mathcal{A}\left(X_{n} \backslash X_{v}\right)$, then any $\omega$-nondeterministic BP1 representing $f$ according to $G_{0}$ has a size of at least $\left|\mathcal{A}_{v}\right|$.

Now our proof idea is the following one. We use the fact that in a nondeterministic BP for a 1-input there has to be at least one accepting path and for 0-inputs accepting paths are not allowed. This property does not hold for parity BPs and until now promising methods for proving large lower bounds on the size of (usual) parity graph-driven BP1s are unknown.

Let $G$ be a nondeterministic graph-driven BP1 representing $f_{n}$ and let $G_{0}$ be a graph ordering such that $G$ is ordered according to $G_{0}$. We choose a large set of subpaths in $G_{0}$ and define $V$ as the set of nodes $v_{1}, \ldots, v_{l}$ which are reached by at least one of the chosen subpaths. If there is large number of subpaths which lead to the nodes $v_{i_{1}}, \ldots, v_{i_{k}}$ in $V$, where $X_{v_{i_{1}}}=\ldots=X_{v_{i_{k}}}$, then we can construct a large strong 1-fooling set. Otherwise, there are many nodes $v_{j_{1}}, \ldots, v_{j_{m}}$, where $X_{v_{j_{l}}} \neq X_{v_{j_{l^{\prime}}}}, 1 \leq l<l^{\prime} \leq m$. Let $S_{w}$ be the union of all variables tested on a path from $w$ to a sink (including the label of $w$ ). Let $x_{i^{\prime}, j^{\prime}}$ be a variable that is not contained in $X_{v_{j_{1}}} \cup \ldots \cup X_{v_{j_{m}}}$. We can prove that for each set $X_{v_{j_{i}}}$ there exists a node $w$ in $G$ such that $S_{w} \subseteq X_{n} \backslash\left\{X_{v_{j_{i}}} \cup\left\{x_{i^{\prime}, j^{\prime}}\right\}\right\}$. Therefore, the size of $G$ is at least $m$.

Theorem 1. The function $f_{n}$ is in $\Sigma_{p}^{3} \cap \Pi_{p}^{3}$ and can be represented by nondeterministic $\mathrm{BP} 1 s$ with one nondeterministic node in linear size and the function $\neg f_{n}$ has size $O\left(n^{4}\right)$ for nondeterministic $\mathrm{OBDD} s$ but its nondeterministic graph-driven BP 1 size is $2^{\Omega(n)}$.

## Proof.

## The upper bounds:

The proof of the upper bounds are not difficult to prove.

- We can test whether the matrix $X$ has exactly one 1-entry in row $i$ by

$$
\left(x_{i, 1} \vee \ldots \vee x_{i, n}\right) \wedge \bigwedge_{1 \leq j<k \leq n}\left(\bar{x}_{i, j} \vee \bar{x}_{i, k}\right)
$$

Therefore, the test whether the matrix $X$ has exactly one 1-entry in each row can be computed by $\Pi_{p}^{2}$-circuits of size $O\left(n^{3}\right)$. Similarly, the test whether the matrix $X$ contains no 1-entry in the jth column and exactly one 1-entry in each of the other columns can be computed by $\Pi_{p}^{2}$-circuits of size $O\left(n^{3}\right)$. Since there are $n$ possibilities to choose the column without a 1-entry, we can conclude that the function $f_{n}$ can be computed by $\Sigma_{p}^{3}$-circuits of size $O\left(n^{4}\right)$.

- The output of the function $\neg f_{n}$ is 1 iff there exist a row and two columns such that the row and the columns do not contain exactly one 1-entry each or there exist a row without exactly one 1-entry and a column with at least two 1-entries. The test
whether the matrix $X$ contains in row $i$ not exactly one 1 -entry can be computed by the $\Pi_{p}^{2}$-circuit

$$
\bigwedge_{1 \leq j \leq n}\left(x_{i, 1} \vee \ldots \vee x_{i, j-1} \vee \bar{x}_{i, j} \vee x_{i, j+1} \vee \ldots \vee x_{i, n}\right)
$$

in size $O(n)$. Simlilarly the test whether the matrix $X$ contains neither in column $j_{1}$ nor in column $j_{2}$ exactly one 1 -entry can be performed. The test whether the matrix $X$ contains in a chosen column at least two 1-entries can be performed in the same way in size $O(n)$. Since there are $O\left(n^{3}\right)$ possibilities to choose a row and two columns and $O\left(n^{2}\right)$ possibilities to choose a row and a column, $\neg f_{n}$ can be computed by $\Sigma_{p^{-}}^{3}$ circuits of size $O\left(n^{4}\right)$. Therefore, $f_{n}$ can be computed in size $O\left(n^{4}\right)$ by $\Pi_{p}^{3}$-circuits.

- Now we construct two (deterministic) OBDDs $G_{1}$ and $G_{2}$, where $G_{1}$ accepts exactly the satisfying inputs with exactly one 1-entry in each row and $G_{2}$ accepts exactly the satisfying inputs with exactly $n-1$ columns with exactly one 1 -entry and $n-11$ entries in $X$. The BP1 $G_{1}\left(G_{2}\right)$ uses a rowwise (columnwise) variable ordering which means that all variables of one row (column) are tested one after another. Moreover, in $G_{2}$ we store the information whether there has been one column without a 1-entry. Obviously, $G_{1} \vee G_{2}$ is a nondeterministic BP1 with one nondeterministic node for $f_{n}$ and - because any satisfying input contains either exactly $n$ ones or $n-1$ ones - also a valid parity BP1. The size of $G_{i}, i \in\{1,2\}$, is bounded above by $O\left(n^{2}\right)$. (Note, that $n^{2}$ is the number of variables in $X$.)
- For $\neg f_{n}$ we choose an arbitrary variable ordering and guess a row and two columns such that the number of 1 -entries is not exactly 1 in the row and for each of the chosen columns. Since there are $O\left(n^{3}\right)$ possibilities to choose a row and two columns and $O\left(n^{2}\right)$ possibilities to choose a row with not exactly one 1 -entry and a column with at least two 1 -entries, the size of the nondeterministic OBDD (for an arbitrary variable ordering) is $O\left(n^{4}\right)$.


## The lower bound:

For the lower bound the following observation is helpful. A partial assignment $a$ is crucial if it is possible to complete $a$ in two different ways $a_{r}$ and $a_{c}$ such that for the first complete assignment ( $a, a_{r}$ ) there exist in each row of the matrix $X$ exactly one 1 -entry and for the second one ( $a, a_{c}$ ) there exist exactly $n-1$ columns with exactly one 1 -entry and $n-11$-entries altogether.

Let $G$ be a nondeterministic graph-driven BP 1 representing $f_{n}$ and $G_{0}$ be a graph ordering such that $G$ is ordered according to $G_{0}$. We consider all paths in $G_{0}$ that correspond to permutation matrices which means that there exist $n$ variables $x_{i_{1}, j_{1}}, \ldots, x_{i_{n}, j_{n}}$ which are set to 1 , where $i_{l} \neq i_{l^{\prime}}$ and $j_{l} \neq j_{l^{\prime}}$ if $l \neq l^{\prime}$, and all other variables are set to 0 . The number of these paths is $n$ ! Next, we define a cut through all these paths after exactly $n / 2+1$ variables are set to 1 . For each crucial assignment $a$ corresponding to a chosen path to the cut there exist ( $n / 2-1$ )! different possibilities to complete $a$ to a permutation matrix. Therefore, there exist $\frac{n!}{(n / 2-1)!}$ different paths from the source to the cut. Let $R_{p}\left(C_{p}\right)$ be the set of indices $i$ for which a variable $x_{i, \cdot}\left(x_{, i}\right)$ is set to 1 on $p$. If $(n / 2+1)$ rows and columns have been chosen, there are $(n / 2+1)$ ! possibilities to map the indices of the rows to the indices of the columns. Therefore, there is a set $P$ of different paths, $|P| \geq\binom{ n}{n / 2+1}$, such that for two different paths $p$ and $p^{\prime}$ it is $R_{p} \neq R_{p^{\prime}}$
or $C_{p} \neq C_{p^{\prime}}$. Using the pigeonhole principle we can conclude that there exists a variable $x_{i, j}$ such that for at least $|P| / n^{2}$ paths $p$ in $P$ the variable $x_{i, j}$ is the last variable tested on $p$. Let $P^{\prime} \subseteq P$ be the set of these paths. The set $V$ consists of all nodes $v$ on a path from $P^{\prime}$ labeled by $x_{i, j}$. Now we consider all subpaths from the paths in $P^{\prime}$ to a node $v \in V$. Let $P^{\prime \prime}$ be the set of these paths. Obviously, $\left|P^{\prime \prime}\right|=\left|P^{\prime}\right|$. By the definition of $P$ and the fact that $x_{i, j}$ is set to 1 on all paths in $P^{\prime}$, we know that $R_{p} \neq R_{p^{\prime}}$ or $C_{p} \neq C_{p^{\prime}}$ for two different paths $p, p^{\prime} \in P^{\prime \prime}$.

In the following let $a_{p}$ be the corresponding (partial) assignment of the variables tested on a path $p$. Let $v_{p}$ be a node that is reached by a path $p \in P^{\prime \prime}$. We distinguish two cases.

1) There are at least $\left|P^{\prime \prime}\right|^{2 / 3}$ paths in $P^{\prime \prime}$ such that for any two of them $X_{v_{p}}=X_{v_{p^{\prime}}}$ or
2) there are at least $\left|P^{\prime \prime}\right|^{1 / 3}$ paths in $P^{\prime \prime}$ such that for any two of them $p, p^{\prime}$, where $p \neq p^{\prime}, X_{v_{p}} \neq X_{v_{p^{\prime}}}$.

- We consider the first case. One of the following properties is true. There are at least $\left|P^{\prime \prime}\right|^{1 / 3}$ paths in $P^{\prime \prime}$ such that for any two of them $R_{p} \neq R_{p^{\prime}}$ or at least $\left|P^{\prime \prime}\right|^{1 / 3}$ paths such that $C_{p} \neq C_{p^{\prime}}$ for two different paths $p, p^{\prime} \in P^{\prime \prime}$.
- If there are at least $\left|P^{\prime \prime}\right|^{1 / 3}$ paths in $P^{\prime \prime}$ such that for any two of them $R_{p} \neq R_{p^{\prime}}$, let $\mathcal{A}_{v}$ be the set of the partial assignments which correspond to these paths. The variable $x_{i, j}$ is the label of the nodes reached by these paths. We choose for each $a_{p} \in \mathcal{A}_{v}$ a partial assignment $a_{p}^{R}$ of the variables in $X_{n} \backslash X_{v_{p}}$ such that ( $a_{p}, a_{p}^{R}$ ) corresponds to a permutation matrix and $x_{i, j}=1$. By the definition of $P^{\prime \prime}$ such an assignment $a_{p}^{R}$ exists. The function value $f\left(a_{p}, a_{p}^{R}\right)$ is 1 since in each row there exists exactly one 1-entry. For $a_{p^{\prime}} \in \mathcal{A}_{v}$, let $a_{p^{\prime}}^{R}$ be a partial assignment such that $\left(a_{p^{\prime}}, a_{p^{\prime}}^{R}\right)$ corresponds to a permutation matrix and $x_{i, j}=1$. For $\left(a_{p^{\prime}}, a_{p}^{R}\right), p^{\prime} \neq p$, there exists at least one row without a variable set to 1 . Since the number of ones in $X$ is $n, f\left(a_{p^{\prime}}, a_{p}^{R}\right)=0$. With the same arguments $f\left(a_{p}, a_{p^{\prime}}^{R}\right)=0$.
- If there are at least $\left|P^{\prime \prime}\right|^{1 / 3}$ paths in $P^{\prime \prime}$ such that for any two of them $C_{p} \neq C_{p^{\prime}}$, let $\mathcal{A}_{v}$ be the set of the partial assignments which correspond to these paths. Again $x_{i, j}$ is the label of the nodes reached by these paths. We choose for each $a_{p}$ a partial assignment $a_{p}^{C}$ which resembles $a_{p}^{R}$ but with the exception that $x_{i, j}$ is set to 0 . Since there are exactly $n-1$ columns with one 1 -entry and the number of 1 -entries is $n-1$ altogether, the function value $f\left(a_{p}, a_{p}^{C}\right)$ is 1 . For $\left(a_{p^{\prime}}, a_{p}^{C}\right)$, $p^{\prime} \in P^{\prime \prime}$ and $p^{\prime} \neq p$, there exist at least two columns without a variable set to 1 . Since the number of 1-entries in $X$ is $n-1$, we can conclude $f\left(a_{p^{\prime}}, a_{p}^{C}\right)=0$. With the same arguments $f\left(a_{p}, a_{p^{\prime}}^{C}\right)=0$.
Altogether, we have proved that there exists a strong 1-fooling set of size at least $\left|P^{\prime \prime}\right|^{1 / 3}$.
- Now we consider the case that for at least $\left|P^{\prime \prime}\right|^{1 / 3}$ paths in $P^{\prime \prime}$ for any two of them $X_{v_{p}} \neq X_{v_{p^{\prime}}}, p \neq p^{\prime}$. For each path $p$ we consider a partial assignments $a_{p}^{C}$ of the variables in $X_{n} \backslash X_{v_{p}}$. As a first step we consider a partial assignment $a_{p}^{R}$ such that $\left(a_{p}, a_{p}^{R}\right)$ corresponds to a permutation matrix and $x_{i, j}=1$. By the definition of $P^{\prime \prime}$ such an assignment $a_{p}^{R}$ exists. Now the partial assignment $a_{p}^{C}$ resembles $a_{p}^{R}$ but with the exception that $x_{i, j}$ is set to 0 . Obviously, $f\left(a_{p}, a_{p}^{C}\right)=1$. For each of the chosen assignments, we consider one accepting path in $G$. Each of these accepting paths has
length of at least $n^{2}-1$ and the variable $x_{i, j}$ is the only variable that can be left out. We define a cut in $G$ through all these accepting paths after exactly $n / 2$ variables are set to 1 . The variable $x_{i, j}$ cannot be tested on any of these paths, because $x_{i, j}$ is set to 0 and after the test of $x_{i, j}$ only $n / 2-1$ variables are set to 1 . Let $W$ be the set of nodes reached for one of these accepting paths. If $p$ is one of the chosen paths in $P^{\prime \prime}$ and $v$ is the node in $G_{0}$ that is reached by $p$, then there exist a node $w \in W$ such that $X_{w}=X_{v_{p}}$ and $S_{w}=X_{n} \backslash X_{v_{p}}$ or $S_{w}=X_{n} \backslash\left(X_{v_{p}} \cup\left\{x_{i, j}\right\}\right)$. By assumption the sets $X_{v_{p}}$ for the chosen paths in $P^{\prime \prime}$ are all different, therefore, there are at least $\left|P^{\prime \prime}\right|^{1 / 3}$ nodes in $W$.
Summarizing we can conclude that the nondeterministic BP1 complexity is at least $\left.\left|P^{\prime \prime}\right|^{1 / 3}=\left(|P| / n^{2}\right)^{1 / 3}=\binom{n}{n / 2+1} / n^{2}\right)^{1 / 3}=2^{\Omega(n)}$.


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