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Oracles Are Subtle But Not Malicious

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Abstract

Theoretical computer scientists have been debating the role of oracles since the 1970's. This paper illustrates both that oracles can give us nontrivial insights about the barrier problems in circuit complexity, and that they need not prevent us from trying to solve those problems.

First, we give an oracle relative to which PP has linear-sized circuits, by proving a new lower bound for perceptrons and low-degree threshold polynomials. This oracle settles a longstanding open question, and generalizes earlier results due to Beigel and to Buhrman, Fortnow, and Thierauf. More importantly, it implies *the first nonrelativizing separation of "traditional" complexity classes*, as opposed to interactive proof classes such as MIP and $\mathsf{MA_{EXP}}$. For Vinodchandran showed, by a nonrelativizing argument, that PP does not have circuits of size n^k for any fixed k. We present an alternative proof of this fact, which shows that PP does not even have quantum circuits of size n^k with quantum advice. To our knowledge, this is the first nontrivial lower bound on quantum circuit size.

Second, we study a beautiful algorithm of Bshouty et al. for learning Boolean circuits in $\mathsf{ZPP^{NP}}$. We show that the NP queries in this algorithm cannot be parallelized by any relativizing technique, by giving an oracle relative to which $\mathsf{ZPP}_{||}^{\mathsf{NP}}$ and even $\mathsf{BPP}_{||}^{\mathsf{NP}}$ have linear-size circuits. On the other hand, we also show that the NP queries *could* be parallelized if $\mathsf{P} = \mathsf{NP}$. Thus, classes such as $\mathsf{ZPP}_{||}^{\mathsf{NP}}$ inhabit a "twilight zone," where we need to distinguish between relativizing and black-box techniques. Our results on this subject have implications for computational learning theory as well as for the circuit minimization problem.

1 Introduction

It is often lamented that, half a century after Shannon's insight [30] that almost all Boolean functions require exponential-size circuits, there is still no explicit function for which we can prove even a superlinear lower bound. Yet whether this lament is justified depends on what we mean by "explicit." For in 1982, Kannan [18] did show that for every constant k, there exists a language in Σ_2^p (the second level of the polynomial hierarchy) that does not have circuits of size n^k. His proof used the oldest trick in the book: *diagonalization*, defined broadly as any method for simulating all machines in one class by a single machine in another. In some sense, diagonalization is still the only method we know that zeroes in on a specific property of the function being lower-bounded, and thereby escapes the jaws of Razborov and Rudich [27].

But can we generalize Kannan's theorem to other complexity classes? A decade ago, Bshouty et al. [8] discovered an algorithm to learn Boolean circuits in $\mathsf{ZPP^{NP}}$ (that is, probabilistic polynomial time with NP oracle). As noticed by Köbler and Watanabe [20], the existence of this algorithm implies that $\mathsf{ZPP^{NP}}$ itself cannot have circuits of size n^k for any k.[1]

So our task as lowerboundsmen and lowerboundswomen seems straightforward: namely, to find increasingly powerful algorithms for learning Boolean circuits, which can then be turned around to yield increasingly powerful circuit lower bounds. But when we try to do this, we quickly run into the brick wall of *relativization*.

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[1] For Bshouty et al.'s algorithm implies the following improvement to the celebrated Karp-Lipton theorem [19]: if $\mathsf{NP} \subset \mathsf{P/poly}$ then PH collapses to $\mathsf{ZPP^{NP}}$. There are then two cases: if $\mathsf{NP} \not\subset \mathsf{P/poly}$, then certainly $\mathsf{ZPP^{NP}} \not\subset \mathsf{P/poly}$ as well and we are done. On the other hand, if $\mathsf{NP} \subset \mathsf{P/poly}$, then $\mathsf{ZPP^{NP}} = \mathsf{PH}$, but we already know from Kannan's theorem that PH does not have circuits of size n^k. Indeed, we can repeat this argument for the class S_2^p, which Cai [11] showed is contained in $\mathsf{ZPP^{NP}}$.

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Just as Baker, Gill, and Solovay [6] gave a relativized world where $\mathsf{P} = \mathsf{NP}$, so Wilson [40] gave relativized worlds where NP and P^{NP} have linear-size circuits. Since the results of Kannan [18] and Bshouty et al. [8] relativize, this suggests that new techniques will be needed to make further progress.

Yet attitudes toward relativization vary greatly within our community. Some computer scientists ridicule oracle results as elaborate formalizations of the obvious—apparently believing that (1) there exist relativized worlds where just about anything is true, (2) the creation of such worlds is a routine exercise, (3) the only conjectures ruled out by oracle results are trivially false ones, which no serious researcher would waste time trying to prove, and (4) nonrelativizing results such as $\mathsf{IP} = \mathsf{PSPACE}$ [29] render oracles irrelevant anyway. At the other extreme, some computer scientists see oracle results not as a spur to create nonrelativizing techniques or as a guide to where such techniques might be needed, but as an excuse to abandon hope.

This paper will offer new counterexamples to both of these views, in the context of circuit lower bounds. We focus on two related topics: first, the classical and quantum circuit complexity of PP; and second, the learnability of Boolean circuits using parallel NP queries.

1.1 On PP and Quantum Circuits

In Section 2, we give an oracle relative to which PP has linear-size circuits. Here PP is the class of languages accepted by a nondeterministic polynomial-time Turing machine that accepts and if and only if the majority of its paths do. Our construction also yields an oracle relative to which PEXP (the exponential-time version of PP) has polynomial-size circuits, and indeed $\mathsf{P}^{\mathsf{NP}} = \oplus\mathsf{P} = \mathsf{PEXP}$. This settles several questions that were open for years,[2] and subsumes at least three previous results:

(1) that of Beigel [7] giving an oracle relative to which $\mathsf{P}^{\mathsf{NP}} \not\subset \mathsf{PP}$ (since clearly $\mathsf{P}^{\mathsf{NP}} = \mathsf{PEXP}$ implies $\mathsf{P}^{\mathsf{NP}} \not\subset \mathsf{PP}$),

(2) that of Buhrman, Fortnow, and Thierauf [10] giving an oracle relative to which $\mathsf{MA}_{\mathsf{EXP}} \subset \mathsf{P/poly}$, and

(3) that of Buhrman et al. [9] giving an oracle relative to which $\mathsf{P}^{\mathsf{NP}} = \mathsf{NEXP}$.

Note that our result is nearly optimal, since Toda's theorem [35] yields a relativizing proof that P^{PP} and even $\mathsf{BP} \cdot \mathsf{PP}$ do not have circuits of any fixed polynomial size.

Our proof first represents each PP machine by a low-degree multilinear polynomial, whose variables are the bits of the oracle string. It then combines these polynomials into a single polynomial called Q. The key fact is that, if there are no variables left "unmonitored" by the component polynomials, then we can modify the oracle in a way that increases Q. Since Q can only increase a finite number of times, it follows that we will eventually win our "war of attrition" against the polynomials, at which point we can simply write down what each machine does in an unmonitored part of the oracle string. The main novelty of the proof lies in how we combine the polynomials to create Q.

We can state our result alternatively in terms of *perceptrons* [24], also known as threshold-of-AND circuits or polynomial threshold functions. Call a perceptron "small" if it has size $2^{n^{o(1)}}$, order $n^{o(1)}$, and weights in $\{-1, 1\}$. Also, given an n-bit string $x_1 \ldots x_n$, recall that the ODDMAXBIT problem is to decide whether the maximum i such that $x_i = 1$ is even or odd, promised that such an i exists. Then Beigel [7] showed that no small perceptron can solve ODDMAXBIT. What we show is a strong generalization of Beigel's theorem: for any $k = n^{o(1)}$ small perceptrons, there exists a "problem set" consisting of k ODDMAXBIT instances, such that for every i, the i^{th} perceptron will get the i^{th} problem wrong even if it can examine the whole problem set. Previously this had been open even for $k = 2$.

But the real motivation for our result is that in the unrelativized world, PP is known *not* to have linear-size circuits. Indeed, Vinodchandran [39] showed that for every k, there exists a language in PP that does not have circuits of size n^k. As a consequence, we obtain *the first nonrelativizing separation that does not involve artificial classes or classes defined using interactive proofs*. There have been nonrelativizing separations in the past, but most of them have followed easily from the collapse of interactive proof classes: for example,

[2] Lance Fortnow, personal communication.

$\mathsf{NP} \neq \mathsf{MIP}$ from $\mathsf{MIP} = \mathsf{NEXP}$ [5], and $\mathsf{IP} \not\subset \mathsf{SIZE}\left(n^k\right)$ from $\mathsf{IP} = \mathsf{PSPACE}$ [29]. The one exception was the result of Buhrman, Fortnow, and Thierauf [10] that $\mathsf{MA_{EXP}} \not\subset \mathsf{P/poly}$, where $\mathsf{MA_{EXP}}$ is the exponential-time version of MA. However, the class $\mathsf{MA_{EXP}}$ exists for the specific purpose of not being contained in $\mathsf{P/poly}$, and the resulting separation does not scale down below NEXP, to show (for example) that MA does not have linear-size circuits. By contrast, PP is one of the most natural complexity classes there is. That is why, in our opinion, our result adds some heft to the idea that *currently-understood nonrelativizing techniques can lead to progress on the fundamental questions of complexity theory.*

The actual lower bound of Vinodchandran [39] follows easily from three well-known results: the LFKN interactive protocol for the permanent [22], Toda's theorem [35], and Kannan's theorem [18].[3] In Section 3, we present an alternative, more self-contained proof, which does not go through Toda's theorem. As a bonus, our proof also shows that PP does not have *quantum* circuits of size n^k for any k. Indeed, this remains true even if the quantum circuits are given "quantum advice states" on n^k qubits, which might require exponential time to prepare. One part of our proof is a "quantum Karp-Lipton theorem," which states that if PP has polynomial-size quantum circuits, then the "counting hierarchy" (consisting of PP, $\mathsf{PP^{PP}}$, $\mathsf{PP^{PP^{PP}}}$, and so on) collapses to QMA, the quantum analogue of NP. By analogy to the classical nonrelativizing separation of Buhrman, Fortnow, and Thierauf [10], we also show that $\mathsf{QMA_{EXP}}$, the exponential-time version of QMA, is not contained in $\mathsf{BQP/qpoly}$. Indeed, $\mathsf{QMA_{EXP}}$ requires quantum circuits of at least "half-exponential" size, meaning size $f(n)$ where $f(f(n))$ grows exponentially.[4]

While none of the results in Section 3 are really difficult, we include them here for three reasons:

(1) So far as we know, the only existing lower bounds for arbitrary quantum circuits are due to Nishimura and Yamakami [26], who showed (among other things) that $\mathsf{EESPACE} \not\subset \mathsf{BQP/qpoly}$.[5] We felt it worthwhile to point out that much better bounds are possible.

(2) When it comes to understanding the limitations of quantum computers, our knowledge to date consists almost entirely of oracle lower bounds. Many researchers have told us that they would much prefer to see some unrelativized results, or at the very least conditional statements—for example, "if NP-complete problems are solvable in quantum polynomial time, then the polynomial hierarchy collapses." The results of Section 3 represent a first step in that direction.

(3) Recently Aaronson [2] gave a new characterization of PP, as the class of problems solvable in quantum polynomial time, given the ability to *postselect* (that is, to discard all runs of the computation in which a given measurement result does not occur). If we replace "quantum" by "randomized" in this definition, then we obtain a classical complexity class called $\mathsf{BPP_{path}}$, which was introduced by Han, Hemaspaandra, and Thierauf [16]. So the fact that we can prove a quantum circuit lower bound for PP implies one of two things: either that (i) we can prove a nonrelativizing quantum separation theorem, but *not* the classical analogue of the same theorem, or that (ii) we should be able to prove classical circuit lower bound for $\mathsf{BPP_{path}}$. As we will see later, the latter possibility would be a significant breakthrough.

1.2 On Parallel NP Queries and Black-Box Learning

In a second part of the paper, we study the learning algorithm of Bshouty et al. [8] mentioned earlier. Given a Boolean function f that is promised to have a polynomial-size circuit, this algorithm *finds* such a circuit in the class $\mathsf{ZPP^{NP^f}}$: that is, zero-error probabilistic polynomial time with NP oracle with oracle for f. One of the most basic questions about this algorithm is whether the NP queries can be made nonadaptive. For if so, then we immediately obtain a new circuit lower bound: namely that $\mathsf{ZPP}_{||}^{\mathsf{NP}}$ (that is, ZPP with parallel

[3] Suppose by contradiction that PP has circuits of size n^k. Then $\mathsf{P^{\#P}} \subset \mathsf{P/poly}$, and therefore $\mathsf{MA} = \mathsf{PP} = \mathsf{P^{\#P}}$ by a result of LFKN [22] (this is the only part of the proof that fails to relativize). Now $\mathsf{MA} \subseteq \mathbf{\Sigma}_2^p \subseteq \mathsf{P^{\#P}}$ by Toda's theorem [35], so $\mathbf{\Sigma}_2^p = \mathsf{PP}$ as well. But we already know from Kannan's theorem [18] that $\mathbf{\Sigma}_2^p$ does not have circuits of size n^k.

[4] See Miltersen, Vinodchandran, and Watanabe [23] for a discussion of this concept.

[5] A similar bound is implicit in a paper by Stockmeyer and Meyer [34].

NP queries) does not have circuits of size n^k for any k.[6] Conceptually, this would not be so far from showing that NP itself does not have circuits of size n^k.[7]

Let $\mathcal{C}$ be the set of circuits of size n^k. In Bshouty et al.'s algorithm, we repeatedly ask the NP oracle to find us an input x_t such that, *among the circuits in* $\mathcal{C}$ *that succeed on all previous inputs* $x_1, \ldots, x_{t-1}$, at least a $1/3$ fraction fail on x_t. Since each such input reduces the number of circuits "still in the running" by at least a constant factor, this process can continue for at most $\log |\mathcal{C}|$ steps. Furthermore, when it ends, by assumption we have a set $\mathcal{C}^*$ of circuits such that for all inputs x, a uniform random circuit drawn from $\mathcal{C}^*$ will succeed on x with probability at least $2/3$. So now all we have to do is sample a polynomial number of circuits from $\mathcal{C}^*$, then generate a new circuit that outputs the majority answer among the sampled circuits. The technical part is to express the concepts "at least a $1/3$ fraction" and "a uniform random sample" in NP. For that Bshouty et al. use pairwise-independent hash functions.

When we examine the above algorithm, it is far from obvious that adaptive NP queries are necessary. For why can't we simply ask the following question in parallel, for all $T \leq \log |\mathcal{C}|$?

> "Do there exist inputs $x_1, \ldots, x_T$, such that at least a $1/3$ fraction of circuits in $\mathcal{C}$ fail on x_1, and among the circuits that succeed on x_1, at least a $1/3$ fraction fail on x_2, and among the circuits that succeed on x_1 and x_2, at least a $1/3$ fraction fail on x_3, ... and so on up to x_T?"

By making clever use of hashing and approximate counting, perhaps we could control the number of circuits that succeed on $x_1, \ldots, x_t$ for all $t \leq T$. In that case, by finding the largest T such that the above question returns a positive answer, and then applying the Valiant-Vazirani reduction [38] and other standard techniques, we would achieve the desired parallelization of Bshouty et al.'s algorithm. Indeed, when we began studying the topic, it seemed entirely likely to us that this was possible.

Nevertheless, in Section 4 we give an oracle relative to which $\mathsf{ZPP}_{||}^{\mathsf{NP}}$ and even $\mathsf{BPP}_{||}^{\mathsf{NP}}$ have linear-size circuits. The overall strategy of our oracle construction is the same as for PP, but the details are somewhat less elegant. The existence of this oracle means that any parallelization of Bshouty et al.'s algorithm will need to use nonrelativizing techniques.

Yet even here, the truth is subtler than one might imagine. To explain why, we need to distinguish carefully between *relativizing* and *black-box* algorithms. An algorithm for learning Boolean circuits is *relativizing* if, when given access to an oracle A, the algorithm can learn circuits that are also given access to A. But a nonrelativizing algorithm can still be *black-box*, in the sense that it learns about the target function f only by querying it, and does not exploit any succinct description of f (for example, that $f(x) = 1$ if and only if x encodes a satisfiable Boolean formula). Bshouty et al.'s algorithm is both relativizing *and* black-box. What our oracle construction shows is that no relativizing algorithm can learn Boolean circuits in $\mathsf{BPP}_{||}^{\mathsf{NP}}$. But what about a nonrelativizing yet still black-box algorithm?

Surprisingly, we show in Section 5 that if $\mathsf{P} = \mathsf{NP}$, then there *is* a black-box algorithm to learn Boolean circuits even in $\mathsf{P}_{||}^{\mathsf{NP}}$ (as well as in NP/log). Despite the outlandishness of the premise, this theorem is not trivial, and requires many of the same techniques originally used by Bshouty et al. [8]. One way to interpret the theorem is that we cannot show the *impossibility* of black-box learning in $\mathsf{P}_{||}^{\mathsf{NP}}$, without also showing that $\mathsf{P} \neq \mathsf{NP}$. By contrast, it is easy to show that black-box learning is impossible in NP, regardless of what computational assumptions we make.[8]

These results provide a new perspective on one of the oldest problems in computer science, the *circuit minimization problem*: given a Boolean circuit C, does there exist an equivalent circuit of size at most s? Certainly this problem is NP-hard and in Σ_2^p. Also, by using Bshouty et al.'s algorithm, we can find a circuit whose size is within an $O(n/\log n)$ factor of minimal in $\mathsf{ZPP}^{\mathsf{NP}}$. Yet after fifty years of research,

[6] This follows from the same reasoning used by Köbler and Watanabe [20] to show that $\mathsf{ZPP}^{\mathsf{NP}}$ does not have circuits of size n^k. For such an algorithm would readily imply that if $\mathsf{NP} \subset \mathsf{P/poly}$, then PH collapses to $\mathsf{ZPP}_{||}^{\mathsf{NP}}$.

[7] For as observed by Shaltiel and Umans [28] and Fortnow and Klivans [13] among others, there is an intimate connection between the classes $\mathsf{P}_{||}^{\mathsf{NP}}$ and NP/log. Furthermore, any circuit lower bound for NP/log implies the same lower bound for NP, since we can tack the advice onto the input.

[8] Note that by "learn," we always mean "learn exactly" rather than "PAC-learn." Of course, if $\mathsf{P} = \mathsf{NP}$, then *approximate* learning of Boolean circuits could be done in polynomial time.

almost nothing else is known about the complexity of this problem. For example, is it Σ_2^p-complete? Can we approximate the minimum circuit size in $\mathsf{ZPP}_{||}^{\mathsf{NP}}$?

What our techniques let us say is the following. First, there exists an oracle A such that minimizing circuits with oracle access to A is not even approximable in $\mathsf{BPP}_{||}^{\mathsf{NP}^A}$. Indeed, any probabilistic algorithm to distinguish the cases "C is minimal" and "there exists an equivalent circuit for C of size s," using fewer than s adaptive NP queries, would have to use nonrelativizing techniques. If one wished, one could take this as evidence that the true complexity of the circuit minimization problem should be P^{NP} rather than $\mathsf{P}_{||}^{\mathsf{NP}}$. On the other hand, one cannot rule out even a "black-box" circuit minimization algorithm (that is, an algorithm that treats C itself as an oracle) in $\mathsf{P}_{||}^{\mathsf{NP}}$, without also showing that $\mathsf{P} \neq \mathsf{NP}$.

From a learning theory perspective, perhaps what is most interesting about our results is that they show a clear tradeoff between two complexities: the complexity of the learner who queries the target function f, and the complexity of the resulting computational problem that the learner has to solve. If the learner is a $\mathsf{ZPP}^{\mathsf{NP}^f}$ machine, then the problem is easy; if the learner is a $\mathsf{ZPP}_{||}^{\mathsf{NP}^f}$ machine, then the problem is (probably) hard; and if the learner is an NP^f machine, then there is *no* computational problem whose solution would suffice to learn f.

1.3 Outlook

Figure 1 shows the "battle map" for nonrelativizing circuit lower bounds that emerges from this paper. The figure displays not one but two barriers: a "relativization barrier," below which any Karp-Lipton collapse or superlinear circuit size lower bound will need to use nonrelativizing techniques; and a "black-box barrier," below which black-box learning even of unrelativized circuits is provably impossible. At least for the thirteen complexity classes shown in the figure, we now know exactly where to draw these two barriers—something that would have been less than obvious *a priori* (at least to us!).

To switch metaphors, we can think of the barriers as representing "phase transitions" in the behavior of complexity classes. Below the black-box barrier, we cannot learn circuits relative to any oracle A. Between the relativization and black-box barriers, we can learn Boolean circuits relative to *some* oracles A but not others. For example, we can learn relative to a PSPACE oracle, since it collapses P and NP, but we cannot learn relative to the oracles in this paper, which cause PP and $\mathsf{BPP}_{||}^{\mathsf{NP}}$ to have linear-size circuits. Finally, above the relativization barrier, we can learn Boolean circuits relative to *every* oracle A.[9] As we move upward from the black-box barrier toward the relativization barrier, we can notice "steam bubbles" starting to form, as the assumptions needed for black-box learning shift from implausible ($\mathsf{P} = \mathsf{NP}$), to plausible (the standard derandomization assumptions that collapse P^{NP} with $\mathsf{ZPP}^{\mathsf{NP}}$ and PP with $\mathsf{BP} \cdot \mathsf{PP}$), and finally to no assumptions at all.

To switch metaphors again, the oracle results have laid before us a rich and detailed landscape, which a nonrelativizing Lewis-and-Clark expedition might someday visit more fully.

2 The Oracle for PP

In this section we construct an oracle relative to which PP has linear-size circuits. To do so, we will need a lemma about multilinear polynomials, which follows from the well-known lower bound of Nisan and Szegedy [25] on the approximate degree of the OR function.

Lemma 1 (Nisan-Szegedy) *Let $p : \{0,1\}^N \to \mathbb{R}$ be a real multilinear polynomial of degree at most $\sqrt{N}/7$, and suppose that $|p(X)| \leq \frac{2}{3}\left|p\left(0^N\right)\right|$ for all $X \in \{0,1\}^N$ with Hamming weight 1. Then there exists an $X \in \{0,1\}^N$ such that $|p(X)| \geq 6\,|p(0^n)|$.*

[9] There is one important caveat: in S_2^p, we currently only know how to learn self-reducible functions, such as the characteristic functions of NP-complete problems. For if the circuits from the two competing provers disagree with each other, then we need to know which one to trust.

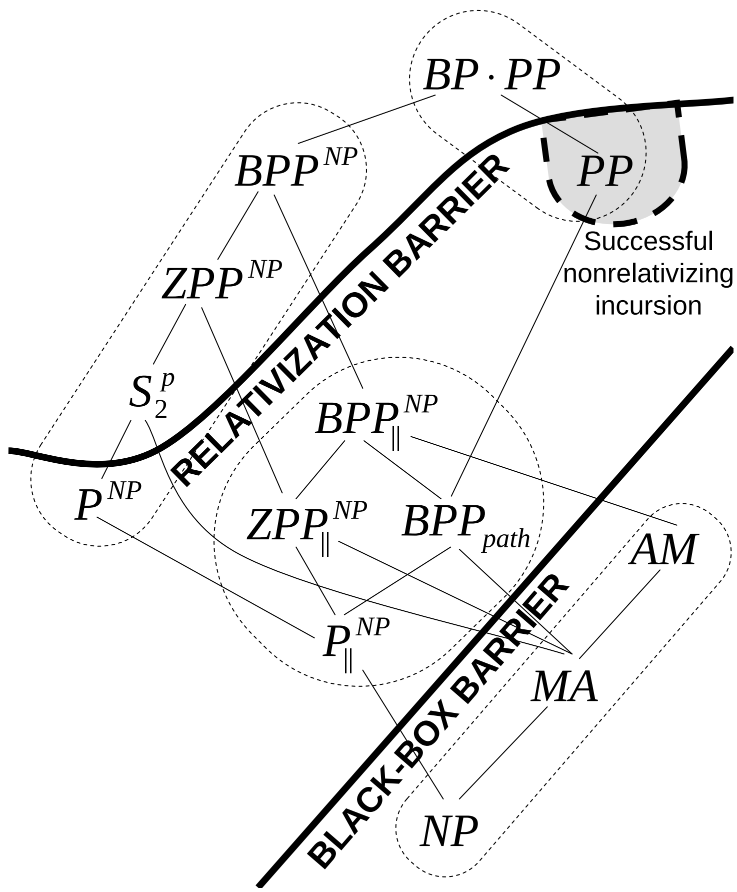

Figure 1: "Battle map" of some complexity classes between NP and BP · PP, in light of this paper's results. Classes that coincide under a plausible derandomization assumption are grouped together with dashed ovals. Below the relativization barrier, we must use nonrelativizing techniques to show any Karp-Lipton collapse or superlinear circuit size lower bound. Below the black-box barrier, black-box learning of Boolean circuits is provably impossible.

We now prove the main result.

Theorem 2 *There exists an oracle relative to which* PP *has linear-size circuits.*

Proof. For simplicity, we first give an oracle that works for a specific value of n, and then generalize to all n simultaneously. Let $M_1, M_2, \ldots$ be an enumeration of $\mathsf{PTIME}\left(n^{\log n}\right)$ machines. Then it suffices to simulate $M_1, \ldots, M_n$, for in that case every M_i will be simulated on all but finitely many n.

The oracle A will consist of 2^{5n} "rows" and $n2^n$ "columns," with each row labeled by a string $r \in \{0,1\}^{5n}$, and each column labeled by a pair $\langle i, x\rangle$ where $i \in \{1, \ldots, n\}$ and $x \in \{0,1\}^n$. Then given a triple $\langle r, i, x\rangle$ as input, A will return the bit $A(r, i, x)$.

We will construct A via an iterative procedure. Initially A is empty (that is, $A(r,i,x) = 0$ for all r, i, x). Let A_t be the state of A after the t^{th} iteration. Also, let $M_{i,x}(A)$ be a Boolean function that equals 1 if M_i accepts on input $x \in \{0,1\}^n$ and oracle string A, and 0 otherwise. Then to *encode* a row r means to set $A_t(r,i,x) := M_{i,x}(A_{t-1})$ for all i, x. At a high level, our entire procedure will consist of repeating the following two steps, for all $t \geq 1$:

(1) Choose a set of rows $S \subseteq \{0,1\}^{5n}$ of A_{t-1}.

(2) Encode each $r \in S$, and let A_t be the result.

The problem, of course, is that each time we encode a row r, the $M_{i,x}(A)$'s might change as a result. So we need to show that, by carefully implementing step (1), we can guarantee that the following condition holds after a finite number of steps.

($\mathcal{C}$) There exists an r such that $A(r,i,x) = M_{i,x}(A)$ for all i, x.

If ($\mathcal{C}$) is satisfied, then clearly $M_1, \ldots, M_n$ will have linear-size circuits relative to A, since we can just hardwire r into the circuits.

We will use the following fact, which is immediate from the definition of PP. For all i, x, there exists a multilinear polynomial $p_{i,x}(A)$, whose variables are the bits of A, such that:

(i) If $M_{i,x}(A) = 1$ then $p_{i,x}(A) \geq 1$.

(ii) If $M_{i,x}(A) = 0$ then $p_{i,x}(A) \leq -1$.

(iii) $p_{i,x}$ has degree at most $n^{\log n}$.

(iv) $|p_{i,x}(A)| \leq 2^{n^{\log n}}$ for all A.

Now for all integers $0 \leq k \leq n^{\log n}$ and $b \in \{0,1\}$, let

$$q_{i,x,b,k}(A) = 2^{2k-3} + \left(2^k + (-1)^b p_{i,x}(A)\right)^2.$$

Then we will use the following polynomial as a progress measure:

$$Q(A) = \prod_{i,x} \prod_{b \in \{0,1\}} \prod_{k=0}^{n^{\log n}} q_{i,x,b,k}(A).$$

Notice that

$$\deg(Q) \leq n2^n \cdot 2 \cdot \left(n^{\log n} + 1\right) \cdot 2 \deg(p_{i,x}) = 2^{n+o(n)}.$$

Since $1/8 \leq q_{i,x,b,k}(A) \leq 5 \cdot 2^{2n^{\log n}}$ for all i, x, b, k, we also have

$$Q(A) \leq \left(5 \cdot 2^{2n^{\log n}}\right)^{n2^n \cdot 2 \cdot \left(n^{\log n}+1\right)} = 2^{2^{n+o(n)}},$$

$$Q(A) \geq \left(\frac{1}{8}\right)^{n2^n \cdot 2 \cdot \left(n^{\log n}+1\right)} = 2^{-2^{n+o(n)}}$$

for all A. The key claim is the following.

At any given iteration, suppose there is no r such that, by encoding r, we can satisfy condition ($\mathcal{C}$). Then there exists a set $S \subseteq \{0,1\}^{5n}$ such that, by encoding each $r \in S$, we can increase $Q(A)$ by at least a factor of 2 (that is, ensure that $Q(A_t) \geq 2Q(A_{t-1})$).

The above claim readily implies that ($\mathcal{C}$) can be satisfied after a finite number of steps. For, by what was said previously, $Q(A)$ can double at most $2^{n+o(n)}$ times—and once $Q(A)$ can no longer double, by assumption we can encode an r that satisfies ($\mathcal{C}$). (As a side note, "running out of rows" is not an issue here, since we can re-encode rows that were encoded in previous iterations.)

We now prove the claim. Call the pair $\langle i, x\rangle$ *sensitive* to row r if encoding r would change the value of $M_{i,x}(A)$. By hypothesis, for every r there exists an $\langle i, x\rangle$ that is sensitive to r. So by a counting argument, there exists a single $\langle i, x\rangle$ that is sensitive to at least $2^{5n}/(n2^n) > 2^{3n}$ rows. Fix that $\langle i, x\rangle$, and let $r_1, \ldots, r_{2^{3n}}$ be the first 2^{3n} rows to which $\langle i, x\rangle$ is sensitive. Also, given a binary string $Y = y_1 \ldots y_{2^{3n}}$, let $S(Y)$ be the set of all r_j such that $y_j = 1$, and let $A^{(Y)}$ be the oracle obtained by starting from A and then encoding each $r_j \in S(Y)$.

Set b equal to $M_{i,x}(A)$, and set k equal to the least integer such that $2^k \geq |p_{i,x}(A)|$. Then we will think of $Q(A)$ as the product of two polynomials $q(A)$ and $v(A)$, where $q(A) = q_{i,x,b,k}(A)$, and $v(A) = Q(A)/q(A)$ is the product of all other terms in $Q(A)$. Notice that $q(A) > 0$ and $v(A) > 0$ for all A. Also,

$$\begin{aligned} q(A) &= 2^{2k-3} + \left(2^k + (-1)^b p_{i,x}(A)\right)^2 \\ &\leq 2^{2k-3} + \left(2^k - 2^{k-1}\right)^2 \\ &= \frac{3}{8} \cdot 2^{2k}. \end{aligned}$$

Here the second line follows since $-2^k \leq (-1)^b p_{i,x}(A) \leq -2^{k-1}$. On the other hand, for all $Y \in \{0,1\}^{2^{3n}}$ with Hamming weight 1, we have $(-1)^b p_{i,x}(A) \geq 0$, and therefore

$$\begin{aligned} q\left(A^{(Y)}\right) &= 2^{2k-3} + \left(2^k + (-1)^b p_{i,x}\left(A^{(Y)}\right)\right)^2 \\ &\geq 2^{2k-3} + \left(2^k\right)^2 \\ &= \frac{9}{8} \cdot 2^{2k} \\ &\geq 3q(A). \end{aligned}$$

There are now two cases. The first is that there exists a Y with Hamming weight 1 such that $v\left(A^{(Y)}\right) \geq \frac{2}{3} v(A)$. In this case

$$\begin{aligned} Q\left(A^{(Y)}\right) &= q\left(A^{(Y)}\right) v\left(A^{(Y)}\right) \\ &\geq 3q(A) \cdot \frac{2}{3} v(A) \\ &= 2q(A)\, v(A) \\ &= 2Q(A). \end{aligned}$$

So we simply set $S = S(Y)$ and are done.

The second case is that $v\left(A^{(Y)}\right) < \frac{2}{3} v(A)$ for all Y with Hamming weight 1. In this case, we can consider v as a real multilinear polynomial in the bits of $Y \in \{0,1\}^{2^{3n}}$, of degree at most $\deg(Q) < \sqrt{2^{3n}}/7$. Then Lemma 1 implies that there exists a $Y \in \{0,1\}^{2^{3n}}$ such that $\left|v\left(A^{(Y)}\right)\right| = v\left(A^{(Y)}\right) \geq 6v(A)$. Furthermore, for all Y we have

$$\frac{q\left(A^{(Y)}\right)}{q(A)} \geq \frac{2^{2k-3}}{\frac{3}{8} \cdot 2^{2k}} = \frac{1}{3}.$$

Hence

$$
\begin{aligned}
Q\left(A^{(Y)}\right) &= q\left(A^{(Y)}\right) v\left(A^{(Y)}\right) \\
&\geq \frac{1}{3} q(A) \cdot 6v(A) \\
&= 2q(A)\, v(A) \\
&= 2Q(A).
\end{aligned}
$$

So again we can set $S = S(Y)$. This completes the claim.

All that remains is to handle $\mathsf{PTIME}\left(n^{\log n}\right)$ machines that could query *any* bit of the oracle string, rather than just the bits corresponding to a specific n. The oracle A will now take as input a *list* of strings $R = (r_1, \ldots, r_\ell)$, with $r_\ell \in \{0,1\}^{5 \cdot 2^\ell}$ for all ℓ, in addition to i, x. Call R an ℓ*-secret* if $A(R, i, x) = M_{i,x}(A)$ for all $n \leq 2^\ell$, $i \in \{1, \ldots, n\}$, and $x \in \{0,1\}^n$. Then we will try to satisfy the following.

($\mathcal{C}'$) There exists an infinite list of strings $r_1^*, r_2^*, \ldots,$, such that $R_\ell^* := (r_1^*, \ldots, r_\ell^*)$ is an ℓ-secret for all $\ell \geq 1$.

If ($\mathcal{C}'$) is satisfied, then clearly each M_i can be simulated by linear-size circuits. For all $n \geq i$, simply find the smallest ℓ such that $2^\ell \geq n$, then hardwire R_ℓ^* into the circuit for size n. Since $\ell \leq 2n$, this requires at most $5\left(2^1 + \cdots + 2^\ell\right) \leq 20n$ bits.

To construct an oracle A that satisfies ($\mathcal{C}'$), we iterate over all $\ell \geq 1$. Suppose by induction that $R_{\ell-1}^*$ is an $(\ell-1)$-secret; then we want to ensure that R_ℓ^* is an ℓ-secret for some $r_\ell \in \{0,1\}^{5 \cdot 2^\ell}$. To do so, we use a procedure essentially identical to the one for a specific n. The only difference is this: previously, all we needed was a row $r \in \{0,1\}^{5n}$ such that no $\langle i, x\rangle$ pair was sensitive to a *particular* change to r (namely, setting $A_t(r, i, x) := M_{i,x}(A_{t-1})$ for all i, x). But in the general case, the "row" labeled by $R = (r_1, \ldots, r_\ell)$ consists of all triples $\langle R', i, x\rangle$ such that $R' = \left(r_1, \ldots, r_\ell, r'_{\ell+1}, \ldots, r'_L\right)$ for some $L \geq \ell$ and $r'_{\ell+1}, \ldots, r'_L$. Furthermore, we do not yet know how later iterations will affect this "row." So we should call a pair $\langle i, x\rangle$ "sensitive" to R, if there is *any* oracle A' such that (1) A' disagrees with A only in row R, and (2) $M_{i,x}(A') \neq M_{i,x}(A)$.

Fortunately, this new notion of sensitivity requires no significant change to the proof. Suppose that for every row R of the form $\left(r_1^*, \ldots, r_{\ell-1}^*, r_\ell\right)$ there exists an $\langle i, x\rangle$ that is sensitive to R. Then as before, there exists an $\langle i', x'\rangle$ that is sensitive to at least $2^{5 \cdot 2^\ell} / \left(2^{2\ell} 2^{2^\ell + 1}\right) > 2^{3n}$ rows of that form. For each of those rows R, fix a change to R to which $\langle i', x'\rangle$ is sensitive. We thereby obtain a polynomial $Q(A)$ with the same properties as before—in particular, there exists a string $Y \in \{0,1\}^{2^{3n}}$ such that $Q\left(A^{(Y)}\right) \geq 2Q(A)$. ■

Let us make three remarks about Theorem 2.

(1) If we care about constants, it is clear that the advice r can be reduced to $3n + o(n)$ bits for a specific n, or $12n + o(n)$ for all n simultaneously. Presumably these bounds are not tight.

(2) One can easily extend Theorem 2 to give an oracle relative to which $\mathsf{PE} = \mathsf{PTIME}\left(2^{O(n)}\right)$ has linear-size circuits, and hence $\mathsf{PEXP} \subset \mathsf{P/poly}$ by a padding argument.

(3) Han, Hemaspaandra, and Thierauf [16] showed that $\mathsf{MA} \subseteq \mathsf{BPP_{path}} \subseteq \mathsf{PP}$. So in addition to implying the result of Buhrman, Fortnow, and Thierauf that MA has linear-size circuits relative to an oracle, Theorem 2 also yields the new result that $\mathsf{BPP_{path}}$ has linear-size circuits relative to an oracle.

Another application of our techniques, the construction of relativized worlds where $\mathsf{P^{NP}} = \mathsf{PEXP}$ and $\oplus\mathsf{P} = \mathsf{PEXP}$, is outlined in Appendix 8.

3 Quantum Circuit Lower Bounds

In this section we show, by a nonrelativizing argument, that PP does *not* have circuits of size n^k, not even quantum circuits with quantum advice. We first show that $\mathsf{P^{PP}}$ does not have quantum circuits of size n^k, by a direct diagonalization argument. Our argument will use the following lemma of Aaronson [1].

Lemma 3 ("Almost As Good As New Lemma") *Suppose a two-outcome measurement of a mixed quantum state* ρ *yields outcome* 0 *with probability* $1-\varepsilon$. *Then after the measurement, we can recover a state* $\widetilde{\rho}$ *such that* $\|\widetilde{\rho}-\rho\|_{\mathrm{tr}} \leq \sqrt{\varepsilon}$.

(Recall that the trace distance $\|\rho-\sigma\|_{\mathrm{tr}}$ between two mixed states ρ and σ is the maximum bias with which those states can be distinguished via a single measurement. In particular, trace distance satisfies the triangle inequality.)

Theorem 4 P^{PP} *does not have quantum circuits of size* n^k *for any fixed* k. *Furthermore, this holds even if the circuits can use quantum advice.*

Proof. For simplicity, let us first explain why P^{PP} does not have *classical* circuits of size n^k. Fix an input length n, and let $x_1,\ldots,x_{2^n}$ be a lexicographic ordering of n-bit strings. Also, let $\mathcal{C}$ be the set of all circuits of size n^k, and let $\mathcal{C}_t \subseteq \mathcal{C}$ be the subset of circuits in $\mathcal{C}$ that correctly decide the first t inputs $x_1,\ldots,x_t$. Then we define the language $L \cap \{0,1\}^n$ by the following iterative procedure. First, if at least half of the circuits in $\mathcal{C}$ accept x_1, then set $x_1 \notin L$, and otherwise set $x_1 \in L$. Next, if at least half of the circuits in $\mathcal{C}_1$ accept x_2, then set $x_2 \notin L$, and otherwise set $x_2 \in L$. In general, let $N = \lceil \log_2 |\mathcal{C}'| \rceil + 1$. Then for all $t < N$, if at least half of the circuits in $\mathcal{C}_t$ accept x_{t+1}, then set $x_{t+1} \notin L$, and otherwise set $x_{t+1} \in L$. Finally, set $x_t \notin L$ for all $t > N$.

It is clear that the resulting language L is in P^{PP}. Given an input x_t, we just reject if $t > N$, and otherwise call the PP oracle t times, to decide if $x_i \in L$ for each $i \in \{1,\ldots,t\}$. Note that, once we know $x_1,\ldots,x_i$, we can decide in polynomial time whether a given circuit belongs to $\mathcal{C}_i$, and can therefore decide in PP whether the majority of circuits in $\mathcal{C}_i$ accept or reject x_{i+1}. On the other hand, our construction guarantees that $|\mathcal{C}_{t+1}| \leq |\mathcal{C}_t|/2$ for all $t < N$. Therefore $|\mathcal{C}_N| \leq |\mathcal{C}|/2^N = 1/2$, which means that $\mathcal{C}_N$ is empty, and hence no circuit in $\mathcal{C}$ correctly decides $x_1,\ldots,x_N$.

The above argument extends naturally to quantum circuits. Let $\mathcal{C}$ be the set of all quantum circuits of size n^k, over a basis of (say) Hadamard and Toffoli gates.[10] (Note that these circuits need not be bounded-error.) Then the first step is to amplify each circuit $C \in \mathcal{C}$ a polynomial number times, so that if C's initial error probability was at most $1/3$, then its new error probability is at most (say) 2^{-10n}. Let $\mathcal{C}'$ be the resulting set of amplified circuits. Now let $|\psi_0\rangle$ be a uniform superposition over all descriptions of circuits in $\mathcal{C}'$, together with an "answer register" that is initially set to $|0\rangle$:

$$|\psi_0\rangle := \frac{1}{\sqrt{|\mathcal{C}'|}} \sum_{C \in \mathcal{C}'} |C\rangle\,|0\rangle\,.$$

For each input $x_t \in \{0,1\}^n$, let U_t be a unitary transformation that maps $|C\rangle\,|0\rangle$ to $|C\rangle\,|C(x_t)\rangle$ for each $C \in \mathcal{C}'$, where $|C(x_t)\rangle$ is the output of C on input x_t. (In general, $|C(x_t)\rangle$ will be a superposition of $|0\rangle$ and $|1\rangle$.) To implement U_t, we simply simulate running C on x_t, and then run the simulation in reverse to uncompute garbage qubits.

Let $N = \lceil \log_2 |\mathcal{C}'| \rceil + 2$. Also, given an input x_t, let $L(x_t) = 1$ if $x_t \in L$ and $L(x_t) = 0$ otherwise. Fix $t < N$, and suppose by induction that we have already set $L(x_i)$ for all $i \leq t$. Then we will use the following quantum algorithm, called $\mathcal{A}_t$, to set $L(x_{t+1})$.

```
Set |ψ⟩ := |ψ_0⟩
For i := 1 to t
    Set |ψ⟩ := U_i |ψ⟩
    Measure the answer register
    If the measurement outcome is not L(x_i), then FAIL
Next i
Set |ψ⟩ := U_{t+1} |ψ⟩
Measure the answer register
```

[10] Shi [31] showed that this basis is universal. Any finite, universal set of gates with rational amplitudes would work equally well.

Say that $\mathcal{A}_t$ succeeds if it outputs $L(x_i)$ for all $x_1, \ldots, x_t$. *Conditioned* on $\mathcal{A}_t$ succeeding, if the final measurement yields the outcome $|1\rangle$ with probability at least $1/2$, then set $L(x_{t+1}) := 0$, and otherwise set $L(x_{t+1}) := 1$. Finally, set $L(x_t) := 0$ for all $t > N$.

By a simple extension of the result $\mathsf{BQP} \subseteq \mathsf{PP}$ due to Adleman, DeMarrais, and Huang [3], Aaronson [2] showed that polynomial-time quantum computation with postselected measurement can be simulated in PP (indeed the two are equivalent; that is, $\mathsf{PostBQP} = \mathsf{PP}$). In particular, a PP machine can simulate the postselected quantum algorithm $\mathcal{A}_t$ above, and thereby decide whether the final measurement will yield $|0\rangle$ or $|1\rangle$ with greater probability, conditioned on all previous measurements having yielded the correct outcomes. It follows that $L \in \mathsf{P}^{\mathsf{PP}}$.

On the other hand, suppose by way of contradiction that there exists a quantum circuit $C \in \mathcal{C}'$ that outputs $L(x_t)$ with probability at least $1 - 2^{-10n}$ for all t. Then the probability that C succeeds on $x_1, \ldots, x_N$ *simultaneously* is at least (say) 0.9, by Lemma 3 together with the triangle inequality. Hence the probability that $\mathcal{A}_t$ succeeds on $x_1, \ldots, x_N$ is at least $0.9/|\mathcal{C}'|$. Yet by construction, $\mathcal{A}_t$ succeeds with probability at most $1/2^t$, which is less than $0.9/|\mathcal{C}'|$ when $t = N - 1$. This yields the desired contradiction.

Finally, to incorporate quantum advice of size $s = n^k$, all we need to do is add an s-qubit "quantum advice register" to $|\psi_0\rangle$, which U_t's can use when simulating the circuits. We initialize this advice register to the maximally mixed state on s qubits. The key fact (see [1] for example) is that, whatever the "true" advice state $|\phi\rangle$, we can decompose the maximally mixed state into

$$\frac{1}{2^s} \sum_{j=1}^{2^s} |\phi_j\rangle \langle\phi_j|,$$

where $|\phi_1\rangle, \ldots, |\phi_{2^s}\rangle$ form an orthonormal basis and $|\phi_1\rangle = |\phi\rangle$. By linearity, we can then track the evolution of each of these 2^s components independently. So the previous argument goes through as before, if we set $N = \lceil \log_2 |\mathcal{C}'| \rceil + s + 2$. (Note that we are assuming the advice states are suitably amplified, which increases the running time of $\mathcal{A}_t$ by at most a polynomial factor.) ∎

Similarly, for all time-constructible functions $f(n) \leq 2^n$, one can show that the class $\mathsf{DTIME}(f(n))^{\mathsf{PP}}$ does not have quantum circuits of size $f(n)/n^2$. So for example, E^{PP} requires quantum circuits of exponential size.

Having shown a quantum circuit lower bound for P^{PP}, we now bootstrap our way down to PP. To do so, we use the following "quantum Karp-Lipton theorem." Here $\mathsf{BQP/poly}$ is BQP with polynomial-size classical advice, $\mathsf{BQP/qpoly}$ is BQP with polynomial-size quantum advice, QMA is like MA but with quantum verifiers and quantum witnesses, and QCMA is like MA but with quantum verifiers and *classical* witnesses. Also, recall that the counting hierarchy CH is the union of PP, $\mathsf{PP}^{\mathsf{PP}}$, $\mathsf{PP}^{\mathsf{PP}^{\mathsf{PP}}}$, and so on.

Theorem 5 *If* $\mathsf{PP} \subset \mathsf{BQP/poly}$ *then* $\mathsf{QCMA} = \mathsf{PP}$*, and indeed* CH *collapses to* QCMA*. Likewise, if* $\mathsf{PP} \subset \mathsf{BQP/qpoly}$ *then* CH *collapses to* QMA*.*

Proof. Let L be a language in CH. It is clear that we could decide L in quantum polynomial time, if we were given polynomial-size quantum circuits for a PP-complete language such as MajSat. For Fortnow and Rogers [14] showed that BQP is "low" for PP; that is, $\mathsf{PP}^{\mathsf{BQP}} = \mathsf{PP}$. So we could use the quantum circuits for MajSat to collapse $\mathsf{PP}^{\mathsf{PP}}$ to $\mathsf{PP}^{\mathsf{BQP}} = \mathsf{PP}$ to BQP, and similarly for all higher levels of CH.

Assume $\mathsf{PP} \subset \mathsf{BQP/poly}$; then clearly $\mathsf{P}^{\#\mathsf{P}} = \mathsf{P}^{\mathsf{PP}}$ is contained in $\mathsf{BQP/poly}$ as well. So in QCMA we can do the following: first guess a bounded-error quantum circuit C for computing the permanent of a $\operatorname{poly}(n) \times \operatorname{poly}(n)$ matrix over a finite field $\mathbb{F}_p$, for some prime $p = \Theta(\operatorname{poly}(n))$. (For convenience, here $\operatorname{poly}(n)$ means "a sufficiently large polynomial depending on L.") Then verify that with $1 - o(1)$ probability, C works on at least a $1 - 1/\operatorname{poly}(n)$ fraction of matrices. To do so, simply simulate the interactive protocol for the permanent due to Lund, Fortnow, Karloff, and Nisan [22], but with C in place of the prover. Next, use the random self-reducibility of the permanent to generate a new circuit C' that, with $1 - o(1)$ probability, is correct on *every* $\operatorname{poly}(n) \times \operatorname{poly}(n)$ matrix over $\mathbb{F}_p$. Since Permanent is $\#\mathsf{P}$-complete over all fields of characteristic $p \neq 2$ [37], we can then use C' to decide MajSat instances of size $\operatorname{poly}(n)$, and therefore the language L as well.

The case $\mathsf{PP} \subset \mathsf{BQP/qpoly}$ is essentially identical, except that in QMA we guess a quantum circuit with quantum advice. That quantum advice states cannot be reused indefinitely does not present a problem here: we simply guess a boosted circuit, or else $\operatorname{poly}(n)$ copies of the original circuit. ■

By combining Theorems 4 and 5, we immediately obtain the following.

Corollary 6 PP *does not have quantum circuits of size n^k for any fixed k, not even quantum circuits with quantum advice.*

Proof. Suppose by contradiction that PP had such circuits. Then certainly $\mathsf{PP} \subset \mathsf{BQP/qpoly}$, so $\mathsf{QMA} = \mathsf{PP} = \mathsf{P}^{\mathsf{PP}} = \mathsf{CH}$ by Theorem 5. But P^{PP} does *not* have such circuits by Theorem 4, and therefore neither does PP. ■

More generally, for all $f(n) \leq 2^n$ we find that $\mathsf{PTIME}(f(f(n)))$ requires quantum circuits of size approximately $f(n)$. For example, PEXP requires quantum circuits of "half-exponential" size.

Finally, we point out a quantum analogue of Buhrman, Fortnow, and Thierauf's classical nonrelativizing separation [10].

Theorem 7 $\mathsf{QCMA}_{\mathsf{EXP}} \not\subset \mathsf{BQP/poly}$*, and* $\mathsf{QMA}_{\mathsf{EXP}} \not\subset \mathsf{BQP/qpoly}$*.*

Proof. Suppose by contradiction that $\mathsf{QCMA}_{\mathsf{EXP}} \subset \mathsf{BQP/poly}$. Then clearly $\mathsf{EXP} \subset \mathsf{BQP/poly}$ as well. Babai, Fortnow, and Lund [5] showed that any language in EXP has a two-prover interactive protocol where the provers are in EXP. We can simulate such a protocol in QCMA as follows: first guess (suitably amplified) $\mathsf{BQP/poly}$ circuits computing the provers' strategies. Then simulate the provers and verifier, and accept if and only if the verifier accepts. It follows that $\mathsf{EXP} = \mathsf{QCMA}$, and therefore $\mathsf{QCMA} = \mathsf{P}^{\mathsf{PP}}$ as well. So by padding, $\mathsf{QCMA}_{\mathsf{EXP}} = \mathsf{EXP}^{\mathsf{PP}}$. But we know from Theorem 4 that $\mathsf{EXP}^{\mathsf{PP}} \not\subset \mathsf{BQP/poly}$, which yields the desired contradiction. The proof that $\mathsf{QMA}_{\mathsf{EXP}} \not\subset \mathsf{BQP/qpoly}$ is essentially identical, except that we guess quantum circuits with quantum advice. ■

One can strengthen Theorem 7 to show that $\mathsf{QMA}_{\mathsf{EXP}}$ requires quantum circuits of half-exponential size. However, in contrast to the case for PEXP, here the bound does not scale down to QMA. Indeed, it turns out that the smallest f for which we get *any* superlinear circuit size lower bound for $\mathsf{QMATIME}(f(n))$ is itself half-exponential.

4 The Oracle for $\mathsf{BPP}_{||}^{\mathsf{NP}}$

In this section we construct an oracle relative to which $\mathsf{BPP}_{||}^{\mathsf{NP}}$ has linear-size circuits.

Theorem 8 *There exists an oracle relative to which* $\mathsf{BPP}_{||}^{\mathsf{NP}}$ *has linear-size circuits.*

Proof. As in Theorem 2, we first give an oracle A that works for a specific value of n. Let $M_1, M_2, \ldots$ be an enumeration of "syntactic" $\mathsf{BPTIME}\left(n^{\log n}\right)_{||}^{\mathsf{NP}}$ machines, where syntactic means not necessarily satisfying the promise. Then it suffices to simulate $M_1, \ldots, M_n$. We assume without loss of generality that only the NP oracle (not the M_i's themselves) query A, and that each NP call is actually an $\mathsf{NTIME}(n)$ call (so in particular, it involves at most $n^{\log n}$ queries to A). Let $M_{i,x,z}(A)$ be a Boolean function that equals 1 if M_i accepts on input $x \in \{0,1\}^n$, random string $z \in \{0,1\}^{n^{\log n}}$, and oracle A, and 0 otherwise. Then let $p_{i,x}(A) := \mathrm{EX}_z[M_{i,x,z}(A)]$ be the probability that M_i accepts x.

The oracle A will consist of 2^{3n} rows and $n2^n$ columns, with each row labeled by $r \in \{0,1\}^{3n}$, and each column labeled by an $\langle i, x\rangle$ pair where $i \in \{1, \ldots, n\}$ and $x \in \{0,1\}^n$. We will construct A via an iterative procedure $\mathcal{P}$. Initially A is empty (that is, $A(r,i,x) = 0$ for all r, i, x). Let A_t be the state of A after the t^{th} iteration. Then to *encode* a row r means to set $A_t(r,i,x) := \operatorname{round}(p_{i,x}(A_{t-1}))$ for all i, x, where $\operatorname{round}(p) = 1$ if $p \geq 1/2$ and $\operatorname{round}(p) = 0$ if $p < 1/2$.

Call an $\langle i, x\rangle$ pair *sensitive* to row r, if encoding r would change $p_{i,x}(A)$ by at least $1/6$. Then $\mathcal{P}$ consists entirely of repeating the following two steps, for $t = 1, 2, 3 \ldots$:

(1) If there exists an r to which no $\langle i, x\rangle$ is sensitive, then encode r and halt.

(2) Otherwise, by a counting argument, there exists a pair $\langle j, y\rangle$ that is sensitive to at least $N = 2^{3n}/(n2^n)$ rows, call them $r_1, \ldots, r_N$. Let $A^{(k)}$ be the oracle obtained by starting from A and then encoding r_k. Choose an integer $k \in \{1, \ldots, N\}$ (we will specify how later), and set $A_t := A_{t-1}^{(k)}$.

Suppose $\mathcal{P}$ halts after t iterations, and let r be the row encoded by step (1). Then by assumption, $|p_{i,x}(A_t) - p_{i,x}(A_{t-1})| < 1/6$ for all i, x. So in particular, if $p_{i,x}(A_t) \geq 2/3$ then $p_{i,x}(A_{t-1}) > 1/2$ and therefore $A_t(r, i, x) = 1$. Likewise, if $p_{i,x}(A_t) \leq 1/3$ then $p_{i,x}(A_{t-1}) < 1/2$ and therefore $A_t(r, i, x) = 0$. It follows that any valid $\mathsf{BPTIME}\left(n^{\log n}\right)_{||}^{\mathsf{NP}}$ machine in $\{M_1, \ldots, M_n\}$ has linear-size circuits relative to A_t—since we can just hardwire $r \in \{0,1\}^{2n}$ into the circuits.

It remains only to show that $\mathcal{P}$ halts after a finite number of steps, for some choice of k's. Given an input x, random string z, and oracle A, let $S_{i,x,z}(A)$ be the set of NP queries made by M_i that accept. Then we will use

$$W(A) := \sum_{i,x} \operatorname*{EX}_z \left[|S_{i,x,z}(A)|\right]$$

as our progress measure. Since each M_i can query the NP oracle at most $n^{\log n}$ times, clearly $0 \leq |S_{i,x,z}(A)| \leq n^{\log n}$ for all i, x, z, and therefore

$$0 \leq W(A) \leq n2^n \cdot n^{\log n}$$

for all A. On the other hand, we claim that whenever step (2) is executed, if $k \in \{1, \ldots, N\}$ is chosen uniformly at random then

$$\operatorname*{EX}_k \left[W\left(A^{(k)}\right)\right] \geq W(A) + \frac{1}{6} - 2^{-n+o(n)}.$$

So in step (2), we should simply choose k to maximize $W\left(A^{(k)}\right)$. For we will then have $W(A_t) \geq \left(1/6 - 2^{-n+o(n)}\right) t$ for all t, from which it follows that $\mathcal{P}$ halts after at most

$$\frac{n2^n \cdot n^{\log n}}{1/6 - 2^{-n+o(n)}} = 2^{n+o(n)}$$

iterations.

We now prove the claim. Observe that for each accepting NP query $q \in S_{i,x,z}(A)$, there are at most $n^{\log n}$ rows r_k such that encoding r_k would cause $q \notin S_{i,x,z}\left(A^{(k)}\right)$. For to change q's output from 'accept' to 'reject,' we would have to eliminate (say) the lexicographically first accepting path of the NP oracle, and that path can depend on at most $n^{\log n}$ rows of A. Hence by the union bound, for all i, x, z, A we have

$$\begin{aligned}
\Pr_k \left[S_{i,x,z}(A) \not\subset S_{i,x,z}\left(A^{(k)}\right)\right] &\leq \sum_{q \in S_{i,x,z}(A)} \Pr_k \left[q \notin S_{i,x,z}\left(A^{(k)}\right)\right] \\
&\leq |S_{i,x,z}(A)| \frac{n^{\log n}}{N} \\
&\leq \frac{n^{2\log n}}{2^{3n}/(n2^n)} \\
&= 2^{-2n+o(n)}.
\end{aligned}$$

So in particular, for all i, x, A,

$$\begin{aligned}
\operatorname*{EX}_{k,z}\left[\left|S_{i,x,z}\left(A^{(k)}\right)\right|\right] &\geq |S_{i,x,z}(A)| \cdot \Pr_{k,z}\left[\left|S_{i,x,z}\left(A^{(k)}\right)\right| \geq |S_{i,x,z}(A)|\right] \\
&\geq |S_{i,x,z}(A)| \left(1 - 2^{-2n+o(n)}\right)
\end{aligned}$$

On the other hand, by assumption there exists a pair $\langle j, y\rangle$ that is sensitive to row r_k for every $k \in \{1, \ldots, N\}$. Furthermore, given y and z, the output $M_{j,y,z}(A)$ of M_j is a function of the NP oracle responses $S_{j,y,z}(A)$, and can change only if $S_{j,y,z}(A)$ changes. Therefore

$$\Pr_{k,z}\left[S_{j,y,z}\left(A^{(k)}\right) \neq S_{j,y,z}(A)\right] \geq \Pr_{k,z}\left[M_{j,y,z}\left(A^{(k)}\right) \neq M_{j,y,z}(A)\right] \geq \frac{1}{6}.$$

So by the union bound,

$$\begin{aligned}\Pr_{k,z}\left[\left|S_{j,y,z}\left(A^{(k)}\right)\right| > |S_{j,y,z}(A)|\right] &\geq \Pr_{k,z}\left[S_{j,y,z}\left(A^{(k)}\right) \neq S_{j,y,z}(A)\right] - \Pr_{k,z}\left[S_{j,y,z}(A) \not\subset S_{j,y,z}\left(A^{(k)}\right)\right] \\ &\geq \frac{1}{6} - 2^{-2n+o(n)}.\end{aligned}$$

Putting it all together,

$$\begin{aligned}\mathop{\mathrm{EX}}_{k}\left[W\left(A^{(k)}\right)\right] &= \sum_{i,x} \mathop{\mathrm{EX}}_{k,z}\left[\left|Q_{i,x,z}\left(A^{(k)}\right)\right|\right] \\ &\geq \frac{1}{6} - 2^{-2n+o(n)} + \sum_{i,x} |S_{i,x,z}(A)|\left(1 - 2^{-2n+o(n)}\right) \\ &= \frac{1}{6} - 2^{-2n+o(n)} + \left(1 - 2^{-2n+o(n)}\right) W(A) \\ &= W(A) + \frac{1}{6} - 2^{-n+o(n)},\end{aligned}$$

which completes the claim.

To handle all values of n simultaneously, we use exactly the same trick as in Theorem 2. That is, we replace r by an ℓ-tuple $R = (r_1, \ldots, r_\ell)$ where $r_\ell \in \{0,1\}^{3 \cdot 2^\ell}$; define the "row" $\mathcal{R}_\ell$ to consist of all triples $\langle R'_L, i, x\rangle$ such that $L \geq \ell$ and $r'_h = r_h$ for all $h \leq \ell$; and call the pair $\langle i, x\rangle$ "sensitive" to row $\mathcal{R}_\ell$ if there is any oracle A' that disagrees with A only in $\mathcal{R}_\ell$, such that $|p_{i,x}(A') - p_{i,x}(A)| \geq 1/6$. We then run the procedure $\mathcal{P}$ repeatedly to encode $r_1, r_2, \ldots$, where "encoding" r_ℓ means setting $A_t(R_\ell, i, x) := \mathrm{round}(p_{i,x}(A_{t-1}))$ for all $n \leq 2^\ell$, $i \in \{1, \ldots, n\}$, and $x \in \{0,1\}^n$. The rest of the proof goes through as before. ■

Let us make six remarks about Theorem 8.

(1) An immediate corollary is that any Karp-Lipton collapse to $\mathsf{BPP}_{||}^{\mathsf{NP}}$ would require nonrelativizing techniques. For relative to the oracle A from the theorem, we have $\mathsf{NP} \subseteq \mathsf{BPP}_{||}^{\mathsf{NP}} \subset \mathsf{P/poly}$. On the other hand, if $\mathsf{PH}^A = \mathsf{BPP}_{||}^{\mathsf{NP}^A}$, then $\mathsf{BPP}_{||}^{\mathsf{NP}^A}$ would not have linear-size circuits by Kannan's Theorem [18] (which relativizes), thereby yielding a contradiction.

(2) If we care about constants, we can reduce the advice r to $2n + o(n)$ bits for a specific n, or $8n + o(n)$ for all n simultaneously.

(3) As with Theorem 2, one can easily modify Theorem 8 to give a relativized world where $\mathsf{BPEXP}_{||}^{\mathsf{NP}} \subset \mathsf{P/poly}$. Thus, Theorem 8 provides an alternate generalization of the result of Buhrman, Fortnow, and Thierauf [10] that $\mathsf{MA}_{\mathsf{EXP}} \subset \mathsf{P/poly}$ relative to an oracle.

(4) Since $\mathsf{BPP}_{\mathsf{path}} \subseteq \mathsf{BPP}_{||}^{\mathsf{NP}}$ (as is not hard to show using approximate counting), Theorem 8 also provides an alternate proof that $\mathsf{BPP}_{\mathsf{path}}$ has linear-size circuits relative to an oracle.

(5) Completely analogously to Theorem 12, one can modify Theorem 8 to give oracles relative to which $\mathsf{P}^{\mathsf{NP}} = \mathsf{BPEXP}_{||}^{\mathsf{NP}}$ and $\oplus\mathsf{P} = \mathsf{BPEXP}_{||}^{\mathsf{NP}}$.

(6) For any function f, the construction of Theorem 8 actually yields an oracle relative to which $\mathsf{BPP}^{\mathsf{NP}[f(n)]}$ (that is, BPP with $f(n)$ adaptive NP queries) has circuits of size $O(n + f(n))$. For clearly we can simulate $f(n)$ adaptive queries using $2^{f(n)}$ nonadaptive queries. We then repeat Theorem 8 with the bound $0 \leq W(A) \leq n2^n \cdot 2^{f(n)}$.

5 Black-Box Learning in Algorithmica

"Algorithmica" is one of Impagliazzo's five possible worlds [17], the world in which $\mathsf{P} = \mathsf{NP}$. In this section we show that in Algorithmica, black-box learning of Boolean circuits is possible in $\mathsf{P}_{||}^{\mathsf{NP}}$. Let us first define what we mean by black-box learning.

Definition 9 *Say that black-box learning is possible in a complexity class $\mathcal{C}$ if the following holds. There exists a $\mathcal{C}$ machine M such that, for all Boolean functions $f : \{0,1\}^n \rightarrow \{0,1\}$ with circuit complexity at most $s(n)$, the machine M^f outputs a circuit for f given $\langle 0^n, 0^{s(n)} \rangle$ as input. Also, M has approximation ratio $\alpha(n)$ if for all f, any circuit output by M has size at most $s(n)\,\alpha(n)$.*

The above definition is admittedly somewhat vague, but for most natural complexity classes $\mathcal{C}$ it is clear how to make it precise. Firstly, by "$\mathcal{C}$ machine" we really mean "$\mathcal{FC}$ machine," where $\mathcal{FC}$ is the function version of $\mathcal{C}$. Secondly, for semantic classes, we do not care if the machine violates the promise on inputs not of the form $\langle 0^n, 0^{s(n)} \rangle$, or oracles f that do not have circuit complexity at most $s(n)$. Let us give a few examples.

- Almost by definition, black-box learning is possible in Σ_2^p with approximation ratio 1.
- As pointed out by Umans [36], the result of Bshouty et al. [8] implies that black-box learning is possible in $\mathsf{ZPP}^{\mathsf{NP}}$, with approximation ratio $O(n/\log n)$.
- Under standard derandomization assumptions, black-box learning is possible in P^{NP} with approximation ratio $O(n/\log n)$, and in PP with approximation ratio 1. For not only do these assumptions imply that $\mathsf{ZPP}^{\mathsf{NP}} = \mathsf{P}^{\mathsf{NP}}$ and that $\mathsf{BP} \cdot \mathsf{PP} = \mathsf{PP}$, but they also yield a *black-box simulation* of a $\mathsf{ZPP}^{\mathsf{NP}}$ or $\mathsf{BP} \cdot \mathsf{PP}$ algorithm that learns a circuit for f by just querying an existing circuit C on various inputs (without "cheating" and looking at C).

On the other hand:

Proposition 10 *Black-box learning is impossible in* NP*, or for that matter in* AM*,* IP*, or* MIP*.*

Proof. Suppose there are two possibilities: either f is the identically zero function, or else f is a point function (that is, there exists a y such that $f(x) = 1$ if and only if $x = y$). In both cases $s(n) = O(n)$. But since the verifier has only oracle access to f, it is obvious that no polynomially-bounded sequence of messages from the prover(s) could convince the verifier that f is identically zero. We omit the details, which were worked out by Fortnow and Sipser [15]. ■

We now prove the main result.

Theorem 11 *If* $\mathsf{P} = \mathsf{NP}$*, then black-box learning is possible in* $\mathsf{P}_{||}^{\mathsf{NP}}$ *(indeed, with approximation ratio 1.)*

Proof. We use a procedure inspired by that of Bshouty et al. [8].

Fix n, and suppose $f : \{0,1\}^n \rightarrow \{0,1\}$ has circuits of size $s = s(n)$. Let $\mathcal{B}$ be the set of all circuits of size s, so that $|\mathcal{B}| = s^{O(s)}$. Also, say that a circuit $C \in \mathcal{B}$ *succeeds* on input $x \in \{0,1\}^n$ if $C(x) = f(x)$, and *fails* otherwise. Then given a list of inputs $X = (x_1, x_2, \ldots)$, let $\mathcal{B}(X)$ be the set of circuits in $\mathcal{B}$ that succeed on every $x \in X$.

For the remainder of the proof, let $X_t = (x_1, \ldots, x_t)$ be a list of t inputs, and for all $0 \leq i < t$, let $X_i = (x_1, \ldots, x_i)$ be the prefix of X_t consisting of the first i inputs (so in particular, X_0 is the empty list). Then our first claim is that there exists an NP^f machine Q_t with the following behavior:

- If there exists an X_t such that $|\mathcal{B}(X_i)| \leq \frac{2}{3} |\mathcal{B}(X_{i-1})|$ for all $i \in \{1, \ldots, t\}$, then Q_t accepts.
- If for all X_t there exists an $i \in \{1, \ldots, t\}$ such that $|\mathcal{B}(X_i)| \geq \frac{3}{4} |\mathcal{B}(X_{i-1})|$, then Q_t rejects.

(As usual, if neither of the two stated conditions hold, then the machine can behave arbitrarily.)

In what follows, we can assume without loss of generality that t is polynomially bounded. For, since *some* circuit $C \in \mathcal{B}$ succeeds on every input, we must have $|\mathcal{B}(X_i)| \geq 1$ for all i. Therefore Q_t can accept only if $|\mathcal{B}|(3/4)^t \geq 1$, or equivalently if $t = O(s \log s)$.

Let $f(X_t) := (f(x_1), \ldots, f(x_t))$, and let z be a "witness string" consisting of X_t and $f(X_t)$. Then given z and $i \leq t$, we can easily decide whether a circuit C belongs to the set $\mathcal{B}(X_i)$: we simply check whether $C(x_j) = f(x_j)$ for all $j \leq i$. So by standard results on approximate counting due to Stockmeyer [33] and Sipser [32], we can approximate the cardinality $|\mathcal{B}(X_i)|$ in $\mathsf{BPP}^{\mathsf{NP}}$. More precisely, for all t, i there exists a $\mathsf{PromiseBPP}^{\mathsf{NP}}$ machine $M_{t,i}$ such that for all $z = \langle X_t, f(X_t)\rangle$:

- If $|\mathcal{B}(X_i)| \leq \frac{2}{3}|\mathcal{B}(X_{i-1})|$ then $M_{t,i}(z)$ accepts with probability at least 2/3 (where the probability is over $M_{t,i}$'s internal randomness).
- If $|\mathcal{B}(X_i)| \geq \frac{3}{4}|\mathcal{B}(X_{i-1})|$ then $M_{t,i}(z)$ rejects with probability least 2/3.

Now by the Sipser-Lautemann Theorem [32, 21], the assumption $\mathsf{P} = \mathsf{NP}$ implies that $\mathsf{PromiseP} = \mathsf{PromiseBPP}^{\mathsf{NP}}$ as well. So we can convert $M_{t,i}$ into a deterministic polynomial-time machine $M'_{t,i}$ such that for all z:

- If $|\mathcal{B}(X_i)| \leq \frac{2}{3}|\mathcal{B}(X_{i-1})|$ then $M'_{t,i}(z)$ accepts.
- If $|\mathcal{B}(X_i)| \geq \frac{3}{4}|\mathcal{B}(X_{i-1})|$ then $M'_{t,i}(z)$ rejects.

Using $M'_{t,i}$, we can then rewrite Q_t as follows.

"Does there exist a witness z, of the form $\langle X_t, f(X_t)\rangle$, such that $M'_{t,1}(z) \wedge \cdots \wedge M'_{t,t}(z)$?"

This proves the claim, since the above query is clearly in NP^f.

To complete the theorem, we will need one other predicate $A_t(z, x)$, with the following behavior. For all $z = \langle X_t, f(X_t)\rangle$ and $x \in \{0,1\}^n$:

- If $\Pr_{C \in \mathcal{B}(X_t)}[C(x) = 1] \geq 2/3$ then $A_t(z, x)$ accepts.
- If $\Pr_{C \in \mathcal{B}(X_t)}[C(x) = 0] \geq 2/3$ then $A_t(z, x)$ rejects.

It is clear that we can implement A_t in $\mathsf{PromiseBPP}^{\mathsf{NP}}$, again because of approximate counting and the ease of deciding membership in $\mathcal{B}(X_t)$. So by the assumption $\mathsf{P} = \mathsf{NP}$, we can also implement A_t in P.

Now let $C_{t,z}$ be the lexicographically first circuit $C \in \mathcal{B}$ such that $C(x) = A_t(z, x)$ for all $x \in \{0,1\}^n$. Notice that $A_t(z, x)$ is an *explicit procedure*: that is, we can evaluate it without recourse to the oracle for f. So given z, we can find $C_{t,z}$ in $\Delta_3^p = \mathsf{P}^{\mathsf{NP}^{\mathsf{NP}}}$, and hence also in P.

Let t^* be the maximum t for which Q_t accepts, and let $z = \langle X_{t^*}, f(X_{t^*})\rangle$ be any accepting witness for Q_{t^*}. Then for all $x \in \{0,1\}^n$, we have

$$\Pr_{C \in \mathcal{B}(X_{t^*})}[C(x) = f(x)] \geq \frac{2}{3}.$$

For otherwise the sequence $(x_1, \ldots, x_{t^*}, x)$ would satisfy Q_{t^*+1}, thereby contradicting the maximality of t^*. An immediate corollary is that $A_{t^*}(z, x) = f(x)$ for all $x \in \{0,1\}^n$. Hence $C_{t^*,z}$ is the lexicographically first circuit for f, independently of the particular accepting witness z.

The $\mathsf{P}_{||}^{\mathsf{NP}^f}$ learning algorithm now follows easily. For all $t = O(s \log s)$, the algorithm submits the query Q_t to the NP oracle. It also submits the following query, called $R_{t,j}$, for all $t = O(s \log s)$ and $j = O(s \log s)$:

"Does there exist a witness $z = \langle X_t, f(X_t)\rangle$ satisfying Q_t, such that the j^{th} bit in the description of $C_{t,z}$ is a 1?"

Using the responses to the Q_t's, the algorithm then determines t^*. Finally it reads a description of $C_{t^*,z}$ off the responses to the $R_{t^*,j}$'s. ■

Theorem 11 has the following easy corollaries. First, we cannot show that a Karp-Lipton collapse to $\mathsf{P}_{||}^{\mathsf{NP}}$ would require non-black-box techniques, without also showing $\mathsf{P} \neq \mathsf{NP}$. Second, if $\mathsf{P} = \mathsf{NP}$, then black-box learning is possible in NP/log. For since the $\mathsf{P}_{||}^{\mathsf{NP}}$ algorithm of Theorem 11 does not take any input, we simply count how many of its NP queries return a positive answer, and then feed that number as advice to the NP/log machine.

6 Open Problems

The main open problem is, of course, to prove better nonrelativizing lower bounds. For example, can we show that $\mathsf{BPP}_{||}^{\mathsf{NP}}$ does not have linear-size circuits? To do so, we would presumably need a nonrelativizing technique that applies directly to the polynomial hierarchy, without requiring the full strength of $\#\mathsf{P}$. Arora, Impagliazzo, and Vazirani [4] argue that "local checkability," as used for example in the PCP Theorem, constitutes such a technique (though see Fortnow [12] for a contrary view). For us, the relevant question now is not which techniques are "truly" nonrelativizing, but simply which ones lead to lower bounds!

Here are a few other problems.

- Can we show that $\mathsf{P}^{\mathsf{NP}} \neq \mathsf{PEXP}$? If so, then we would obtain perhaps the first nonrelativizing separation of uniform complexity classes that does not follow immediately from a collapse such as $\mathsf{IP} = \mathsf{PSPACE}$ or $\mathsf{MIP} = \mathsf{NEXP}$.
- Can we show that PEXP requires circuits of exponential size, rather than just half-exponential?
- As mentioned in Section 1.2, Bshouty et al.'s algorithm does not find a *minimal* circuit for a Boolean function f, but only a circuit within an $O(n/\log n)$ factor of minimal.[11] Can we improve this approximation ratio, or alternatively, show that doing so would require nonrelativizing techniques?
- Is black-box learning possible in $\mathsf{P}_{||}^{\mathsf{NP}}$ or $\mathsf{ZPP}_{||}^{\mathsf{NP}}$, under some computational assumption that we actually believe (for example, a derandomization assumption)? Alternatively, can we show that black-box learning is *impossible* in $\mathsf{P}_{||}^{\mathsf{NP}}$ under some plausible computational assumption?

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8 Appendix: A *Really* Big Crunch

By slightly modifying the construction of Theorem 2, we can resolve two other open questions of Fortnow.

Theorem 12

(i) There exists an oracle relative to which $\mathsf{P}^{\mathsf{NP}} = \mathsf{PEXP}$*, and indeed* $\mathsf{P}^{\mathsf{NP}} = \mathsf{P}^{\mathsf{NP}^{\mathsf{PEXP}}}$*.*

(ii) There exists an oracle relative to which $\oplus\mathsf{P} = \mathsf{PEXP}$*.*

Proof.

(i) In the oracle construction of Theorem 2 dealing with all n simultaneously, make the following simple change. Whenever a row R gets encoded, record the "current time" t as a prefix to that row. In other words, the oracle A will now take two kinds of queries: those of the form $\langle R, i, x\rangle$ as before, and those of the form $\langle R, j\rangle$ for an integer $j \geq 0$. Initially $A(R, j) = 0$ for all R, j. At any step of the iterative procedure, let t be the number of encoding steps that have already occurred. Then call the pair $\langle i, x\rangle$ "sensitive" to row R, if there exists an oracle A' such that

- A' disagrees with A only in row R,
- $M_{i,x}(A') \neq M_{i,x}(A)$, and
- as we range over j, the $A'(R,j)$'s encode the binary expansion of $t+1$.

Clearly the proof of Theorem 2 still goes through with this change. For let $\ell = \lceil \log_2 n \rceil$. Then as before, whenever there does not exist a row R of the form $\left(r_1^*, \ldots, r_{\ell-1}^*, r_\ell\right)$ to which no $\langle i, x \rangle$ is sensitive, we can encode a subset of those rows so as to double $Q(A)$. Since $2^{-2^{O(n)}} \leq Q(A) \leq 2^{2^{O(n)}}$ for all A, this process will halt after at most $2^{O(n)}$ steps, meaning that t will never require more than $O(n)$ bits to represent. Indeed, this is true even if we are dealing with $\mathsf{PTIME}(2^n)$ machines, rather than $\mathsf{PTIME}\left(n^{\log n}\right)$ machines.

Now consider a $\mathsf{PTIME}^A(2^n)$ machine M_i. We can simulate M_i in $\mathsf{DTIME}\left(n^2\right)^{\mathsf{NP}^A}$, as follows. Given an input $x \in \{0,1\}^n$, first find the unique row $R = \left(r_1, \ldots, r_{\lceil \log_2 n \rceil}\right)$ for which t is maximal—in other words, the last such row to have been encoded. This requires $O(n)$ adaptive queries to the NP oracle, each of size $O(n)$. Then output $A(R,i,x)$.

It follows that $\mathsf{DTIME}\left(n^2\right)^{\mathsf{NP}} = \mathsf{PE}$ relative to A, and (by padding) that $\mathsf{P}^{\mathsf{NP}} = \mathsf{PEXP}$. Indeed, once the P^{NP} machine finds the r_ℓ's, it can use them to decide an arbitrary language in $\mathsf{P}^{\mathsf{NP}^{\mathsf{PEXP}}}$, which is why $\mathsf{P}^{\mathsf{NP}} = \mathsf{P}^{\mathsf{NP}^{\mathsf{PEXP}}}$ as well.

(ii) In this case the change to Theorem 2 is even simpler. Whenever we encode a row $R = (r_1, \ldots, r_\ell)$, instead of setting $A_t(R,i,x) := M_{i,x}(A_{t-1})$ for all i, x, we now set

$$A_t(R,i,x) := M_{i,x}(A_{t-1}) \oplus \bigoplus_{R' \neq R} A_t(R',i,x),$$

where the sum mod 2 ranges over all $R' = (r_1', \ldots, r_\ell')$ other than R itself. Then when we are done, by assumption A will satisfy

$$M_{i,x}(A) = \bigoplus_{R=(r_1,\ldots,r_\ell)} A(R,i,x)$$

for all $n \leq 2^\ell$, $i \in \{1, \ldots, n\}$, and $x \in \{0,1\}^n$. So to simulate a PE machine M_i on input x, a $\oplus\mathsf{DTIME}(n)$ machine just needs to return the above sum. Hence $\oplus\mathsf{DTIME}^A(n) = \mathsf{PE}^A$, and $\oplus\mathsf{P}^A = \mathsf{PEXP}^A$ by padding.

■

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