# A note on semantic cutting planes 

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#### Abstract

We show that the semantic cutting planes proof system has feasible interpolation via monotone real circuits. This gives an exponential lower bound on proof length in the system, answering a question from [5].

We also pose the following problem: can every multivariate non-decreasing function be expressed as a composition of non-decreasing functions in two variables?


## 1 Introduction

Cutting planes is a proof system designed to show that a given set of linear inequalities $\mathcal{L}$ has no 0,1solution. A cutting planes proof starts from the inequalities in $\mathcal{L}$, produces new inequalities by means of simple syntactic rules (namely, adding two inequalities and the "rounding-up" rule), until it reaches the contradictory inequality $0 \geq 1$. The system is based on the procedure of Gomory and Chvátal [6, 3]; as a proof system, it was introduced in [4]. The complexity of cutting plane proofs has been intensively studied. The most interesting result is due to Pudlák [11, who proved that there exists a set of unsatisfiable linear inequalities which require exponential size cutting planes refutation (moreover, the inequalities represent a Boolean formula in a conjunctive normal form). His proof is a beautiful example of the so-called "feasible interpolation technique", and it required extending monotone Boolean circuit lower bounds to the new class of real monotone circuits.

In this note, we consider a stronger system called semantic cutting planes. In a semantic cutting planes proof, we are allowed to derive from inequalities $L_{1}$ and $L_{2}$ any inequality $L$ which semantically follows from $L_{1}$ and $L_{2}$ - i.e., such that every 0,1 -assignment which satisfies $L_{1}$ and $L_{2}$, satisfies also $L$. A cutting planes proof is automatically a semantic cutting planes proof, but the latter system is stronger. This is suggested by the fact that it is $N P$-hard to check whether a semantic inference is correct (the knapsack problem can be stated in terms of just two inequalities). That semantic cutting planes are indeed exponentially more powerful was proved by Y. Filmus and M. Lauria in [5], who also gave the system its name. However, semantic inferences were investigated earlier in [7] or [2]. In [2], Beame, Pitassi and Segerlind consider semantic inferences using polynomial inequalities of degree $k$. Their results, together with the new lower bounds on communication complexity of disjointness [8, 12], imply exponential lower bounds on the tree-like version of such systems - including the tree-like semantic cutting planes. Here, we will prove an exponential lower bound on length of semantic cutting planes refutations. As in Pudlák's lower bound, we show that the semantic cutting planes system has feasible interpolation via monotone real circuits - in fact, our proof is a straightforward adaptation of Pudlák's original proof; the changes are all but cosmetic.

In Section 3 we discuss semantic inferences which can use more than two assumptions. In this context, we come across the following problem: can every multivariate non-decreasing function be expressed as a composition of non-decreasing functions in two variables?

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## 2 Feasible interpolation for semantic cutting planes

## The proof system

A (linear) inequality in variables $x_{1}, \ldots, x_{n}$ is an expression of the form

$$
a_{1} x_{1}+\cdots+a_{n} x_{n} \geq b, \quad \text { with } a_{1}, \ldots, a_{n}, b \in \mathbb{R}
$$

We view the left hand side simply as a linear function $U: \mathbb{R}^{n} \rightarrow \mathbb{R}$, with $U(0)=0$. We say that a 0 , 1 assignment $\sigma \in\{0,1\}^{n}$ satisfies the inequality $U \geq b$, if $U(\sigma) \geq b$. A linear inequality $L$ semantically follows from a set inequalities $\mathcal{L}$, if every 0,1 -assignment, which satisfies every inequality in $\mathcal{L}$, also satisfies $L$. A set of inequalities $\mathcal{L}$ is satisfiable, if there exists an assignment which satisfies every inequality in $\mathcal{L}$.

Let $\mathcal{L}$ be a set of inequalities. A semantic cutting planes proof of an inequality $L$ from $\mathcal{L}$ is a sequence of inequalities $L_{1}, \ldots, L_{m}$ such that $L_{m}=L$, and for every $i \in\{1, \ldots, m\}$,
(i). $L_{i} \in \mathcal{L}$, or
(ii). there exist $j_{1}, j_{2}<i$ such that $L_{i}$ follows from $L_{j_{1}}, L_{j_{2}}$.
$L_{i}$ will be called a proof line in the proof. A semantic cutting planes refutation of $\mathcal{L}$ is a proof $0 \geq b$ from $\mathcal{L}$, where $b$ is a positive real number.

We deviate from the definition in [5] in two details. First, we do not add the inequalities $x_{i} \geq 0$ and $-x_{i} \geq-1$ as extra axioms. However, both of those inequalities are satisfied by every assignment, and hence can be derived from an arbitrary inequality. Second, we work with inequalities with real rather than integer coefficients. But, as we are dealing with the Boolean cube, every inequality with real coefficients is equivalent to an equality with integers (see [10]).

The semantic cutting planes system is sound and complete, i.e.:

- $\mathcal{L}$ has a semantic cutting planes refutation iff $\mathcal{L}$ is unsatisfiable.

Soundness is obvious, and completeness follows from completeness of the cutting planes system.
We are chiefly interested in sets of inequalities which arise from a Boolean formula in conjunctive normal form. A disjunction such as $x_{1} \vee \neg x_{2} \vee \neg x_{3}$ is represented as the inequality $x_{1}+\left(1-x_{2}\right)+\left(1-x_{3}\right) \geq 1$ (or rather, $x_{1}-x_{2}-x_{3} \geq-1$ ). A conjunction of disjunctions is then represented by the set of inequalities representing the disjunctions. Clearly, an assignment satisfies the Boolean formula iff it satisfies the corresponding set of inequalities.

## Feasible interpolation via monotone real circuits

Let $X, Y_{1}, Y_{2}$ be disjoint sets of variables with $X=\left\{x_{1}, \ldots, x_{n}\right\}$. An inequality $L$ of the form $U \geq b$ in the variables $X \cup Y_{1} \cup Y_{2}$ can be uniquely written as $U^{x}+U^{y_{1}}+U^{y_{2}} \geq b$, where $U^{x}, U^{y_{1}}$ and $U^{y_{2}}$ depend only on the variables $X, Y_{1}, Y_{2}$, respectively. If $\sigma \in\{0,1\}^{n}$ is an assignment to the variables $X, L(\sigma)$ will denote the inequality

$$
\begin{equation*}
U^{y_{1}}+U^{y_{2}} \geq b-U^{x}(\sigma) \tag{1}
\end{equation*}
$$

Let $\mathcal{L}_{1}=\left\{L_{1}, \ldots, L_{p}\right\}$ and $\mathcal{L}_{2}=\left\{L_{1}^{\prime}, \ldots, L_{q}^{\prime}\right\}$ be two sets of inequalities, such that every inequality in $\mathcal{L}_{1}$ depends only the variables $X \cup Y_{1}$, and every inequality in $\mathcal{L}_{2}$ depends only the variables $X \cup Y_{2}$. We say that a Boolean function $f:\{0,1\}^{n} \rightarrow\{0,1\}$ interpolates $\mathcal{L}_{1}$ and $\mathcal{L}_{2}$, if for every $\sigma \in\{0,1\}^{n}$
(i). if $f(\sigma)=0$ then the set $\mathcal{L}_{1}(\sigma)=\left\{L_{1}(\sigma), \ldots, L_{p}(\sigma)\right\}$ is unsatisfiable, and
(ii). if $f(\sigma)=1$ then the set $\mathcal{L}_{2}(\sigma)=\left\{L_{1}^{\prime}(\sigma), \ldots, L_{q}^{\prime}(\sigma)\right\}$ is unsatisfiable.

Recall the definition of monotone real circuit from [11]. A monotone real circuit $C$ computes a nondecreasing function $f: \mathbb{R}^{n} \rightarrow \mathbb{R}$. A gate can be any nondecreasing function : $\mathbb{R} \rightarrow \mathbb{R}$ or $: \mathbb{R}^{2} \rightarrow \mathbb{R}$. If $f\left(\{0,1\}^{n}\right) \subseteq\{0,1\}, C$ is said to compute the Boolean function $\left.f\right|_{\{0,1\}^{n}}$. Clearly, the Boolean function must be monotone.

We will prove the following:

Theorem 1. Let $\mathcal{L}_{1}$ and $\mathcal{L}_{2}$ be as above. Assume that the variables $X$ have non-positive coefficients in every inequality in $\mathcal{L}_{2}$, and that $\mathcal{L}_{1} \cup \mathcal{L}_{2}$ has a semantic cutting planes refutation with $m$ proof lines. Then there exists a Boolean function which interpolates $\mathcal{L}_{1}$ and $\mathcal{L}_{2}$ and which can be computed by a monotone real circuit of size $O(m+(p+q) n)$.

Fortunately, Pudlák has also provided an exponential lower bound on the size of real monotone circuits interpolating the "clique versus coloring" tautologies. He used this to obtain an exponential lower bound for syntactic cutting planes. In the same manner, Theorem 1 implies

Corollary 2. Let $\mathcal{L}$ be the set of inequalities representing the "clique versus coloring" tautology as in Corollary 7 in [11]. Then any semantic cutting planes refutation of $\mathcal{L}$ has an exponential number of lines.

Note that in Theorem 1 , the assumption that " $X$ have non-positive coefficients in every inequality in $\mathcal{L}_{2}$ " can be replaced by the assumption " $X$ have non-negative coefficients in every inequality in $\mathcal{L}_{1}$ ".

## Proof of Theorem 1

Let us first imagine that $X=\emptyset$. That is, the sets of inequalities $\mathcal{L}_{1}$ and $\mathcal{L}_{2}$ depend on disjoint sets of variables $Y_{1}$ and $Y_{2}$, respectively. Assume we have a refutation $R$ of $\mathcal{L}_{1} \cup \mathcal{L}_{2}$ with $m$ proof lines. This means that at least one of $\mathcal{L}_{1}$ or $\mathcal{L}_{2}$ is unsatisfiable. We will prove a stronger statement, that at least one of $\mathcal{L}_{1}, \mathcal{L}_{2}$ has a refutation with $m$ proof lines:

Claim. There exists $e \in\{1,2\}$ and a refutation $R_{e}$ of $\mathcal{L}_{e}$ with $m$ proof-lines.
Proof. Let $R$ be the sequence $U_{1} \geq b_{1}, \ldots, U_{m} \geq b_{m}$ with $U_{m}=0$ and $b_{m}$ positive. For $e \in\{1,2\}$ Let $R_{e}$ be the sequence of inequalities

$$
U_{1}^{y_{e}} \geq c_{1}^{e}, \ldots, U_{m}^{y_{e}} \geq c_{m}^{e}
$$

where the constants $c_{1}^{e}, \ldots, c_{m}^{e}$ are defined as follows:
(i). if $\left(U_{i} \geq b_{i}\right) \in \mathcal{L}_{e}$, let $c_{i}^{e}:=b_{i}$, else
(ii). if $\left(U_{i} \geq b_{i}\right) \in \mathcal{L}_{e^{\prime}}, e^{\prime} \neq e$, let $c_{i}^{e}:=0$, else
(iii). if $U_{i} \geq b_{i}$ semantically follows from $U_{j_{1}} \geq b_{j_{1}}$ and $U_{j_{2}} \geq b_{j_{2}}$ with $j_{1}, j_{2}<i$, let

$$
\begin{aligned}
& c_{i}^{e}:=\min \left\{U_{i}^{y_{e}}(\rho) \in \mathbb{R}: \rho \in A_{i}^{e}\right\} \\
& \quad \text { where } A_{i}^{e}=\left\{\rho \in\{0,1\}^{\left|Y_{e}\right|}: U_{j_{1}}^{y_{e}}(\rho) \geq c_{j_{1}}^{e}, U_{j_{2}}^{y_{e}}(\rho) \geq c_{j_{2}}^{e}\right\}
\end{aligned}
$$

If $A_{i}^{e}=\emptyset$, let $c_{i}^{e}:=\infty$ (or rather, a fixed but large enough real number).
The construction guarantees that
(a) For $e \in\{1,2\}, R_{e}$ is a correct proof of $0 \geq c_{m}^{e}$ from $\mathcal{L}_{e}$, and
(b) for every $i \in\{1, \ldots, m\}, c_{i}^{1}+c_{i}^{2} \geq b_{i}$, unless $U_{i} \geq b_{i}$ is vacuous: i.e., $U_{i}=0$ and $b_{i}$ is negative.

The statement (a) is straightforward. Part (b) is proved by induction on $i \in\{1, \ldots, m\}$. In cases (i) and (ii) equality holds, except when $\left(U_{i} \geq b_{i}\right) \in \mathcal{L}_{1} \cap \mathcal{L}_{2}$. Then $U_{i}=0$ and $c_{i}^{1}=c_{i}^{2}=b_{i}$, and so $c_{i}^{1}+c_{i}^{2}=2 b_{i}$. Hence $c_{i}^{1}+c_{i}^{2} \geq b_{i}$ unless $b_{i}$ is negative, and $U_{i} \geq b_{i}$ is indeed vacuous. For (iii) the non-trivial case is when none of $U_{i} \geq b_{i}, U_{j_{1}} \geq b_{j_{1}}, U_{j_{2}} \geq b_{j_{2}}$ is vacuous and $A_{i}^{1}, A_{i}^{2} \neq \emptyset$. Then there exist $\rho_{1} \in\{0,1\}^{\left|Y_{1}\right|}$ and $\rho_{2} \in\{0,1\}^{\left|Y_{2}\right|}$ such that $c_{i}^{1}=U_{i}^{y_{1}}\left(\rho_{1}\right)$ and $c_{i}^{2}=U_{i}^{y_{2}}\left(\rho_{2}\right)$, and

$$
\begin{aligned}
& U_{j_{1}}^{y_{1}}\left(\rho_{1}\right) \geq c_{j_{1}}^{1}, U_{j_{2}}^{y_{1}}\left(\rho_{1}\right) \geq c_{j_{2}}^{1} \\
& U_{j_{1}}^{y_{2}}\left(\rho_{2}\right) \geq c_{j_{1}}^{2}, U_{j_{2}}^{y_{2}}\left(\rho_{2}\right) \geq c_{j_{2}}^{2}
\end{aligned}
$$

Since $c_{j_{1}}^{1}+c_{j_{1}}^{2} \geq b_{j_{1}}$ and $c_{j_{2}}^{1}+c_{j_{2}}^{2} \geq b_{j_{2}}$, we have

$$
U_{j_{1}}^{y_{1}}\left(\rho_{1}\right)+U_{j_{1}}^{y_{2}}\left(\rho_{2}\right) \geq b_{j_{1}}, \text { and } U_{j_{2}}^{y_{1}}\left(\rho_{1}\right)+U_{j_{2}}^{y_{2}}\left(\rho_{2}\right) \geq b_{j_{2}}
$$

Since $U_{i} \geq b_{i}$ semantically follows from $U_{j_{1}} \geq b_{j_{1}}$ and $U_{j_{2}} \geq b_{j_{2}}$, we have

$$
b_{i} \leq U_{i}^{y_{1}}\left(\rho_{1}\right)+U_{i}^{y_{2}}\left(\rho_{2}\right)=c_{i}^{1}+c_{i}^{2}
$$

Finally, $b_{m}>0$ and (b) shows that either $c_{m}^{1}$ or $c_{m}^{2}$ is positive, and hence $R_{1}$ is a refutation of $\mathcal{L}_{1}$, or $R_{2}$ is a refutation of $\mathcal{L}_{2}$.

To prove the theorem, the main observation is that in the case (iii), $c_{i}$ is a non-decreasing function of $c_{j_{1}}$ and $c_{j_{2}}$ : increasing $c_{j_{1}}$ or $c_{j_{2}}$ means that in (iii), the minimum is taken over a smaller set.

Let $\mathcal{L}_{1}, \mathcal{L}_{2}$ be as in the statement of the theorem, and $R$ a refutation of $\mathcal{L}_{1} \cup \mathcal{L}_{2}$ with $m$ lines. For an assignment $\sigma$ to the variables $X$, let $R(\sigma)$ be the refutation obtained by replacing every line $L$ in $R$ by $L(\sigma)$. It is indeed a correct refutation of $\mathcal{L}_{1}(\sigma) \cup \mathcal{L}_{2}(\sigma)$, where the two sets now have disjoint variables. Let $R_{1}^{\sigma}, R_{2}^{\sigma}$ be the two proofs constructed as in the Claim, and consider the $c_{m}^{1}$ and $c_{m}^{2}$ as functions of $\sigma$. By (a), if $c_{m}^{2}(\sigma)>0$ then $R_{2}^{\sigma}$ is a refutation of $\mathcal{L}_{2}(\sigma)$ and so $\mathcal{L}_{2}(\sigma)$ is unsatisfiable. If $c_{m}^{2}(\sigma) \leq 0$ then, by (b), $c_{m}^{1}(\sigma)>0$ and so $\mathcal{L}_{1}(\sigma)$ is unsatisfiable. In other words, if we define the Boolean function $f$ by

$$
f(\sigma)=1 \quad \text { iff } \quad c_{m}^{2}(\sigma)>0
$$

then $f$ interpolates $\mathcal{L}_{1}$ and $\mathcal{L}_{2}$. Moreover, if $X$ have non-positive coefficients in $\mathcal{L}_{2}$, the function $f$ can be computed by a monotone real circuit with $O(m+p n)$ gates. This is because in $(i), c_{i}^{2}(\sigma)$ is a linear function with non-negative coefficients (in (1), $U^{x}(\sigma)$ is moved to the right hand side), in (ii), it is a constant, and in (iii) $c_{i}^{2}$ is a non-decreasing function of $c_{j_{1}}^{2}$ of $c_{j_{2}}^{2}$.

## 3 Inferences with higher fan-in and Hilbert's 13th Problem

In the definition of semantic cutting planes, we assumed that in a refutation of $\mathcal{L}$, every line is either an element of $\mathcal{L}$ or it follows from at most two previously proved inequalities. But why not three or a hundred inequalities? For a fixed $k \in \mathbb{N}$, define $k$-semantic cutting planes refutation of $\mathcal{L}(k$-SCP refutation, for short), as a refutation in which every line $L_{i} \notin \mathcal{L}$ semantically follows from some $L_{j_{1}}, \ldots, L_{j_{k}}$, with $j_{1}, \ldots, j_{k}<i$. The obvious question is whether increasing $k$ makes the proof system more powerful:

Problem 1. For $2 \leq k_{1}<k_{2}$, can we simulate $k_{2}$-semantic cutting planes by $k_{1}$-semantic cutting planes? More exactly, is there a polynomial $p$, such that whenever $\mathcal{L}$ has a $k_{2}$-SCP refutation with $m$ proof-lines, then it has a $k_{1}-S C P$ refutation with $\leq p(m)$ proof-lines?

We do not know an answer to this question. On the other hand, we note that Theorem 1 and Corollary 2 can be extended to $k$-semantic refutations:

- Theorem 1 holds for $k$-SCP refutations, if we allow monotone real circuits to use non-decreasing $k$-ary functions as gates.
- Pudlák's lower bound works for monotone real circuits with $k$-ary gates, for any fixed $k$.
- Hence Corollary 2 holds also for $k$-SCP refutations, giving an exponential lower bound on the number of proof-lines.

In this context, we come across a related question, which is arguably much more interesting as a mathematical problem:

Problem 2. Can every multivariate non-decreasing real function be expressed as a composition of nondecreasing unary or binary functions?

In other words, we want to know whether every non-decreasing function can be computed by a monotone real circuit, with gates of fan-in at most two. If this is the case, there must also exist a function $\lambda: \mathbb{N} \rightarrow \mathbb{N}$ such that every non-decreasing $n$-ary function is computable by a monotone real circuit of size at most $\lambda(n){ }_{\square}^{1}$ This would mean that we can simulate any monotone real circuit with $k$-ary gates by a monotone real circuit with binary gates, with loss in size of a factor at most $\lambda(k)$.

Problem 2 is reminiscent of the solution to Hilbert's 13th Problem due to Arnold and Kolmogorov. They have shown that every multivariate continuous function can be expressed as a composition of unary and binary continuous functions (see [9] Chapter 11). In fact, the only binary function needed is addition: any continuous function can be expressed in terms of addition, and several unary continuous functions. This is rather surprising; Hilbert's 13th problem tacitly assumes that such a representation of continuous functions is impossible. Moreover, such a representation is indeed impossible for many other classes of functions: there exists an analytic function in three variables which cannot be expressed in terms of analytic functions of two variables; similarly for infinitely differentiable or entire functions (see [1] for further references).

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[^1]:    ${ }^{1}$ Hint: for a fixed $n$, assume that for every $k$ there exists $n$-ary non-decreasing function $f_{k}$ which cannot be computed by a monotone real circuit of size $\leq k$. Then we can "amalgamate" the functions $f_{1}, f_{2}, \ldots$ into a single $(n+1)$-ary non-decreasing function, which cannot be computed by a monotone real circuit of any size.

