# Circuits with Medium Fan-In 

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#### Abstract

We consider boolean circuits in which every gate may compute an arbitrary boolean function of $k$ other gates, for a parameter $k$. We give an explicit function $f:\{0,1\}^{n} \rightarrow\{0,1\}$ that requires at least $\Omega\left(\log ^{2} n\right)$ non-input gates when $k=2 n / 3$. When the circuit is restricted to being layered and depth 2 , we prove a lower bound of $n^{\Omega(1)}$ on the number of non-input gates. When the circuit is a formula with gates of fan-in $k$, we give a lower bound $\Omega\left(n^{2} / k \log n\right)$ on the total number of gates.

Our model is connected to some well known approaches to proving lower bounds in complexity theory. Optimal lower bounds for the Number-On-Forehead model in communication complexity, or for bounded depth circuits in $\mathrm{AC}_{0}$, or extractors for varieties over small fields would imply strong lower bounds in our model. On the other hand, new lower bounds for our model would prove new time-space tradeoffs for branching programs and impossibility results for (fan-in 2) circuits with linear size and logarithmic depth. In particular, our lower bound gives a different proof for a known time-space tradeoff for oblivious branching programs.


## 1 Introduction

A boolean circuit is usually defined as a directed acyclic graph where vertices (called gates) have in-degree (called fan-in) at most 2. Every gate with fan-in 0 corresponds to an input variable, and all other gates compute an arbitrary boolean function of the values that feed into them. Sometimes the model is restricted to using gates from the DeMorgan basis (i.e. AND, OR, NOT) gates, but this changes the size of the circuit by at most a constant factor. The circuit computes a function $f:\{0,1\}^{n} \rightarrow\{0,1\}$ if some gate in the circuit evaluates to $f$. A formula is a circuit whose underlying graph is a tree. The depth of the circuit is the length of the longest path in the graph.

Since every algorithm with running time $T(n)$ can be simulated by circuits of size $\tilde{O}(T(n))$, one can hope to prove lower bounds on the time complexity of algorithms by proving lower bounds on circuit size. A super-polynomial lower bound on the circuit size of an NP problem would imply that $\mathrm{P} \neq \mathrm{NP}$. However, we know of no explicit function (even outside NP) for which we can prove a super-linear lower bound. In contrast, counting arguments imply that almost every function requires circuits of exponential size.

We study a stronger model of circuits. We allow the gates to have fan-in $k$, where $k$ is a parameter that depends on $n$, and each gate may compute an arbitrary function of its inputs. Typically, we consider the case where $k$ is a constant fraction of $n$. We write $C_{k}(f)$ to denote the minimum number of non-input gates required to compute $f$ in this model.

These circuits are much stronger than the models usually studied in the context of proving lower bounds. Nevertheless, we show that many attempts at proving lower bounds on other models of computation can be seen as attempts to prove new lower bounds in our model. Truly exponential lower bounds for $\mathrm{AC}_{0}$, optimal lower bounds for the Number-On-Forehead (or NOF) model of communication, or new extractors for varieties over small fields, would all improve the best lower bounds we know how to prove for $C_{k}(f)$.

[^0]On the other hand, new lower bounds in our model would lead to lower bounds for branching programs and (fan-in 2) circuits of logarithmic depth. Our Theorem 1 already leads to a different proof of the lower bounds on oblivious branching programs given by Babai, Nisan and Szegedy BNS92. We elaborate on these connections in Section 4

Similar models have been studied in past work. Circuits with arbitrary gates and arbitrarily large fan-in have been considered for computing several boolean functions simultaneously. If $n$ boolean functions are being computed, the trivial upper bound uses $n^{2}$ wires (edges). Super-linear lower bounds on the number of wires are known for circuits of bounded depth in this scenario [he05, PRS97, RTS00, Juk01. Beame, Koutris and Suciu BKS13, studied a model of communication where $p$ processors, each with memory $n / p^{1-\epsilon}$ attempt to compute with a minimal amount of communication. This model is conceptually related to ours, since each such processor can be thought of as a collection of gates with bounded fan-in. Goldreich and Wigderson GW13 investigated multilinear arithmetic circuits where the gates are allowed to compute arbitrary multilinear functions of a bounded number of inputs. None of these results seem to give non-trivial lower bounds on $C_{k}(f)$.

Clearly, $C_{n}(f)=1$, since $f$ has only $n$ variables. However, when the fan-in is restricted, the power of circuits dramatically decreases. A counting argument shows that for almost every $f, C_{k}(f)>2^{(n-k)-o(n-k)}$, which is exponentially large even for $k$ linear in $n$. On the other hand, one can show that $C_{k}(f) \leq O((n-$ $k) 2^{n-k}$ ) for every $f$. The challenge is to obtain such a lower bound for an explicit function $f$. If $f$ depends on all its inputs, then it is easy to see that $C_{k}(f) \geq n / k$. When $k$ is linear in $n$, this trivial lower bound is just a constant.

Chandra, Furst and Lipton CFL83] defined the Number-on-Forehead model of communication, which we discuss in detail in Section 2.1. They proved lower bounds on branching programs computing the majority function by giving a reduction to the NOF model. The lower bound for the communication model is obtained via Ramsey style argument and displays a tower-like decay. Their reduction is easily adapted to our model as well, yielding super-constant lower bounds on $C_{2 n / 3}$ (Majority). In our work, we use NOF lower bounds to obtain stronger results. We use a different reduction to show: ${ }^{1}$

Theorem 1. There exists $f \in \mathrm{P}$ such that for every $\gamma>0$ and $n$ large enough, $C_{n(1-\gamma)}(f) \geq \Omega\left(\gamma \log ^{2} n\right)$.
The proof is reminiscent of the approaches in Oko93, Ajt02, BNS92, BRS93, BV02 concerning time-space trade-offs for oblivious branching programs.

Next, we define a quantity which is closely related to $C_{k}(f)$. Let $C_{k}^{2}(f)$ denote the smallest number $m$ such that there exist boolean functions $g, f_{1}, \ldots, f_{m}$ with $f=g\left(f_{1}, \ldots, f_{m}\right)$, where every $f_{i}$ reads at most $k$ inputs. We prove:

Theorem 2. There exist $f \in \mathrm{P}, c>0$, such that $C_{(1-\gamma) n}^{2}(f) \geq \Omega\left(n^{c \gamma}\right)$.
The proof of Theorem 2 involves ideas inspired by Nechiporuk's Nec66 lower bound on boolean formula size. We show (Proposition 4) that $C_{k}^{2}(f) \leq C_{k}(f) \cdot 2^{C_{k}(f)}$ for every $f$, and hence Theorem 2 implies a lower bound of $\Omega(\gamma \log n)$ on $C_{n(1-\gamma)}(f)$. In fact, the specific $f$ from Theorem 2 satisfies $C_{2 n / 3}(f) \leq O(\log n)$, showing that $C_{2 n / 3}^{2}$ can be exponentially larger than $C_{2 n / 3}$.

Finally, we observe that Nechiporuk's original proof can be easily extended to formulas with large fan-in. Write $L_{k}(f)$ for the smallest number of leaves in a formula computing $f$ with fan-in at most $k$. Nechiporuk gave an explicit function $f$ for which $L_{2}(f) \geq \Omega\left(n^{2} / \log n\right)$. We prove:

Theorem 3. There exists $f \in \mathrm{P}$ such that $L_{k}(f)=\Omega\left(n^{2} / k \log n\right)$.
Note that for formulas we are counting leaves and not just the non-input gates. Of course, Theorem 3 implies a lower bound of $\Omega\left(n^{2} / k^{2} \log n\right)$ on the number of non-input gates as well.

The lower bound in Theorem 1 is stronger than stated. Consider circuits where the gates can have arbitrarily large fan-in, but each gate can read at most $k$ input variables. Define $C_{k}^{*}(f)$ as the smallest

[^1]number of non-input gates which read some input variable in a circuit computing $f$. Then $C_{k}^{*}(f) \leq C_{k}(f)$. Our lower bound proofs actually give lower bounds on $C_{k}^{*}(f)$ : both Theorem 1 and Proposition 4 work for $C_{k}^{*}$ as well. On the other hand, we always have $C_{k}^{*}(f) \leq n$. Hence, proving a super-linear lower bound on $C_{k}(f)$ requires a technique which fails to work for $C_{k}^{*}(f)$.

In Section 2, we discuss the quantities $C_{k}$ and $C_{k}^{2}$ in greater detail. In Section 3, we give the proofs our lower bounds. In Section 4, we outline the connections between our model and other problems in complexity theory.

## 2 Circuits of medium fan-in

As mentioned in the introduction, counting arguments show that for almost every $f, C_{k}(f)>2^{(n-k)-o(n-k)}$. The bound is exponential even when $k$ is very close to $n$, and super-linear even when $k<n-1.1 \log n$. It becomes sub-linear when $k>n-\log n$. One can check that $C_{n-1.1 \log \log n}(f)=\Omega(\log n)$ for most functions $f$.

The trivial upper bound on the quantity $C_{k}^{2}(f)$ is $n$. The bound is tight even if $k$ very close to $n$ : there exists an $f$ for which $C_{\lfloor n-\log n-1\rfloor}^{2}(f)=n$. Indeed, the number of choices for the functions $g, f_{1}, \ldots, f_{m}$ is at most

$$
2^{2^{m}}\left(\binom{n}{k} 2^{2^{k}}\right)^{m} \leq 2^{2^{m}+m 2^{k}+n m}
$$

In order to realize all $n$-variate functions, we must have $2^{m}+m 2^{k}+n m \geq 2^{n}$. If $m=n-1$ and $k=$ $\lfloor n-\log n-1\rfloor$, the bound is

$$
2^{n-1}+(n-1) 2^{n-1} / n+n^{2}=2^{n}(1-1 /(2 n))+n^{2}<2^{n}
$$

An exercise would show that if $\ell \leq \log n, C_{n-\ell}^{2}(f) \leq 2^{\ell}+\ell$, thus $C_{k}^{2}$ decreases when $k$ goes above $n-\log n$.
The following proposition relates $C_{k}(f)$ to $C_{k}^{2}(f)$.
Proposition 4. $C_{k}^{2}(f) \leq C_{k}(f) \cdot 2^{C_{k}(f)}$.
Proof. Let $u_{1}, \ldots, u_{s}$ be the non-input gates in a circuit of size $s=C_{k}(f)$ where the gate $u_{s}$ evaluates to $f$. For every $i \in[s]$ and every $\sigma:\left\{u_{1}, \ldots, u_{s}\right\} \rightarrow\{0,1\}$, we define a function $f_{i, \sigma}$ that depends on at most $k$ input variables, as follows. $f_{i, \sigma}$ reads the input variables that are read by $u_{i}$, and outputs 1 if and only if there exists some setting of the remaining input variables that could result in the evaluation given in $\sigma$. Define $g$ to be the function that reads the outputs of the $f_{i, \sigma}$ 's and computes $f$ by finding the unique $\sigma$ for which $f_{i, \sigma}=1$ for every $i$. Formally, $f=\bigvee_{\sigma: \sigma\left(u_{s}\right)=1} \bigwedge_{i \in[s]} f_{i, \sigma}$.

Proposition 4 together with Theorem 2 already gives an $\Omega(\log n)$ lower bound on $C_{2 n / 3}(f)$. However, the exponential loss in the transformation means that even an optimal lower bound (of $n$ ) on depth- 2 circuits would give at most a logarithmic lower bound for general circuits. Proposition 5 implies that the exponential loss is inevitable.

### 2.1 Communication complexity

In the Number-On-Forehead model of communication complexity CFL83, there are $p$ parties that are trying to compute a function $f\left(x^{1}, x^{2}, \ldots, x^{p}\right)$, where each $x^{i}$ is a $n / p$-bit string. The $i$ 'th party can see every input except $x^{i}$. To evaluate $f$, the parties exchange messages (by broadcast), until one of the parties can transmit the value of $f$ to the others. The complexity of $f$ is the number of bits the players need to exchange in order to evaluate $f$. Every function can be computed with $n / p$ bits of communication. The strongest lower bounds known are due to Babai, Nisan and Szegedy BNS92]. They proved that the generalized inner product function defined by

$$
\operatorname{GIP}\left(x^{1}, \ldots, x^{p}\right)=\sum_{i=1}^{n / p} \prod_{j=1}^{p} x_{i}^{j} \quad \bmod 2
$$

requires $\Omega\left(n / 2^{2 p}\right)$ bits of communication. They also showed that computing the quadratic character on a sum of numbers requires $\Omega\left(n / 2^{p}\right)$ communication.

The most straightforward connection between circuits and the NOF model is the following observation: Suppose that a circuit computing $f\left(x^{1}, \ldots, x^{p}\right)$ has the property that for every gate $u$ there is some $i \in[p]$ such that $u$ reads no variable from $x^{i}$. Then, if the circuit has s non-input gates, the function $f$ can be evaluated using $s$ bits in the NOF model.
This does not directly imply a circuit lower bound - in a circuit of linear fan-in, gates may access a constant fraction of each of the blocks $x_{i}$. For example, GIP can be computed by a constant size circuit with fan-in $n / 2$ (imagine two gates, one reading the first half of every $x^{i}$, and the other the second half). Nevertheless, this issue can be partially circumvented, as in CFL83 or in our Theorem 1 where we use the GIP function to obtain $C_{2 n / 3}(f) \geq \Omega\left(\log ^{2} n\right)$ for a related function $f$. An explicit function requiring $\Omega(n / p)$ communication in the NOF model would give an explicit function with $C_{2 n / 3}(f) \geq \Omega(\sqrt{n})$.

## 3 The lower bounds

### 3.1 The Nechiporuk method applied to $L_{k}(f)$

The proofs of Theorems 2 and 3 are variations of Nechiporuk's lower bound on formula size, which we now discuss. Given a boolean function $f$ on $n$ variables, a subset of its variables $S$, and an assignment $\sigma$ to the variables outside $S$, we define the function $f_{\sigma}$ be the function obtained by setting the variables outside $S$ to $\sigma$. It is a function in the variables $S$. Any such function is called an $S$-subfunction of $f$. The number of $S$-subfunctions of $f$ is clearly at most $\min \left(2^{2^{|S|}}, 2^{n-|S|}\right)$.

Nechiporuk finds a function $f$ whose input is partitioned into intervals $x_{1}, x_{2}, \ldots, x_{n / \log n}$, each of size $\log n$, such that for every $i, f$ has $2^{\Omega(n)}\left\{x_{i}\right\}$-subfunctions. An example to keep in mind is the element distinctness function:

$$
f\left(x_{1}, \ldots, x_{n / \log n}\right)= \begin{cases}1 & \text { if } x_{1}, \ldots, x_{n / \log n} \text { are distinct } \\ 0 & \text { otherwise }\end{cases}
$$

Observe that whenever $\sigma_{2}, \ldots, \sigma_{n / \log n}$ are distinct, then $f\left(x_{1}, \sigma_{2}, \ldots, \sigma_{n / \log n}\right)$ rejects precisely on the inputs $\sigma_{2}, \ldots, \sigma_{n / \log n}$. Hence $f$ has at least $\binom{n}{n \log ^{-1} n-1}=2^{\Omega(n / \log n)}\left\{x_{1}\right\}$-subfunctions, and likewise for any $\left\{x_{i}\right\}$. A slightly more complicated example gives an explicit $f$ with $2^{\Omega(n)}$ subfunctions.

We now prove Theorem 3, which is a straightforward extension of Nechiporuk's argument for $k=2$ to general $k$. It is however noteworthy that the bound deteriorates only polynomially with $k$.

Claim 1. Let $S$ be a subset of variables of $f$. Assume that $f$ can be computed by a formula with fan-in $k$ in which $m$ leaves correspond to inputs from $S$. Then $f$ has at most $2^{O(m k)} S$-subfunctions.

Proof. Given any such formula computing $f$, let the tree $T$ be obtained by taking the the union of all paths going from some variable in $S$ to the output. If $u, v, w$ is a path in $T$ with $v, w$ having in-degree 1 , then the value of $w$ is determined by the value of $u$ and some function of the inputs from the complement of $S$. We can replace $w$ in our formula by a single gate of fan-in 2 , which takes as input $u$ and a new gate of arbitrarily large fan-in, which only reads inputs from the complement of $S$. This may increase the size of the formula, but the number of leaves from $S$ remains unchanged. Every gate in $T$ still has fan-in at most $k$ in the new formula. Furthermore, $v$ is removed from the tree $T$. We repeat this process until there are no such paths in the tree $T$.

The tree $T$ now has $m$ leaves and at most $4 m$ nodes, since there are no edges connecting gates of in-degree 1 in $T$. Every $S$-subfunction can be described using $4 m k$ bits as follows. For each gate $v$ in $T$, it is enough to specify the inputs to $v$ coming from outside of $T$. Since the fan-in of every such gate is at most $k$, there will be at most $4 m k$ such inputs. Thus $f$ has at most $2^{4 m k} S$-subfunctions.

Applying the claim to the function above, we obtain that every formula computing $f$ contains $\Omega(n / k)$ leaves labelled with a variable from $x_{i}$, for every $i \in\{1, \ldots, n / \log n\}$. This means that any such formula contains $\Omega\left(\frac{n^{2}}{k \log n}\right)$ leaves altogether.

### 3.2 Proof of Theorem 2

In order to prove our theorem, we will find a function $f$ that has a stronger property with regards to its subfunctions. Namely, $f$ will have many $S$ subfunctions not just for $S$ coming from a fixed partition of the inputs; it will have many $S$-subfunctions for almost every $\log n$-element set $S$.

We define our hard function as follows. $f(x, y)$ will take as inputs $x \in\{0,1\}^{\ell+\log \ell}$ and a $O\left(\log ^{2} \ell\right)$-bit string $y$. Thus $f$ is a function of $n=\ell+O\left(\log ^{2} \ell\right)$ bits in total. We view $y$ as representing a subset $S_{y} \subset[\ell+\log \ell]$ of $\log \ell$ variables from $x$. Let $x_{S_{y}}$ be the projection of $x$ to the variables in $S_{y}$. We view the $\log \ell$-bit string $x_{S_{y}}$ as an element of $[\ell]$. Let $S_{y}^{c}$ denote the complement of $S_{y}$. Then define the function $f(x, y)$ to output the $x_{S_{y}}$ 'th bit of $x_{S_{y}^{c}}$.

Given a fixed $y$, each setting of $x_{S_{y}^{c}}$ gives a distinct $S_{y^{-}}$-subfunction of $f(x, y)$. Thus,
Claim 2. For every $\log \ell$-element subset $S$ of the variables $x$, $f$ has $2^{\ell} S$-subfunctions.
To prove Theorem 2, it will be enough to show that any small circuit gives an upper bound on the number of $S$-subfunctions of $f$, for some $\log \ell$ element subset $S$ of the variables in $x$. Suppose that

$$
f=g\left(f_{1}, \ldots, f_{m}\right)
$$

First we observe:
Claim 3. For every $0<\gamma<1$, there is a constant $0<c<1 / 2$ such that if $\ell>100$ and $m<\ell^{c \gamma / 2}$, then there exists a $\log \ell$-element subset $S$ of the variables $x$ such that each $f_{i}$ reads at most $(1-\gamma / 2) \log \ell$ of the variables from $S$.

Proof. Pick $\log \ell$ variables $a_{1}, \ldots, a_{\log \ell}$ from $x, y$ uniformly at random. With high probabilty, they will be distinct and they will completely miss the variables $y$; the probability being larger than $1 / 2$ if $\ell>100$. For a given $i$, let $X$ be the random variable that counts the number of variables of $S$ that are read by the gate $f_{i}$. The Chernoff bound gives,

$$
\operatorname{Pr}\left[\frac{X}{\log n} \geq 1-\gamma / 2\right] \leq e^{-\mathrm{D}(1-\gamma / 2| | 1-\gamma) \log \ell}<\ell^{-c \gamma}
$$

where $D(1-\gamma / 2 \| 1-\gamma)=\gamma / 2 \ln (1 / 2)+(1-\gamma / 2) \ln ((1-\gamma / 2) / 1-\gamma)$ is the Kullback-Leibler divergence. As $\gamma$ approaches 0 , the divergence becomes roughly $\gamma / 2 \ln (1 / 2)+\gamma / 2>0.15 \gamma$; as $\gamma$ approaches 1 it goes to infinity. Hence we indeed have $D(1-\gamma / 2 \| 1-\gamma) \geq c^{\prime} \gamma$ for some constant $c^{\prime}>0$ and every $\gamma \in(0,1)$. If $m<\ell^{c \gamma} / 2$, the union bound gives that there is a $\log \ell$-element set $S$ as required.

If $m \leq \ell^{c \gamma} / 2$, let $S$ be the set promised by Claim 3 . For every $i \in[m]$, the number of $S$-subfunctions of $f_{i}$ is at most $2^{2^{(1-\gamma / 2) \log \ell}}=2^{\ell^{1-\gamma / 2}}$, since each $f_{i}$ reads at most $(1-\gamma / 2) \log \ell$ variables from $S$. Each $S$ subfunction of $f$ is uniquely determined by the $S$-subfunctions of $f_{1}, \ldots, f_{m}$, and so $f$ has at most $2^{\ell^{1-\gamma / 2} m}$ $S$-subfunctions. By Claim 2, this means that $m \geq \ell^{\gamma / 2}>\ell^{c \gamma} / 2$. Hence, $C_{(1-\gamma) n}^{2}(f) \geq \ell^{c \gamma} / 2=\Omega\left(n^{c \gamma}\right)$, proving Theorem 2

### 3.2.1 A Matching Upper Bound for $f(x, y)$

We will now show that the lower bound from Theorem 2 is tight for the function $f(x, y)$ defined abov ${ }^{2}$, thus the exponential gap between $C_{k}$ and $C_{k}^{2}$ from Proposition 4 is inevitable.

[^2]Proposition 5. There exists $c>0$ such that for every $0<\gamma<1 / 2$ and $n$ sufficiently large, $C_{(1-\gamma) n}^{2} f(x, y) \geq$ $n^{c \gamma}$ and $C_{(1-\gamma) n} f(x, y) \leq c \gamma \log n$.

Proof. It is enough to prove the bound for $C_{(1-\gamma) n}$ and invoke Proposition 4. We will outline the construction for $\gamma=1 / 2$ and then sketch how to adapt it to the general case. Divide the variables $x$ into two equal subsets $x_{1}$ and $x_{2}$. Let $g_{1}$ be the function which, on inputs $x_{1}$ and $y$, outputs a $\log n$-bit string whose first bits equal $x_{1}$ restricted to $S_{y}$. Define $g_{2}$ similarly. This means that $x_{S_{y}}$ can be recovered from $x_{2}, y$ and the advice from $g_{1}$; likewise for $x_{1}, y$ and $g_{2}$. It is now easy to see that we can write $f(x, y)=h_{1}\left(g_{1}, x_{2}, y\right) \vee h_{2}\left(g_{2}, x_{1}, y\right)$ with suitable $h_{1}$ and $h_{2}$. This gives approximately $\log n$ gates with fan-in approximately $n / 2$.

In general, partition the variables $x$ into $r$ disjoint subsets $a_{1}, \ldots, a_{r}$ of nearly the same size. The gates will have access to the inputs $y$ and $x \backslash a_{i}$ for some $i \in[r]$. Note that for any $\log \ell$ element subset $S$ of $x$, there will exist two distinct $a_{i}$ and $a_{j}$ with $\left|a_{i} \cap S\right|,\left|a_{j} \cap S\right| \leq 2 \log \ell / r$. We can recover $x_{S_{y}}$ from $x \backslash a_{i}$ with an advice of $2 \log n / r$ bits, and as above, compute $f(x, y)$ using two gates depending $y, x \backslash a_{i}$ and $y, x \backslash a_{j}$ and $2 \log n / r$ bits of advice each. The advice itself can be computed by gates which have access to either $y, x \backslash a_{1}$ or $y, x \backslash a_{2}$. This gives a circuit with roughly $8 \log n / r+r$ gates of fan-in $(1-1 / r) n$; this is at most $10 \log n / r$ gates for fixed $r$ and large enough $n$.

### 3.3 Proof of Theorem 1

We will deduce a lower bound on $C_{k}(f)$ from known NOF lower bounds. The main issue with the reduction to the NOF model is that any gate in the circuit may read an arbitrary set of inputs (perhaps even one bit from every party's forehead).

One way to simulate any circuit with linear fan-in and $m$ gates using $m$ parties is to associate every gate with a party and then greedily assign variables to parties, giving inputs of length $\Omega(n / m)$ for each of the $m$ parties. We manage to reduce the number parties to $O(m / \log n)$, which helps us obtain stronger lower bounds. This is done using the following Lemma:

Lemma 6. Let $G \subseteq A \times B$ be a bipartite graph with $|A|=m,|B|=n$ and with every $a \in A$ having degree at least $\gamma n$, where $0<\gamma<1 / 100$ and $n$ is sufficiently large with respect to $\gamma^{-1}$. If $\log n \leq m \leq \log ^{2} n$, then there exists $p \leq 5 \mathrm{~m} / \gamma$ and disjoint $T_{1}, \ldots, T_{p} \subseteq A, S_{1}, \ldots, S_{p} \subseteq B$, each $S_{i}$ of size at least $n^{0.9}$, such that $A=\bigcup T_{i}$ and $\left(T_{i} \times S_{i}\right) \subseteq G$ for every $i \in[p]$.

Proof. We first prove the following:
Claim. If $m \leq \log n$, $G$ contains a complete bipartite graph with at least $\gamma m / 2$ vertices on the left and $2 n^{0.9}$ vertices on the right.

Proof. Remove from $B$ all vertices with degree $\leq \gamma m / 2$. Since the graph has at least $\gamma m n$ edges to begin with, the remaining set of vertices $B^{\prime}$ has size at least $\gamma n / 2$. For $M \subseteq A$, let $B(M)$ be the set of $b \in B^{\prime}$ such that $b$ is connected to every $a \in M$. Hence,

$$
B^{\prime}=\bigcup_{|M|=\lceil\gamma m / 2\rceil} B(M) .
$$

Since $m \leq \log n$ and $\gamma<1 / 100$, the number of sets with $|M|=\lceil\gamma m / 2\rceil$ is at most $n^{0.09}$. So there is such an $M$ with $B(M) \geq \frac{\gamma n / 2}{n^{0.09}} \geq 2 n^{0.9}$, for $n$ large enough.

We iteratively apply the Claim to prove the Lemma. If $m>\log n$, choose an arbitrary $\log n$-element subset of $A$ and let $T_{1} \times S_{1}$ be the complete graph guaranteed by the Claim. If $m \leq \log n$, apply the Claim directly to $G$. Remove from $G$ all the vertices $T_{1}$ and $S_{1}$, obtaining a new graph $G_{2} \subseteq A_{2} \times B_{2}$. Repeat this process $p$ times to obtain graphs $G_{2}, \ldots, G_{p}$ until $A_{p}=\emptyset$. We claim that $p \leq 5 m /(\gamma \log n)$. For such a small $p$, we have altogether removed $o(n)$ vertices from $B$ and so $\left|B_{i}\right| \geq n(1-o(1))$ for every $i=1,2, \ldots, p$. Similarly, the degree of any $a \in A_{i}$ is at least $\gamma\left|B_{i}\right| / 2$. Hence, as long as $\left|A_{i}\right| \geq \log \left|B_{i}\right|$, we remove at least $\gamma \log n / 4$ vertices from $A_{i}$. After at most $4 m /(\gamma \log n)$ steps, we then must have $\left|A_{i}\right|<\log \left|B_{i}\right|$. After this
point, $A_{i}$ decreases by at least the factor of $(1-\gamma / 2)$, and so the size drops below 1 in roughly $\log \log n / \gamma$ steps, which is much smaller that $m / \gamma$. Finally, the size of every $S_{i}$ is at least $\left|B_{i}\right|^{0.9}=2(n(1-o(1)))^{0.9} \geq n^{0.9}$, if $n$ is large enough.

Our hard function $f(x, y)$ is defined as follows. It takes as inputs $x \in\{0,1\}^{\ell}$ and an auxiliary string $y$. We think of $y$ as defining $p \leq \log \ell$ disjoint subsets $S_{y}^{1}, \ldots, S_{y}^{p}$ of $[\ell]$, of equal size not exceeding $\ell^{0.9}$. Hence, $y$ can be taken as roughly $\ell^{0.9} \log ^{2} \ell$-bit string. We define

$$
f(x, y):=\operatorname{GIP}\left(x_{S_{y}^{1}}, \ldots, x_{S_{y}^{p}}\right) .
$$

$f(x, y)$ has $n=\ell+O\left(\ell^{0.9} \log ^{2} \ell\right)$ variables. As before, $x_{S_{y}^{i}}$ is the projection of $x$ to $S_{y}^{i}$.
Suppose that for a fixed $0<\gamma$ and $n$ sufficiently large, $f(x, y)$ can be computed using $m<\gamma \log ^{2} n / 50$ non-input gates with fan-in $n(1-\gamma)$. Take the graph $G$ whose left vertices are the $m$ gates of the circuit and the right vertices the $\ell$ variables of $x$. There is an edge between a gate and a variable whenever the gate does not read the variable. Since $y$ is much shorter than $x$, the degree of a gate in $G$ is at least $\gamma \ell / 2$. To apply the Lemma, we will assume $\gamma<1 / 100$ (otherwise the circuit is weaker) and that $m \geq \log \ell$. The Lemma shows that there exist disjoint sets of variables $S_{1}, \ldots, S_{p}$ with $p \leq \log n / 5$ and $S_{i}=\left\lfloor\ell^{0.9}\right\rfloor$ such that each gate completely misses at least one set $S_{i}$. We can fix $y$ so that $y$ represents $S_{1}, \ldots S_{p}$ and hence $f(x, y)$ computes $\operatorname{GIP}\left(x_{S_{1}}, \ldots, x_{S_{p}}\right)$. As observed in Section 2.1, the circuit gives an $m$-bit protocol for $\operatorname{GIP}\left(x_{S_{1}}, \ldots, x_{S_{p}}\right)$. By the results of BNS92, this implies $m \geq \Omega\left(\ell^{0.9} 2^{-2 \log \ell / 5}\right)=\Omega(\sqrt{\ell})$, contradicting the assumption that $m<\gamma \log ^{2} n / 50$. This proves Theorem 1.

## 4 Connections to Other Models

Here we show how is our model is connected to several disparate problems in complexity theory.

### 4.1 Circuits of Linear Size and Logarithmic Depth

Obviously, $C_{k}(f) \leq C_{2}(f)$, so any super-linear lower bound in our model would give a super-linear lower bound for circuits of fan-in 2. However, even a linear lower bound on our model would give a function that cannot be computed by a linear sized logarithmic depth circuit:

Proposition 7. If $f$ has a fan-in 2 circuit of linear size and logarithmic depth, then for any $\epsilon>0, C_{n^{\epsilon}}(f)<$ $O\left(\frac{n \log (1 / \epsilon)}{\log \log n}\right)$.

Valiant Val77 showed that any (fan-in 2) circuit of linear size and logarithmic depth contains a set $T$ of $O\left(\frac{n \log (1 / \epsilon)}{\log \log n}\right)$ gates such that every path of length $\epsilon \log n$ in the circuit must touch a gate from the set. Since every such gate in $T$ can be computed from at most $n^{\epsilon}$ other gates from $T$ and the inputs, we obtain Proposition 7

### 4.2 Oblivious Branching Programs

An oblivious branching program of width $w$ and length $\ell$ is a directed graph with vertices partitioned into $\ell$ layers $L_{1}, \ldots, L_{\ell}$. Each layer is associated with an input variable. Every vertex in $L_{i}$ has out-degree 2, with the edges labeled 0,1 . Every vertex of $L_{\ell}$ is labeled with an output value. The program is executed by starting at the first vertex of $L_{1}$, and reading the variables in turn to find a path through the program until the output is determined.

Barrington Bar89 showed that every logarithmic depth circuit (of fan-in 2) can be simulated by a branching program with $w=5, \ell=\operatorname{poly}(n)$. Thus it is very interesting to prove super-polynomial lower bounds on such programs. A line of work has proved time-space tradeoffs on such programs. Alon and Maass AM86 used reductions to Ramsey theory to show that any program for computing the majority
function must have $\ell \log w \geq \omega(n \log n)$. Babai, Nisan and Szegedy BNS92 proved a lower bound of $\ell \log w \geq \Omega\left(n \log ^{2} n\right)$ by reductions to the Number-on-Forehead communication model. Beame and Vee BV02] simplified the proof of this last bound. No better lower bound on $\ell \log w$ is known, to our knowledge.

Our results give lower bounds that match those of BNS92] via the following proposition:
Proposition 8. If $f$ can be computed by an oblivious branching program of width $w<2^{\epsilon n / 2}$ and length $\ell$, then $C_{\epsilon n}(f) \leq \frac{2 \ell \log w}{\epsilon n}$.

The first $\log w$ gates of the circuit read the first $\epsilon n / 2$ variables read by the program and together compute the name of the vertex reached after those layers. The next $\log w$ gates read the outputs of the previous gates and the next $\epsilon n / 2$ variables, to compute the name of the vertex in layer $L_{\epsilon n}$. Continue in this way until all of the program has been simulated. Thus we obtain a lower bound of $\ell \log w \geq \Omega\left(n \log ^{2} n\right)$ on the length of the program using Proposition 8 and Theorem 1 . Any lower bound of the type $C_{\epsilon n}(f)=\omega\left(\log ^{2} n\right)$ would give new time-space tradeoffs for branching programs.

## 4.3 $\quad \mathrm{AC}_{0}$

An $\mathrm{AC}_{0}$ circuit is a circuit of constant depth that uses AND, OR-gates of unbounded fan-in and NOT-gates. As negations can be moved to the leaves, the depth of $A C_{0}$ circuit is defined as the largest number of AND, OR-gates on a path in the circuit. Any size $s \mathrm{AC}_{0}$ circuit can be simulated by a size $s^{2}$ circuit with gates of fan-in 2.

Beautiful methods have been developed to prove lower bounds on these circuits Has86, Raz87, Smo87. The best known lower bounds for a depth $d$ circuit are of the type $2^{\Omega\left(n^{1 /(d-1)}\right)}$. The following proposition shows that truly exponential lower bounds would give linear lower bounds in our model.

Proposition 9. There is a depth-3 $\mathrm{AC}_{0}$ circuit of size $O\left(k C_{k}(f) \cdot 2^{C_{k}(f)+k}\right)$ computing $f$.
To see this, observe that the function $g$ defined in the proof of Proposition 4 can be computed by a monotone formula in disjunctive normal form, with $C_{k}(f) \cdot 2^{C_{k}(f)}$ leaves. Furthermore, each $f_{i, \sigma}$ depends on $k$ variables, and hence it can be computed by a formula in conjuctive normal form, with $k \cdot 2^{k}$ leaves. This gives depth-3 $\mathrm{AC}_{0}$ formula with $k C_{k}(f) \cdot 2^{C_{k}(f)+k}$ leaves.

Propositions 7 and 9 together imply that if $f$ cannot be computed by a depth- $3 \mathrm{AC}_{0}$ circuit of size $2^{k}$, then $C_{k}(f) \geq \Omega(k)$. If $k \gg \sqrt{n}$, this connection gives non-trivial lower bounds on $C_{k}(f)$. In addition, truly exponential lower bounds on the circuit size of a depth-3 $\mathrm{AC}_{0}$ circuit computing $f$ would imply that $C_{k}(f) \geq \Omega(n)$, and so $f$ does not have linear sized circuit of logarithmic depth, an observation already made by Valiant Val77.

### 4.4 Extractors for Varieties

Given a field $\mathbb{F}$, a variety is a set of the form $\left\{x \in \mathbb{F}^{n}: f_{1}(x)=f_{2}(x)=\ldots f_{m}(x)=0\right\}$, where $f_{1}, \ldots, f_{m}$ are polynomials. For a finite field $\mathbb{F}$, an extractor for varieties is a function $f: \mathbb{F}^{n} \rightarrow\{0,1\}$ which is non-constant on any sufficiently large variety defined by low-degree polynomials.

Dvir Dvi12 showed how to use bounds on exponential sum estimates by Deligne Del74 to obtain extractors for varieties. Working over a prime field of size $p$, he shows that if $\rho>1 / 2$ is a constant, and $V \subseteq \mathbb{F}^{n}$ is a variety of size $p^{\rho n}$ defined by polynomials of degree $\rho n$, then there is an efficiently computable extractor for such varieties, as long as $p$ is polynomially large in $n$. Here we show that such a result for $p=2$ would imply non-trivial circuit lower bounds.

Proposition 10. Let $p=2$. If $f$ is an extractor for varieties of size $2^{\rho n}$ defined by degree $k$ polynomials, then $C_{k}(f)>(1-\rho) n$.

Proof. Suppose there is a circuit computing $f$ with $m$ gates of fan-in $k$. By averaging, there must exist some evaluation of the gates which is consistent with $2^{n-m}$ input strings. We now define a variety using $m$ polynomials as follows. Each polynomial checks that the input is consistent with the evaluations of a single
gate. Since every such polynomial depends on at most $k$ variables, and it can be taken multilinear, it has degree at most $k$. Thus we obtain a variety of size $2^{n-m}$ defined by degree $k$ polynomials on which $f$ is constant. So it must be that $n-m<\rho n \Rightarrow m>(1-\rho) n$.

By Proposition 7, any such extractor cannot be computed by linear sized logarithmic depth circuits of fan-in 2.

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## References

[Ajt02] Miklós Ajtai. Determinism versus nondeterminism for linear time RAMs with memory restrictions. Journal of Computer and System Sciences, 65(1):2-37, 2002.
[AM86] Noga Alon and Wolfgang Maass. Meanders, ramsey theory and lower bounds for branching programs. In FOCS, pages 410-417. IEEE Computer Society, 1986.
[BNS92] László Babai, Noam Nisan, and Mario Szegedy. Multiparty protocols, pseudorandom generators for logspace, and time-space trade-offs. J. Comput. Syst. Sci, 45(2):204-232, 1992.
[Bar89] David A. Mix Barrington. Bounded-width polynomial-size branching programs recognize exactly those languages in NC ${ }^{1}$. J. Comput. Syst. Sci, 38(1):150-164, 1989.
[BKS13] Paul Beame, Paraschos Koutris, and Dan Suciu. Communication steps for parallel query processing. In Richard Hull and Wenfei Fan, editors, PODS, pages 273-284. ACM, 2013.
[BV02] Paul Beame and Erik Vee. Time-space tradeoffs, multiparty communication complexity, and nearest-neighbor problems. In John H. Reif, editor, STOC, pages 688-697. ACM, 2002.
[BRS93] Allan Borodin, Alexander A. Razborov, and Roman Smolensky. On lower bounds for read-K-times branching programs. Computational Complexity, 3:1-18, 1993.
[CFL83] A. Chandra, M. Furst, and R. Lipton. Multi-party protocols. In In Proceedings of the fifteenth annual ACM symposium on Theory of computing, STOC, pages 94-99, 1983.
[Che05] D. Y. Cherukhin. The lower estimate of complexity in the class of schemes of depth 2 without restrictions on a basis. Moscow University Mathematics Bulletin, 60(4):42-44, 2005.
[Del74] Pierre Deligne. La conjecture de weil : I, 1974.
[Dvi12] Zeev Dvir. Extractors for varieties. Computational Complexity, 21(4):515-572, 2012.
[GW13] Oded Goldreich and Avi Wigderson. On the size of depth-three boolean circuits for computing multilinear functions. Electronic Colloquium on Computational Complexity (ECCC), 20:43, 2013.
[Has86] Johan Hastad. Almost optimal lower bounds for small depth circuits. In Proceedings of the Eighteenth Annual ACM Symposium on Theory of Computing, pages 6-20, Berkeley, California, 28-30 May 1986.
[Juk01] S. Jukna. Extremal combinatorics, with applications in computer science. Springer-Verlag, 2001.
[Nec66] E. I. Nechiporuk. A boolean function. Sov.Math.Dokl., 7(4):999-1000, 1966.
[Oko93] Elizaveta Okolnishnikova. On lower bounds for branching programs. Siberian Advances in Mathematics, 3(1):152-166, 1993.
[PRS97] P. Pudlák, V. Rö̈l, and J. Sgall. Boolean circuits, tensor ranks, and communication complexity. SIAM J. Comput., 26(3):605-633, 1997.
[RTS00] J. Radhakrishnan and A. Ta-Shma. Bounds for dispersers, extractors, and depth-two superconcentrators. SIAM J. Discrete Math. 13(1): 2-24, 13(1):2-24, 200.
[Raz87] Alexander Razborov. Lower bounds on the size of bounded depth circuits over a complete basis with logical addition. MATHNASUSSR: Mathematical Notes of the Academy of Sciences of the USSR, 41, 1987.
[Smo87] Roman Smolensky. Algebraic methods in the theory of lower bounds for Boolean circuit complexity. In Proceedings of the Nineteenth Annual ACM Symposium on Theory of Computing, pages 77-82, New York City, 25-27 May 1987.
[Val77] Leslie G. Valiant. Graph-theoretic arguments in low-level complexity. In Jozef Gruska, editor, MFCS, volume 53 of Lecture Notes in Computer Science, pages 162-176. Springer, 1977.


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[^1]:    ${ }^{1}$ Abusing notation, we write $f \in \mathrm{P}$ to mean that $f$ is obtained by restricting a polynomial time computable function to $n$-bit inputs.

[^2]:    ${ }^{2}$ In the case when $\gamma$ is fixed and $n$ grows independently.

