

Small hitting-sets for tiny arithmetic circuits or: How to turn bad designs into good

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Abstract

Research in the last decade has shown that to prove lower bounds or to derandomize polynomial identity testing (PIT) for general arithmetic circuits it suffices to solve these questions for restricted circuits. In this work, we study the smallest possibly restricted class of circuits, in particular depth-4 circuits, which would yield such results for general circuits (that is, the complexity class VP). We show that if we can design $\operatorname{poly}(s)$ -time hitting-sets for $\Sigma \wedge^a \Sigma \Pi^{O(\log s)}$ circuits of size s, where $s = \omega(1)$ is arbitrarily small and the number of variables, or arity s = n, is s = n, then we can derandomize blackbox PIT for general circuits in quasipolynomial time. Further, this establishes that either $s \not = n$ poly or that s = n or that s = n diagonal depth-4. Note that these are merely polynomials with arity s = n and degree s = n fact, we show that one only needs a s = n poly s = n poly s = n family against individual-degree s = n family polynomials that are computable by a size-s = n arity-(s = n) s = n family may be s = n. Alternatively, we claim that, to understand VP one only needs to find hitting-sets, for depth-3, that have a small parameterized complexity.

Another tiny family of interest is when we restrict the arity $n = \omega(1)$ to be arbitrarily small. In parameterized complexity terms: We show that if we can design poly $(s, \mu(n))$ -time hitting-sets for size-s arity- $n \Sigma \Pi \Sigma \wedge$ circuits (resp. $\Sigma \wedge^a \Sigma \Pi$), where function μ is arbitrary, then we can solve PIT for VP in quasipoly-time, and prove the corresponding lower bounds.

Our methods are strong enough to prove a surprising arity reduction for PIT– to solve the general problem completely it suffices to find a blackbox PIT with time-complexity $sd2^{O(n)}$. This suggests that, in algebraic-geometry terms, PIT is inherently an 'extremely low'-dimensional (or 'extremely low' individual-degree) problem.

One expects that with this severe restriction on n,a and the semantic individual-degree, it should be at least "exponentially" easier to design hitting-sets. Indeed, we give several examples of $(\log s)$ -variate circuits where a new measure (called cone-size) helps in devising poly-time hitting-sets, but the same question for their s-variate versions is open till date: For eg., diagonal depth-3 circuits, and in general, models that have a small partial derivative space. The latter models are very well studied, following (Nisan & Wigderson, FOCS'95 [NW95]), but no $sd2^{O(n)}$ -time PIT algorithm was known for them.

We also introduce a new concept, called *cone-closed basis* isolation, and provide example models where it occurs, or can be achieved by a small shift. This refines the previously studied notions of low-support (resp. low-cone) rank concentration and least basis isolation in certain ABP models. Cone-closure holds special relevance in the low-arity regime.

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1 Introduction

The Polynomial Identity Testing (PIT) problem is to decide whether a multivariate polynomial is zero, where the input is given as an arithmetic circuit. An arithmetic circuit over a field \mathbb{F} is a layered acyclic directed graph with one sink node called output node; source nodes are called input nodes and are labeled by variables or field constants; non-input nodes are labeled by \times (multiplication gate) and + (addition gate) in alternate layers. Sometimes edges may be labeled by field constants. The computation is defined in a natural way. The complexity parameters of a circuit are: 1) size- maximum number of edges and vertices, 2) depth- maximum number of layers, and 3) degree- maximum degree among all polynomials computed at each node. This is sometimes called the syntactic degree, to distinguish from the (semantic) degree of the final polynomial computed. The families of circuits, that are n-variate poly(n)-size and poly(n)-degree, define the class VP [Val79]; also see [Bür13] for interesting variants of this algebraic computing model.

In this work we study n-variate polynomials computable by circuits of size $\leq s$ of individual degree $\leq a$, where one of the parameter is tiny as compared to the others. For example, we study such polynomials where the number of variables n is very small, such as $n \leq O(\log s)$. When $a \leq O(1)$ this model is equivalent to that of poly(s)-size depth-2 circuits, which are well-understood even in the blackbox model [BOT88], and thus we probe these polynomials when $a \geq \omega(1)$. We can even approach this question when n is $\omega(1)$ but arbitrarily small, in which case we now consider polynomials of individual degree O(s) to again avoid collapsing to poly(s)-size depth-2 circuits. Basically, we need to allow the number of monomials a^n to grow as $s^{\omega(1)}$ for the model to be nontrivial, and we demonstrate that this is precisely the chasm to be crossed to get a fundamentally new understanding of VP.

The polynomial computed by a circuit may have, in the worst-case, an exponential number of monomials compared to its size. So, by computing the explicit polynomial from input circuit, we cannot solve PIT problem in polynomial time. However, evaluation of the polynomial at a point can be done, in time polynomial in the circuit size, by assigning the values at input nodes. This helps us to get a polynomial time randomized algorithm for PIT by evaluating the circuit at a random point, since any nonzero polynomial evaluated at a random point gives a nonzero value with high probability [DL78, Zip79, Sch80]. However, finding a deterministic polynomial time algorithm for PIT is a long-standing open question in arithmetic complexity theory. It naturally appears in the algebraic approaches to the $P\neq NP$ question, eg. [GMQ16, Gro15, Mul12b, Mul12a]. The famous algebraic analog is the $VP\neq VNP$ question [Val79]. The PIT problem has applications both in proving circuit lower bounds [KI03, Agr05] and in algorithm design [MVV87, AKS04, KSS14, DdOS14]. For more details on PIT, see the surveys [Sax09, Sax13, SY10].

PIT algorithms are of two kinds: 1) whitebox- allowed to see the internal structure of the circuit, and 2) blackbox- only evaluation of the circuit is allowed at points in a small field extension. Blackbox PIT is equivalent to efficiently finding a set of points, called a hitting-set \mathcal{H} , such that for any circuit C, in a family \mathcal{C} , computing a nonzero polynomial, the set \mathcal{H} must contain a point where $C \neq 0$. For us a more functional approach would be convenient. We think in terms of an n-tuple of univariates $\mathbf{f}(y) = (f_1(y), \ldots, f_n(y))$ whose set of evaluations contain \mathcal{H} . Such an $\mathbf{f}(y)$ can be efficiently obtained from a given \mathcal{H} (using interpolation) and vice-versa. Clearly, if \mathcal{H} is a hitting-set for \mathcal{C} then $C(\mathbf{f}(y)) \neq 0$, for any nonzero $C \in \mathcal{C}$. This tuple of univariates is called a hitting-set generator (Sec.2) and we will freely exploit this connection in all the proofs or theorem statements.

Existing deterministic algorithms solving PIT for restricted classes have been developed by leveraging insight into the weaknesses of these models. For example, deterministic PIT algo-

rithms are known for subclasses of depth-3 circuits [KS07, Sax08, SS12], subclasses of depth-4 circuits [ASSS12, BMS13, SSS13, For15, KS16a, KS16b, PSS16], read-once arithmetic branching programs (ROABP) and related models [FS12, ASS13, FSS14, AGKS15, GKST16, GKS16], certain types of symbolic determinants [FGT16, GT16], as well as non-commutative models [GGOW16]. An equally large number of special models have been used to prove lower bound results, see for example the ongoing survey of Saptharishi [Sap16].

While studying such restricted models may at first seem to give limited insight into general circuits, various works (discussed below) have shown this not to be the case as full derandomization of PIT for depth-4 (resp. depth-3) circuits would imply derandomization of PIT for general circuits. The goal of this work is to sharpen this connection by additionally limiting the *number of variables* (resp. semantic individual-degree) in the depth-4 circuit, and showing that such a connection still holds. In doing so we establish new concepts for studying this small-variable regime, and show how to derive polynomial-size hitting sets for some small-variable circuit classes where only quasipolynomial-size, but not poly-sized, hitting-sets were previously known.

1.1 Main results

Arithmetic circuits were defined with the hope that they would have better structure than boolean circuits. Indeed, unlike boolean circuits, any VP circuit of arbitrary depth can be reduced nontrivially to depth-4 [AV08, Koi12, Tav15, CKSV16] or depth-3 [GKKS13]. As a consequence, the lower bound questions against VP reduce to the lower bound questions for depth-4 (or to depth-3 for selected fields). In circuit complexity the base field \mathbb{F} of interest is either \mathbb{Q} or \mathbb{F}_q (for a prime-p-power q). Other popular fields, eg. number field, function field or p-adic field, are dealt with using similar computational methods. In this paper, unless stated otherwise, we assume $\mathbb{F} = \mathbb{Q}$. (Though many of our ideas would generalize to other base rings.)

The PIT question for VP circuits reduces even more drastically. The reason is that now one invokes circuit factorization results [Kal89] that use algebra in a way heavier than the depthreduction results. So we will invoke that VP is closed under factorization, in addition to the fact that it affords depth-reduction. Recall the $\Sigma\Pi\Sigma\Pi$ (resp. $\Sigma\wedge\Sigma\Pi$) model that computes a polynomial by summing products (resp. powers) of sparse polynomials (see Defn.12). In 2008, Agrawal and Vinay [AV08, Thm.3.2] showed that solving blackbox PIT in poly(s)-time for sizes s-variate depth-4 circuits of the form $\Sigma\Pi^a\Sigma\Pi^{O(\log s)}$ (Defn.12), where a is any unbounded function, gives an $(sd)^{O(\log sd)}$ -time hitting-set for VP (size-s degree-d). Here, we weaken the hypothesis further. We show that solving blackbox PIT in poly(s)-time for size-s $O(\log s)$ $variate \ \Sigma \wedge^a \Sigma \Pi^{O(\log s)}$ circuits, where a is an arbitrarily small unbounded function and semantic individual-degree a' is also arbitrarily small, is sufficient to get an $(sd)^{O(\log sd)}$ -time hitting-set for VP. We note that the brute-force deterministic algorithm would run here in time $a^{O(\log s)} =$ $s^{O(\log a')}$, and thus we show that reducing this runtime to polynomial would have dramatic consequences. We call such depth-4 circuits as tiny diagonal depth-4 (a better definition is Defn. 12). Compared to the previous result, one advantage in our model is that an exponential running time, wrt the number of variables (arity n), is allowed. Formally, we design an efficient arity reducing polynomial-map (the polynomials designed have individual-degree O(1)). Clearly, the map can be used to also deduce about the quasipoly-time blackbox PIT for VP.

Theorem 1. Suppose we have poly-time hitting-sets for a tiny diagonal depth-4 model. Then, we design a poly(sd)-time arity reducing $(n \mapsto O(\log sd))$ polynomial-map of constant individual-degree that preserves the nonzeroness of any n-variate size-s degree-d arithmetic circuit.

By the known depth-3 chasm [GKKS13], the hypothesis in Thm.1 can be weakened to: if, for an arbitrary function μ' , we have a $poly(s, 2^n, \mu'(a'))$ -time hitting-set for size-s arity-n

depth-3 circuits that compute polynomials of semantic individual-degree $\leq a'$, then \cdots . The proof sketch is given in Sec.A (Thm.14), where also the case of a tiny width-2 ABP (Thm.15) and a 'multilinear tiny' depth-3 variant (Thm.16) are discussed. Note that the sparsity of a polynomial computed by tiny diagonal depth-4 is $a'^n = s^{O(\log a')}$ which gives us a brute-force hitting-set of similar complexity [BOT88]. We want to bring it down to $s^{O(1)}$; leaving us with an arbitrarily small gap to close algorithmically. Our methods show that any of these hitting-set designs will establish: Either $\mathbb{E} \not\subseteq \mathbb{H}P/\text{poly}$ or VNP has polynomials of arithmetic circuit complexity $2^{\Omega(n)}$ (Lem.13, Cor.7). Note that these are long-standing open questions [NW94, Val79]. Their connection with PIT, in our results, is a significant strengthening of [KI03] who had first proved: PIT in poly-time implies that either NEXP $\not\subseteq P/\text{poly}$ or VNP \neq VP.

Moreover, we get the following curious property of PIT. (In some sense, it signifies: tiny-VP PIT implies VP PIT.)

Theorem 2 (PIT arity reduction). If we have $poly(sd2^n)$ -time hitting-set for size-s degree-d arity-n circuits, then for general circuits we have a poly(sd)-time hitting-set (and we get an E-computable polynomial with exponential arithmetic complexity).

One now wonders whether the hypothesis, in the theorem above, can be further weakened. We give a partial answer by studying the model $\Sigma\Pi\Sigma\wedge$ (i.e. a sum of products, where each factor is a sum of univariate polynomials).

Theorem 3 (Tinier arity). Fix a function $\mu = \mu(s)$. If we have $poly(s, \mu(n))$ -time hitting-set for size-s arity-n $\Sigma\Pi\Sigma\wedge$ circuits, then for VP circuits we have a poly(sd)-time arity reduction $(n \mapsto O(\log sd))$ that preserves nonzeroness (and proves an exponential lower bound).

The PIT algorithms in current literature always try to achieve a subexponential dependence on n, the number of variables. Our results demonstrate that all we need is a $\operatorname{poly}(sd2^n)$ -time algorithm to completely solve VP PIT. Or, a $\operatorname{poly}(s\mu(n))$ -time algorithm (for $\Sigma\Pi\Sigma\wedge$) to partially solve VP PIT and to prove "either $\mathbb{E}\not\subseteq\#P/\operatorname{poly}$ or $\operatorname{VP}\neq\operatorname{VNP}$ ". For example, even a $\operatorname{poly}(s,A(n))$ -time hitting-set for $\Sigma\Pi\Sigma\wedge$, where A is an Ackermann function [Ack28], would be tremendous progress. A similar case can be made for $\Sigma\wedge^a\Sigma\Pi(n)$ circuits, where both a and a are arbitrarily small unbounded functions, see Thm.17 (i.e. time-complexity may be arbitrary in terms of both a and a).

Obviously, we should now discover techniques and measures that are specialized to this tiny regime. Many previous works use support size of a monomial as a measure to achieve rank concentration [ASS13, FSS14, GKST16]. For a monomial m, its support is the set of variables whose exponents are positive. We introduce a different measure: cone-size (see Defn.18) which is the number of monomials that divide m (also see [ASS13, For14]). It has two advantages in the tiny regime. First, the number of monomials with cone-size at most s is poly(s) (Lem.20). Second, for any circuit C and a monomial m, we devise (in blackbox) a circuit C' which computes the coefficient of m in C and has size polynomial in that of C and the cone-size of m (Lem.19). Using this measure we can define a new concept of rank concentration [ASS13]—'low'-cone concentration—and we are able to give poly-time hitting-sets for a large class of tiny circuits (i.e. n is logarithmic wrt size). We prove our result in a general form (Thm.4) and as a corollary (Cor.21) we get our claim. This gives us a poly-time hitting-set for depth-3 diagonal circuits where the rank of the linear forms is logarithmic wrt the size (Thm.22).

Theorem 4. Let C be a set of arity-n degree-d circuits with size-s s.t. for all $C \in C$, the dimension of the partial derivative space of C is at most k. Then, blackbox PIT for C can be solved in $(sdk)^{O(1)} \cdot (3n/\log k)^{O(\log k)}$ time.

Note that for $n = O(\log k) = O(\log sd)$, the above bound is poly-time and such a PIT result was not known before. For instance, general diagonal depth-3 is a prominent model with low partial derivative space; it has a whitebox poly-time PIT [Sax08] but no poly-time hitting-set is known (though [FSS14] gave an $s^{O(\log \log s)}$ -time hitting-set.). Even for $O(\log k)$ -variate diagonal depth-3 no poly-time hitting-sets were known before our work.

We investigate another structural property useful in the tiny regime. Consider a polynomial $f(\mathbf{x})$ with coefficients over \mathbb{F}^k . Let $\operatorname{sp}(f)$ be the subspace spanned by its coefficients. We say that f has a cone-closed basis if there is a set of monomials B whose coefficients form a basis of $\operatorname{sp}(f)$ and if B is closed under submonomials. We prove that this notion is a strengthening of both low-support [ASS13] and low-cone concentration ideas [For15] (see Lem.23). Recently, this notion of closure has also appeared as an abstract simplicial complex in [GKPT16].

Interestingly, we show that a general polynomial f, when shifted by a 'random' amount (or by 'formal' variables), becomes cone-closed. More generally, we prove the following theorem relating this concept to that of basis isolating weight assignment [AGKS15].

Theorem 5. Let $f(\mathbf{x}) \in \mathbb{F}[\mathbf{x}]^k$ be an arity-n degree-d polynomial over \mathbb{F}^k . Let \mathbf{w} be a basis isolating weight assignment of $f(\mathbf{x})$. Then $f(\mathbf{x} + t^{\mathbf{w}})$ has a cone-closed basis over $\mathbb{F}(t)$.

1.2 Proof ideas

The proof of Thm.1 is a technical refinement of the strategy of [AV08, Thm.3.2] in at least three ways. First, we get a polynomial g that is hard for tiny diagonal depth-4 circuits \mathcal{C} from the hitting-set for \mathcal{C} (Lem.6). Essentially, g will be an annihilating polynomial of the hitting-set generator. The novelty here is that we have to allow g to be non-multilinear (unlike [AV08, Lem.3.3]), for it to exist, as the arity of \mathcal{C} in our case is logarithmically small. We show that a multi- δ -ic g suffices (i.e. g has individual degree bounded by a constant δ). By VP depth-reduction, and Fischer's trick (that is 'cheap' to apply in the tiny regime), this polynomial remains hard for VP. Next by Lem.10, where we use Nisan-Wigderson design [NW94] and Kaltofen's factorization [Kal89], we get a poly-time arity-reducing polynomial-map based on g that keeps any nonzero VP circuit nonzero and reduces its arity from n to $O(\log sd)$. This gives the theorem.

The 'log' function in our method comes from the use of Nisan-Wigderson design and because we want the δ to be constant in depth-reduction; reducing the arity further would need a new idea. Moreover, we observe that the annihilator g is an E-computable polynomial with exponential arithmetic circuit complexity. This means that: Either $\mathbb{E} \not\subseteq \#P/\text{poly}$ or VNP has a polynomial with exponential arithmetic complexity (Lem.13). It is not clear whether we can strengthen the connection all the way to the (conjectured) $\text{VNP} \neq \text{VP}$. Perhaps, this will require starting with a more structured hitting-set generator for \mathcal{C} , so that its annihilator g is a polynomial whose coefficient bits are (#P/poly)-computable (see Valiant's criterion [Bür13, Prop.2.20]).

The proof of Thm.3 requires one to move in a different regime where the arity is $n = a \log s = \omega(\log s)$, the semantic individual-degree is *one* and the circuit is depth-3 (Thm.16). We reach there in a sequence of steps, and then apply the Kronecker map $(x_i \mapsto y^{2^i})$ locally in blocks of size $\log s$. This leads to an arity reduction $a \log s \mapsto a$ in the PIT problem.

The proof of Thm.4 has two steps. In the first step, we show that with respect to any monomial ordering, the dimension k of the partial derivative space of a polynomial is lower bounded by the cone-size of its leading monomial. So, for every nonzero $C \in \mathcal{C}$ there is a monomial with nonzero coefficient and cone-size $\leq k$. The second step is to check whether the coefficients of all the monomials in C, with cone-size $\leq k$, are zero. Lem.19 describes the time

required to check whether the coefficient of a monomial is zero. Lem.20 gives us an optimal upper bound on the number of monomials with cone-size $\leq k$.

Thm.5 unfolds an interesting combinatorial interaction between variable shift and cones of resulting monomials. Let a set of monomials B be the least basis, wrt to the basis isolating weight assignment, of f. We consider the set of all submonomials of those in B and identify a subset A that is cone-closed. We define A in an algorithmic way, as described in Algo.1. The fact that A is exactly a basis of the shifted f is proved in Lem.28 by studying the action of the shift on the coefficient vectors. This has an immediate (nontrivial) consequence that any polynomial f over \mathbb{F}^k , when shifted by formal variables, becomes cone-closed.

2 Tiny diagonal depth-4 circuits—Proof of Theorems 1-3

In this section we will revisit the techniques that have appeared in some form in [NW94, KI03, Agr05, AV08, GKKS13] and strengthen them to derive our results. First, we show how to get a hard polynomial from a hitting-set.

Hitting-set generator. Let C be a set of arity-n circuits. We call an n-tuple of univariates $\mathbf{f}(y) = (f_1(y), \dots, f_n(y))$ a (t, d)-hsg (hitting-set generator) for C if: (1) for any nonzero $C \in C$, $C(\mathbf{f}(y)) \neq 0$, and (2) \mathbf{f} has time-complexity t and the degree of each f_i is at most d.

From this, we get a hard polynomial simply by looking at an annihilating polynomial of $\mathbf{f}(y)$.

Lemma 6 (Hitting-set to hardness). Let $\mathbf{f}(y) = (f_1(y), \dots, f_n(y))$ be a (t, d)-hsg for \mathcal{C} . Then, there exists an arity-n polynomial $g(\mathbf{x})$ that is not in \mathcal{C} , is computable in poly(tdn)-time, and has individual degree less than $2\delta := 2\lceil (dn+1)^{1/(n-1)} \rceil$. Moreover, we can ensure that its degree is exactly δn .

Proof. A natural candidate for $g(\mathbf{x})$ is any annihilating polynomial of the n polynomials $\mathbf{f}(y) = (f_1(y), \dots, f_n(y))$, since for every nonzero $h \in \mathcal{C}$, $h(\mathbf{f})$ is nonzero. Define δ as the smallest integer such that $\delta^{n-1} > dn \ge (\delta-1)^{n-1}$. Consider $g(\mathbf{x})$ as an arity-n polynomial with individual degree less than δ . Then, $g(\mathbf{x})$ can be written as:

$$g(\mathbf{x}) = \sum_{\mathbf{e}: 0 \le \mathbf{e}_i < \delta} c_{\mathbf{e}} \mathbf{x}^{\mathbf{e}} \tag{1}$$

where, $c_{\mathbf{e}}$'s are unknown to us. We can set all these $c_{\mathbf{e}}$'s to zero except the ones corresponding to an index-set I of size $\delta_0 := (dn\delta + 1)$. This way we have only δ_0 unknowns. To be an annihilating polynomial of $\mathbf{f}(y)$, we need $g(\mathbf{f}) = 0$. By comparing the coefficients of the monomials, in y, both sides we get a linear system in the unknowns.

Suppose that δ_1 is the degree of y in $g(\mathbf{f})$. Then, $g(\mathbf{f})$ can be written as $g(\mathbf{f}) = \sum_{i=0}^{\delta_1} p_i \cdot y^i$, where p_i 's are linear polynomials in $c_{\mathbf{e}}$'s. The constraint $g(\mathbf{f}) = 0$ gives us a system of linear equations with the number of unknowns δ_0 and the number of equations δ_1 . The value of δ_1 can be at most $d \cdot n \cdot \delta$, which means that the number of unknowns δ_0 is greater than the number of equations δ_1 . So, our system of linear equations always has a nontrivial solution, which gives us a nonzero g. In case its degree is $< \delta n$, we can multiply by an appropriate monomial $\mathbf{x}^{\mathbf{e}}$ to make the degree $= \delta n$. Note that $g \notin \mathcal{C}$ holds, as $g(\mathbf{f})$ is still nonzero (since $f_1(y), \ldots, f_n(y)$ are nonzero wlog).

Computing $\mathbf{f}(y)$ takes t time and a solution of the linear equations can be computed in poly(tdn)-time. So, $g(\mathbf{x})$ can be computed in poly(tdn)-time.

Corollary 7 (E-computable). In the proof of Lem.6, if $td = 2^{O(n)}$ then the polynomial family $g_n := g$, indexed by the arity, is E-computable (i.e. all the formal monomials and their coefficients can be produced in $poly(2^n)$ -time given unary n).

Proof. The linear system that we got can be solved in poly(tdn)-time. As it is homogeneous we can even get an integral solution in the same time-complexity. Thus, assuming $td = 2^{O(n)}$, the time-complexity of computing a bit of $coef_{\mathbf{x}^e}(g)$ is $poly(tdn) = poly(2^n)$, the coefficient bitsize is $poly(2^n)$ and so is the number of monomials in g (: $\delta = O(1)$). In other words, if we consider the polynomials $g_n := g$, indexed by the arity, then the family $\{g_n\}_n$ is E-computable.

Towards a converse of the above lemma, a crucial ingredient is the Nisan-Wigderson design [NW94].

Definition 8. Let $\ell > n > d$. A family of subsets $\mathcal{F} = \{I_1, \ldots, I_m\}$ on $[\ell]$ is called an (ℓ, n, d) -design, if $|I_i| = n$ and for all $i \neq j \in [m]$, $|I_i \cap I_j| \leq d$.

Lemma 9 (Nisan-Wigderson design, Chap.16 [AB09]). There exists an algorithm which takes (ℓ, n, d) and a base set S of size $\ell > \frac{10n^2}{d}$ as input, and outputs an (ℓ, n, d) -design \mathcal{F} having $> 2^{\frac{d}{10}}$ subsets, in time $2^{O(\ell)}$.

Next, we use a hard polynomial q_m on a small design to get a poly-time computable arity-reducing polynomial-map for VP that preserves nonzeroness.

Lemma 10 (Hardness to VP reduction). Let $\{q_m\}_{m\geq 1}$ be a family of multi- $(\delta-1)$ -ic polynomials such that it can be computed in $\delta^{O(m)}$ time, but has no $\delta^{o(m)}$ -size arithmetic circuit. Then there is a $\delta^{O(\log sd)}$ -time arity reduction, from n to $O(\log sd)$, for VP circuits.

Proof. Note that there is a constant $c_0 > 0$ such that q_m requires $\Omega(\delta^{c_0 m})$ -size arithmetic circuits. Otherwise $\{q_m\}_{m\geq 1}$ will be in $\cap_{c>0} \text{Size}(\delta^{cm})$, and hence in $\text{Size}(\delta^{o(m)})$.

Let \mathcal{C} be a set of arity-n degree-d VP circuits with size $\leq s$. Let $n' := sd \geq n$. Let $\mathcal{F} = \{S_1, \ldots, S_{n'}\}$ be a $(c_2 \log n', c_1 \log n', 10 \log n')$ -design on the variable set $Z = \{z_1, \ldots, z_{c_2 \log n'}\}$. Constants $c_2 > c_1 > 10$ will be fixed later. Our hitting-set generator for \mathcal{C} is defined as: for all $i \in [n]$, $x_i = q_{c_1 \log n'}(S_i) =: p_i$. Then, we show that for any nonzero polynomial $C(\mathbf{x}) \in \mathcal{C}$, $C(p_1, \ldots, p_n)$ is also nonzero.

For the sake of contradiction, assume that $C(p_1, \ldots, p_n)$ is zero. Since $C(\mathbf{x})$ is nonzero, we can find the smallest $j \in [n]$ such that $C(p_1, \ldots, p_{j-1}, x_j, \ldots, x_n) =: C_1$ is nonzero, but $C_1(x_j = p_j)$ is zero. Thus, $(x_j - p_j)$ divides C_1 . Let **a** be an assignment on all the variables in C_1 , except x_j and the variables S_j in p_j , with the property: C_1 at **a** is nonzero. Since C_1 is nonzero, we can find such an assignment. Now our new polynomial C_2 on the variables S_j is of the form:

$$C_2(S_j) = C(p'_1, \dots, p'_{j-1}, x_j, a_{j+1}, \dots, a_n)$$

where, for each $i \in [j-1]$, p_i' is the polynomial on the variables $S_i \cap S_j$, and a_i 's are field constants decided by our assignment **a**. By the design, for each $i \in [j-1]$, $|S_i \cap S_j| \leq 10 \log n'$. Since p_i' are polynomials on variables $S_i \cap S_j$ of individual degree $< \delta$, each p_i' has a circuit of size at most $n\delta^{10\log n'} \leq \delta^{11\log n'}$. Then we have a circuit for C_2 of size at most $s_1 := s + n \cdot \delta^{11\log n'}$, and degree at most $d_1 := d \cdot \delta c_1 \log n'$. Since $(x_j - p_j)$ divides C_2 , we can invoke the VP factorization algorithm [Kal89] (see [Bür13, Thm.2.21] for the arithmetic circuit complexity of factors) and get an arithmetic circuit for p_j of size $(s_1d_1)^{c_3}$, for some absolute constant c_3 (independent of c_1, c_2).

Now we fix constants c_1, c_2 . Pick c_1 such that $\delta^{c_0 \cdot c_1 \log n'}$ is asymptoically larger than $(2sn\delta^{11\log n'} \cdot d\delta c_1 \log n')^{c_3} > (s_1d_1)^{c_3}$. Since sd = n' and $\delta \geq 2$, the absolute constant $c_1 := 15c_3/c_0$ (independent of c_2) satisfies the above condition.

Pick c_2 , following Lem.9, such that $c_2 \log n' > 10 \cdot (c_1 \log n')^2/(10 \log n')$. So, $c_2 := 1 + c_1^2$ works. With these values of c_1, c_2 , we have a design that 'stretches' $c_2 \log n'$ variables to n subsets with the required 'low' intersection property. It is computable in poly(n')-time.

Moreover, if $C(p_1, \ldots, p_n)$ is zero then, by the above discussion, $p_j = q_{c_1 \log n'}(S_j)$ has a circuit of size $(s_1 d_1)^{c_3} = o(\delta^{c_0 \cdot c_1 \log n'})$. This violates the lower bound hypothesis. Thus, $C(p_1, \ldots, p_n)$ is nonzero.

The time for computing (p_1, \ldots, p_n) depends on: (1) computing the design (i.e. $\operatorname{poly}(n')$ -time), and (2) computing $q_{c_1 \log n'}$ (i.e. $\delta^{O(\log n')}$ -time). Thus, the arity reduction map for VP is computable in $\delta^{O(\log n')}$ time.

Once we have a polynomial that is hard for a tiny model, to apply the above lemma, we need to show that it is also hard for VP. This is done by depth-reduction results. First, we need a lemma that converts a monomial into a sum of powers. This was used in [GKKS13]. (It requires char $\mathbb{F} = 0$ or large.)

Lemma 11 (Fischer's Trick [Fis94]). Over a field \mathbb{F} of $char(\mathbb{F}) = 0$ or > r, any expression of the form $g = \sum_{i \in [k]} \prod_{j \in [r]} g_{ij}$ with $\deg(g_{ij}) \leq \delta$, can be rewritten as $g = \sum_{i \in [k']} c_i g_i^r$ where $k' := k2^r$, $\deg(g_i) \leq \delta$ and $c_i \in \mathbb{F}$.

Motivated by this transformation (when 2^r is 'small'), we define a tiny subclass of VP.

Definition 12. The diagonal depth-4 circuits compute polynomials of the form $\sum_{i \in [k]} c_i f_i^a$ where f_i 's are sparse polynomials in $\mathbb{F}[x_1,\ldots,x_n]$ of degree $\leq b$ and c_i 's in \mathbb{F} . A standard notation to denote this class is $\sum \wedge^a \Sigma \Pi^b(n)$. This is a special case of the depth-4 $\Sigma \Pi^a \Sigma \Pi^b(n)$ model that computes polynomials of the form $\sum_{i \in [k]} \prod_{j \in [a]} f_{i,j}$ where $f_{i,j}$'s are sparse polynomials in $\mathbb{F}[x_1,\ldots,x_n]$ of degree $\leq b$.

Given a constant c > 1, computable functions $\mu(a) = \Omega(a)$ and μ' , we define the class $\mathcal{T}_{\mu,\mu',c}$, called tiny diagonal depth-4, containing $\Sigma \wedge^a \Sigma \Pi^b(n)$ circuits of size $\leq s$ that compute polynomials of semantic individual-degree $\leq a'$ and $2^n + 2^b + \mu(a) + \mu'(a') < s^c$.

Analogously, we define the class $\mathcal{T}'_{\mu',c}$, called tiny depth-3, containing $\Sigma\Pi\Sigma(n)$ circuits of size $\leq s$ that compute polynomials of semantic individual-degree $\leq a'$, and $2^n + \mu'(a') < s^c$.

Remark. Note that $n, b = O(\log s)$ and by picking the function $\mu(\cdot)$ (resp. μ') arbitrarily large we can make $a = a(s) = \omega(1)$ (resp. a') an arbitrarily small computable function. Also, in this regime the number of monomials in the bottom $\Sigma\Pi^b$ layer is $\binom{n+b}{b} < 2^{n+b} < s^{2c}$, so the f_i 's can be thought of as given in the *dense* representation. Analogously, in tiny depth-3, $n = O(\log s)$ and the semantic individual-degree bound $a' = \omega(1)$ can be picked arbitrarily small.

An alternative interpretation of the tiny models can be given using the parameterized complexity [DF13] of PIT. Essentially, we are interested in hitting-sets for the diagonal depth-4 model that are fixed parameter tractable wrt n, b, a and a' (input size is s). Analogously, we are interested in hitting-sets for the depth-3 model that are fixed parameter tractable wrt n and a', where a' is the semantic individual-degree bound (input size is s).

Now we invoke VP depth-reduction to get to the tiny model, and finish our proof.

Proof of Thm.1. The proof is along the lines of [AV08, Thm.3.2]. Using Lem.6, from poly-time hitting-set generator for tiny diagonal depth-4, we get a hard polynomial for this model. Then we show that it is also hard for VP and invoke Lem.10 to get the VP arity reduction.

Now we provide the details. Let constant c > 1, functions $\mu(a) = \Omega(a)$ and μ' be given in the hypothesis. Let $\mathcal{C} \subset \mathcal{T}_{\mu,\mu',c}$ be the set of tiny diagonal depth-4 circuits of size $\leq s$ and arity $m := \log s$. Assume that \mathcal{C} has a (s^e, s^e) -hsg $\mathbf{f}(y)$ for some constant $e \geq 1$. Then using Lem.6, we have an m-variate polynomial q_m with individual degree less than some constant 2δ , $\delta^{m-1} = \delta^{\log s-1} > s^e \log s$, and q_m is computable in $s^{O(1)} = \delta^{O(m)}$ time. It has degree $= \delta m$. Importantly, $q_m \notin \mathcal{T}_{\mu,\mu',c}$, thus, no tiny diagonal depth-4 circuit of size $\leq s = \delta^{\Theta(m)}$ can

compute it (otherwise, $q_m(\mathbf{f}(y)) \neq 0$ which contradicts its definition as an annihilator). Next we show that it is also not computable by any $\delta^{o(m)}$ -size arithmetic circuit.

For the sake of contradiction, assume that q_m has a $\delta^{o(m)}$ -size circuit. From depth-reduction results [Sap16] we get a circuit C, of $\Theta(\log \delta m)$ -depth and $s_m = \delta^{o(m)}$ size, with the additional properties:

- 1. alternative layers of addition/multiplication gates with the top-gate (root) being addition.
- 2. below each multiplication layer the polynomial degree at least halves.
- 3. fan-in of each multiplication gate is at most 5.

Now we cut the circuit C at the t-th layer of multiplication gates from the top, where $t = t(s_m)$ will be fixed later, to get the two parts:

Top part: the top part computes a polynomial of degree at most 5^t and the number of variables is at most s_m . So it can be reduced to a $\Sigma\Pi$ circuit of size $\binom{s_m+5^t}{5^t} = s_m^{O(5^t)}$ (Stirling's approximation, see [Sap16, Prop.4.4]).

Bottom part: in the bottom part, we can have at most s_m many top-multiplication gates that feed into the top part as input. Each multiplication gate computes a polynomial of degree at most $\delta m/2^t$ and the number of variables is at most m. So each multiplication gate can be reduced to a $\Sigma\Pi$ circuit of size $\binom{m+\delta m/2^t}{\delta m/2^t} = 2^{O(\delta mt/2^t)}$.

From the above discussion, we have a $\Sigma\Pi^{5^t}\Sigma\Pi^{\delta m/2^t}$ circuit C', computing q_m , that has size $s_m^{O(5^t)} + s_m \cdot 2^{O(\delta mt/2^t)}$.

The second summand becomes $2^{o(m\log\delta)}$ if we pick $t=\omega(1)$ (recall that $s_m=\delta^{o(m)}$ and $\delta=O(1)$). To get a similar upper bound on the first summand we need to pick $5^t\log s_m=o(m\log\delta)$. Finally, we also want $\mu(5^t)=o(m)$. A function $t=t(s_m)=t(s)$, satisfying the three conditions, exists as $\log s_m=o(m\log\delta)$ and $\mu(\cdot)$ is an increasing function. Let us fix such a function t. (As C has super-constant depth, we can also assume that the cut at depth t will be possible.) Thus the circuit C', computing q_m , has size $s'_m=\delta^{o(m)}$.

Let $a:=5^t$ and $b:=\delta m/2^t$. Consider the measure $E:=2^m+2^b+\mu(a)+\mu'(2\delta)$. We have the estimate $E=s+2^{o(m)}+o(m)+O(1)=s+o(s)=o(s^c)$. So now we have a shallow circuit for q_m of the form $\Sigma\Pi^a\Sigma\Pi^b$. Applying Lem.11, we get a tiny diagonal depth-4 circuit, in $\mathcal{T}_{\mu,\mu',c}$, computing q_m of the form $\Sigma\wedge^a\Sigma\Pi^b$ and size $s'_m\cdot 2^a=\delta^{o(m)}\cdot 2^{O(\mu(a))}=\delta^{o(m)}$ which is < s. This contradicts the hardness of q_m . Thus, there is no arithmetic circuit for q_m of size $\delta^{o(m)}$.

Now invoking Lem. 10 on the hard family $\{q_m\}_{m\geq 1}$, we get our claim.

Proof of Thm.2. Suppose we have a poly $(sd2^n)$ -time hitting-set $\mathcal{H}_{s,d,n}$ for size-s degree-d arityn circuits. Then, in particular, we have a poly(s)-time blackbox PIT for tiny diagonal depth-4. Thus, Thm.1 gives a poly(sd)-time arity-reducing polynomial-map $(n \mapsto O(\log sd))$ for VP that preserves nonzeroness. Let n' := sd correspond to a given VP circuit C. Now, using $\mathcal{H}_{n'^{O(1)},O(d\log n'),O(\log n')}$ we get a poly(n')-time hitting-set for C.

In the proof of Thm.1 we get the hard polynomial q_m , which by Cor.7 gives us an E-computable polynomial family $\{q_m\}_m$, indexed by the arity, that has arithmetic circuit complexity $2^{\Omega(m)}$.

The existence of such a family $\{q_m\}_m$ has interesting complexity consequences.

Lemma 13 (Class separation). If we have an E-computable polynomial family $\{f_n\}_n$ with individual-degree O(1) and arithmetic circuit complexity $2^{\Omega(n)}$, then either $E \nsubseteq \#P/poly$ or VNP has polynomials of arithmetic complexity $2^{\Omega(n)}$.

Proof. Say, for a constant $\delta \geq 1$, we have an E-computable multi- δ -ic polynomial family $\{f_n\}_n$ with arithmetic circuit complexity $2^{\Omega(n)}$. Clearly, the coefficients in f_n have bitsize $2^{\Omega(n)}$. By using a simple transformation, given in [KP09, Lem.3.9], we get a multi- δ -ic polynomial family $\{h_n\}_n$, that is E-computable and has arithmetic complexity $2^{\Omega(n)}$, such that its coefficients are $\{0,\pm 1\}$.

Assume $E\subseteq \#P/\text{poly}$. Since each coefficient of h_n is a signed-bit that is computable in E, we deduce that the coefficient-function of h_n is in #P/poly. Thus, by $[B\ddot{u}r13, Prop.2.20]$, $\{h_n\}_n$ is in VNP and has arithmetic complexity $2^{\Omega(n)}$.

Our techniques could handle many other 'tiny' models. The proofs are given in Sec.A.

Theorem 14 (Tiny depth-3). If we have poly-time hitting-sets for a tiny depth-3 model, then for VP circuits we have a poly(sd)-time arity reduction $(n \mapsto O(\log sd))$ that preserves nonzeroness (and proves an exponential lower bound).

Theorem 15 (Width-2 ABP). If we have $poly(s2^n)$ -time hitting-sets for size-s arity-n width-2 upper-triangular ABP, then for VP circuits we have a poly(sd)-time arity reduction $(n \mapsto O(\log sd))$ that preserves nonzeroness (and proves an exponential lower bound).

Our method could also handle individual-degree a' = O(1) (eg. multilinear polynomials), but then we have to allow arity $\omega(\log s)$ (clearly, arity $O(\log s)$ trivializes the model [BOT88]). We state our result below in parameterized complexity terms. (Proof in Sec.A.)

Multilinear tiny depth-3. Given a constant c > 1 and an arbitrary function $\mu'(a') = \Omega(a')$, we define the class $\mathcal{M}_{\mu',c}$, called multilinear tiny depth-3, containing $\Sigma\Pi\Sigma(n)$ circuits of size $\leq s$ that compute multilinear polynomials, and $\mu'(n/\log s) < s^c$.

Theorem 16 (Multilinear tiny depth-3). If we have poly-time hitting-sets for a multilinear tiny depth-3 model, then for VP circuits we have a poly(sd)-time arity reduction $(n \mapsto O(\log sd))$ that preserves nonzeroness (and proves an exponential lower bound).

2.1 Arbitrarily small arity suffices

Using the previous result we can now reduce the arity, for PIT purposes, arbitrarily.

Proof of Thm.3. Suppose we have a poly $(s, \mu(n))$ -time hitting-set $\mathcal{H}_{s,n}$ for size-s arity- $n \Sigma \Pi \Sigma \wedge$ circuits. Wlog we can assume that $\mu(n) = \Omega(n)$. Let a = a(s) be a function satisfying $\mu(a) \leq s$, and define $n = n(s) := a \log s$. Consider a size-s $\Sigma \Pi \Sigma(n)$ circuit $C \neq 0$ computing a multilinear polynomial. We intend to design a hitting-set for C.

Partition the variable set $\{x_1, \ldots, x_n\}$ into a blocks $B_j, j \in [a]$, each of size $\log s$. Let $B_j = \{x_{u(j)+1}, x_{u(j)+2}, \ldots, x_{u(j)+\log s}\}$, for all $j \in [a]$ (pick u to be an appropriate function). Consider the arity-reducing "local Kronecker" map $\varphi : x_{u(j)+i} \mapsto y_j^{2^i}$. Note that $\varphi(C) \in \mathbb{F}[y_1, \ldots, y_a]$, and its semantic individual-degree is at most 2s.

It is easy to see that $\varphi(C) \neq 0$ (basically, use the fact that C computes a nonzero multilinear polynomial and φ keeps the multilinear monomials distinct). Finally, $\varphi(C)$ becomes an arity-a $\Sigma\Pi\Sigma\wedge$ circuit of size at most $s+s\cdot 2^{\log s}=O(s^2)$. Thus, using $\mathcal{H}_{O(s^2),a}$ we get a hitting-set for $\varphi(C)$ of time-complexity poly($s^2, \mu(a)$)= poly(s). In turn, we get a poly-time hitting-set for multilinear tiny depth-3 model $\mathcal{M}_{\mu,2}$. By invoking Thm.16 we finish the argument.

We can also work with a version of diagonal depth-4 with arbitrarily small n & a.

Theorem 17 (Tinier n, a). Fix functions $\mu = \mu(s)$ and μ' . If we have $poly(s, \mu(a), \mu'(n))$ -time hitting-set for size-s $\Sigma \wedge^a \Sigma \Pi(n)$ circuits, then for VP circuits we have a poly(sd)-time arity reduction $(n \mapsto O(\log sd))$ that preserves nonzeroness (and proves an exponential lower bound). (Proved in Sec.A.)

3 Low-cone concentration and hitting-sets- Proof of Thm.4

In this section we initiate a study of properties that are relevant for tiny circuits (or the log-arity regime).

Definition 18 (Cone of a monomial). A monomial $\mathbf{x}^{\mathbf{e}}$ is called a submonomial of $\mathbf{x}^{\mathbf{f}}$, if $\mathbf{e} \leq \mathbf{f}$ (i.e. coordinate-wise). We say that $\mathbf{x}^{\mathbf{e}}$ is a proper submonomial of $\mathbf{x}^{\mathbf{f}}$, if $\mathbf{e} \leq \mathbf{f}$ and $\mathbf{e} \neq \mathbf{f}$.

For a monomial $\mathbf{x}^{\mathbf{e}}$, the cone of $\mathbf{x}^{\mathbf{e}}$ is the set of all submonomials of $\mathbf{x}^{\mathbf{e}}$. The cardinality of this set is called cone-size of $\mathbf{x}^{\mathbf{e}}$. It equals $\prod (\mathbf{e} + \mathbf{1}) := \prod_{i \in [n]} (e_i + 1)$, where $\mathbf{e} = (e_1, \dots, e_n)$.

A set S of monomials is called cone-closed if for every monomial in S all its submonomials are also in S.

Lemma 19 (Coef. extraction). Let C be a circuit which computes an arity-n degree-d polynomial. Then for any monomial $m = \prod_{i \in [n]} x_i^{e_i}$, we have blackbox access to a poly(|C|d, cs(m))-size circuit computing the coefficient of m in C, where cs(m) denotes the cone-size of m.

Proof. Our proof is in two steps. First, we inductively build a circuit computing a polynomial which has two parts; one is $coef_m(C) \cdot m$ and the other one is a "junk" polynomial where every monomial is a proper super-monomial of m. Second, we construct a circuit which extracts the coefficient of m. In both these steps the key is a classic interpolation trick.

We induct on the variables. For each $i \in [n]$, let $m_{[i]}$ denote $\prod_{j \in [i]} x_j^{e_j}$. We will construct a circuit $C^{(i)}$ which computes a polynomial of the form,

$$C^{(i)}(\mathbf{x}) = \operatorname{coef}_{m_{[i]}}(C) \cdot m_{[i]} + C_{iunk}^{(i)}$$
(2)

where, for every monomial m' in the support of $C_{junk}^{(i)}$, $m_{[i]}$ is a proper submonomial of $m'_{[i]}$.

Base case: Since $C =: C^{(0)}$ computes an arity-n degree-d polynomial, $C(\mathbf{x})$ can be written as $C(\mathbf{x}) = \sum_{j=0}^{d} c_j x_1^j$ where, $c_j \in \mathbb{F}[x_2, \dots, x_n]$. Let $\alpha_0, \dots, \alpha_{e_1}$ be some $e_1 + 1$ distinct elements in \mathbb{F} . For every α_j , let $C_{\alpha_j x_1}$ denote the circuit $C(\alpha_j x_1, x_2, \dots, x_n)$ which computes $c_0 + c_1 \alpha_j x_1 + \dots + c_{e_1} \alpha_j^{e_1} x_1^{e_1} + \dots + c_d \alpha_j^{d} x_1^{d}$. Since

$$M = \begin{bmatrix} 1 & \alpha_0 & \dots & \alpha_0^{e_1} \\ \vdots & \vdots & \vdots & \vdots \\ 1 & \alpha_{e_1} & \dots & \alpha_{e_1}^{e_1} \end{bmatrix}$$

is an invertible Vandermonde matrix, one can find an $\mathbf{a} = [a_0, \dots, a_{e_1}] \in \mathbb{F}^{e_1+1}$, $\mathbf{a} \cdot M = [0, 0, \dots, 1]$. Using this \mathbf{a} , we get the circuit $C^{(1)} := \sum_{j=0}^{e_1} a_j C_{\alpha_j x_1}^{(0)}$. Its least monomial wrt x_1 has $\deg_{x_1} \geq e_1$, which is the property that we wanted.

Induction step $(i \to i+1)$: From induction hypothesis, we have the circuit $C^{(i)}$ with the properties mentioned in Eqn.2. The polynomial can also be written as $b_0 + b_1 x_{i+1} + \ldots + b_{e_{i+1}} x_{i+1}^{e_{i+1}} + \ldots b_d x_{i+1}^d$, where every b_j is in $\mathbb{F}[x_1, \ldots, x_i, x_{i+2}, \ldots, x_n]$. Like the proof of the base case, for $e_{i+1}+1$ distinct elements $\alpha_0, \ldots, \alpha_{e_{i+1}} \in \mathbb{F}$, we get $C^{(i+1)} = \sum_{j=0}^{e_{i+1}} a_j C^{(i)}_{\alpha_j x_{i+1}}$, for some $\mathbf{a} = [a_0, \ldots, a_{e_{i+1}}] \in \mathbb{F}^{e_{i+1}+1}$ and the structural constraint of $C^{(i+1)}$ is easy to verify, completing the induction.

Now we describe the second step of the proof. After first step, we get

$$C^{(n)}(\mathbf{x}) = \operatorname{coef}_m(C) \cdot m + C^{(n)}_{junk},$$

where for every monomial m' in the support of $C_{junk}^{(n)}$, m is a proper submonomial of m'. Consider the polynomial $C^{(n)}(x_1t,\ldots,x_nt)$ for a fresh variable t. Then, using interpolation wrt

t we can construct a $O(|C^{(n)}| \cdot d)$ -size circuit for $\operatorname{coef}_m(C) \cdot m$, by extracting the coefficient of $t^{\operatorname{deg}(m)}$, since the degree of every monomial appearing in $C_{junk}^{(n)}$ is $> \operatorname{deg}(m)$. Now evaluating at $\mathbf{1}$, we get $\operatorname{coef}_m(C)$. The size, or time, constraint of the final circuit clearly depends polynomially on |C|, d and $\operatorname{cs}(m)$.

But, how many low-cone monomials can there be? Fortunately, in the log-arity regime they are not too many [Sap13]. Though, in general, they are quasipolynomially many.

Lemma 20 (Counting low-cones). The number of arity-n monomials with cone-size at most k is $O(sk^2)$, where $s := (3n/\log k)^{\log k}$.

Proof. First, we prove that for any fixed support set, the number of cone-size $\leq k$ monomials is less than k^2 . Next, we multiply by the number of possible support sets to get the estimate.

Let $T(k,\ell)$ denote the number of cone-size $\leq k$ monomials m with support set, say, exactly $\{x_1,\ldots,x_\ell\}$. Since the exponent of x_ℓ in such an m is at least 1 and at most k-1, we have the following by the disjoint-sum rule: $T(k,\ell) \leq \sum_{i=2}^k T(k/i,\ell-1)$. This recurrence affords an easy inductive proof as, $T(k,\ell) < \sum_{i=2}^k (k/i)^2 < k^2 \cdot \sum_{i=2}^k \left(\frac{1}{i-1} - \frac{1}{i}\right) < k^2$. From the definition of cone, a cone-size $\leq k$ monomial can have support size at most $\ell := k + k$.

From the definition of cone, a cone-size $\leq k$ monomial can have support size at most $\ell := \lfloor \log k \rfloor$. The number of possible support sets, thus, is $\sum_{i=0}^{\ell} \binom{n}{i}$. Using the binomial estimates [Juk10, Chap.1], we get $\sum_{i=0}^{\ell} \binom{n}{i} \leq (3n/\ell)^{\ell}$.

The partial derivative space of arithmetic circuits has been defined, and mined, in various works [CKW11]. Even when this space is small we do not have efficient hitting-sets known (though [FSS14] gave an $s^{O(\log\log s)}$ -time hitting-set.). Below we give a poly-time solution in the log-arity regime. (It requires char $\mathbb{F}=0$ or large.)

Proof of Thm.4. The proof has two steps. First, we show that with respect to any monomial ordering \prec , for all nonzero $C \in \mathcal{C}$, the dimension of the partial derivative space of C is lower bounded by the cone-size of the leading monomial (that nontrivially occurs) in C. Using this, we can get a blackbox PIT algorithm for C by testing the coefficients of all the monomials of C of cone-size $\leq k$ for zeroness. Next, we estimate the time complexity to do this.

The first part is the same as the proof of [For14, Cor.8.4.14] (with origins in [FS13]). Here, we give a brief outline. Let $LM(\cdot)$ be the *leading monomial* operator wrt the monomial ordering \prec . It can be shown that for any polynomial $f(\mathbf{x})$, the dimension of its partial derivative space $\partial_{\mathbf{x}^{<\infty}}(f)$ is the same as $D := \#\{LM(g) \mid g \in \partial_{\mathbf{x}^{<\infty}}(f)\}$ (see [For14, Lem.8.4.12]). This means that dim $\partial_{\mathbf{x}^{<\infty}}(f)$ is lower-bounded by the cone-size of LM(f) [For14, Cor.8.4.13], which completes the proof of our first part.

Next, we apply Lem.19, on the circuit C and a monomial m of cone-size $\leq k$, to get the coefficient of m in C in $\operatorname{poly}(sdk)$ -time. Finally, Lem.20 tells that we have to access at most $k^2 \cdot (3n/\log k)^{\log k}$ many monomials m. Multiplying these two expressions gives us the time bound.

This gives us immediately,

Corollary 21. Let C be a set of arity-n degree-d size-s circuits with $n = O(\log sd)$. Suppose that, for all $C \in C$, the dimension of the partial derivative space of C is poly(sd). Then, the blackbox PIT for C can be solved in poly(sd)-time.

A depth-3 diagonal circuit [Sax08] is of the form $C(\mathbf{x}) = \sum_{i \in [k]} c_i \ell_i^{d_i}$, where ℓ_i 's are linear polynomials over \mathbb{F} and c_i 's in \mathbb{F} . We use $\mathrm{rk}(C)$ to denote the linear rank of the polynomials $\{\ell_i\}_i$.

Theorem 22. Let C be the set of all arity-n degree-d size-s depth-3 diagonal circuits. Suppose that, for all $C \in C$, $rk(C) = O(\log sd)$. Then, the blackbox PIT for C can be solved in poly(sd)-time.

(Proved in Sec.B)

4 Cone-closed basis after shifting—Proof of Thm.5

In this section we will consider polynomials over a vector space, say \mathbb{F}^k . This viewpoint has been useful in studying arithmetic branching programs (ABP), eg. [ASS13, FSS14, AGKS15, GKST16]. Let $D \in \mathbb{F}^k[\mathbf{x}]$ and let $\mathrm{sp}(D)$ be the span of its coefficients. We say that D has a cone-closed basis if there is a cone-closed set of monomials B whose coefficients in D form a basis of $\mathrm{sp}(D)$.

This definition is motivated by the fact that there are some models which have this property naturally, for eg. see Lem.30. In general, this concept subsumes some of the well-known notions of $rank \ concentration \ [ASS13, FSS14, For15, For14]$, i.e. ensuring a basis of sp(D) in a set of monomials that have a small measure in some sense (eg. cone-size or support-size.).

Lemma 23. Let $D(\mathbf{x})$ be a polynomial in $\mathbb{F}^k[\mathbf{x}]$. Suppose that $D(\mathbf{x})$ has a cone-closed basis. Then, $D(\mathbf{x})$ has (k+1)-cone concentration and $(\lg 2k)$ -support concentration.

Proof. Let B be a cone-closed set of monomials forming the basis of $\operatorname{sp}(D)$. Clearly, $|B| \leq k$. Thus, each $m \in B$ has cone-size $\leq k$. In other words, D is (k+1)-cone concentrated.

Moreover, each $m \in B$ has support-size $\leq \lg k$. In other words, D is $(\lg 2k)$ -support concentrated.

Ideally, we would want to modify a given tiny circuit to get a cone-closed basis. This would solve the PIT problem as shown in the previous section. What are the possible ways to get this? We will show that the concept of basis isolating weight assignment, introduced in [AGKS15], leads to a cone-closed basis.

Basis & weights. Consider a weight assignment \mathbf{w} on the variables \mathbf{x} . It extends to monomials $m = \mathbf{x}^{\mathbf{e}}$ as $\mathbf{w}(m) := \langle \mathbf{e}, \mathbf{w} \rangle = \sum_{i=1}^{n} e_i w_i$. Sometimes, we also use $\mathbf{w}(\mathbf{e})$ to denote $\mathbf{w}(m)$. Similarly, for a set of monomials B, the weight of B is $\mathbf{w}(B) := \sum_{m \in B} \mathbf{w}(m)$.

Let $B = \{m_1, \ldots, m_\ell\}$ resp. $B' = \{m'_1, \ldots, m'_\ell\}$ be an ordered set of monomials (non-decreasing wrt \mathbf{w}) that forms a basis of the span of coefficients of $f \in \mathbb{F}^k[\mathbf{x}]$. Wrt \mathbf{w} , we say that B < B' if there exists $i \in [\ell]$ such that $\forall j < i$, $\mathbf{w}(m_j) = \mathbf{w}(m'_j)$ but $\mathbf{w}(m_i) < \mathbf{w}(m'_i)$. We say that $B \leq B'$ if either B < B' or if $\forall i \in [\ell]$, $\mathbf{w}(m_i) \leq \mathbf{w}(m'_i)$. A basis B is called a *least basis*, if for any other basis B', $B \leq B'$. When is it unique?

A weight assignment **w** is called a basis isolating weight assignment for a polynomial $f(\mathbf{x}) \in \mathbb{F}^k[\mathbf{x}]$ if there exists a basis B such that:

- 1. weights of all monomials in B are distinct, and
- 2. the coefficient of every $m \in \text{supp}(f) \setminus B$ is in the linear span of $\{\text{coef}_{m'}(f) \mid m' \in B, \mathbf{w}(m') < \mathbf{w}(m)\}$.

Lemma 24. If **w** is a basis isolating weight assignment for f, then f has a unique least basis B wrt **w**. In particular, for any other basis B' of f, we have $\mathbf{w}(B) < \mathbf{w}(B')$.

Proof. Let ℓ be the dimension of $\operatorname{sp}(f)$. Since \mathbf{w} is a basis isolating weight assignment, we get a basis B that satisfies the two conditions in the definition of \mathbf{w} . We will show that B is the unique least basis. Let $B = \{m_1, \ldots, m_\ell\}$ with $\mathbf{w}(m_1) < \ldots < \mathbf{w}(m_\ell)$.

Consider any other basis $B' = \{m'_1, \ldots, m'_\ell\}$, with $\mathbf{w}(m'_1) \leq \ldots \leq \mathbf{w}(m'_\ell)$. Let j be the minimum number such that $m_j \neq m'_j$ (it exists as $B \neq B'$). Suppose $\mathbf{w}(m_j) \geq \mathbf{w}(m'_j)$. Since $m'_j \notin B$, the coefficient of m'_j can be written as a linear combination of the coefficients of m_i 's for i < j. From the definition of j, for all i < j, $m_i = m'_i$. So the coefficient of m'_j can also be written as a linear combination of the coefficients of m'_i 's for i < j. This contradicts that B' is a basis and proves that $\mathbf{w}(m_j) < \mathbf{w}(m'_j)$.

Now we move beyond j. First, we prove that for all $i \in [\ell]$, $\mathbf{w}(m_i) \leq \mathbf{w}(m_i')$. For the sake of contradiction assume that there exists a number a such that $\mathbf{w}(m_a) > \mathbf{w}(m_a')$. Pick the least such a. Let V be the span of the coefficients of monomials in f whose weights are $\leq \mathbf{w}(m_a')$. Since, for all $i \in [a]$, the coefficient of m_i' is in V and all of them are linearly independent, we know that $\dim(V) \geq a$. On the other hand, for every monomial m in f of $\mathbf{w}(m) \leq \mathbf{w}(m_a') < \mathbf{w}(m_a)$, the coefficient of m can be written as a linear combination of the coefficients of m_i 's where i < a. This implies that $\dim(V) < a$, which yields a contradiction. Thus, for all $i \in [\ell]$, $\mathbf{w}(m_i) \leq \mathbf{w}(m_i')$. In other words, $B \leq B'$.

Togetherwith
$$\mathbf{w}(m_j) < \mathbf{w}(m'_j)$$
, we get that $B < B'$ and $\mathbf{w}(B) < \mathbf{w}(B')$.

Next we want to study the effect of shifting f by a basis isolating weight assignment. To do that we require an elaborate notation. As before $f(\mathbf{x})$ is an arity-n degree-d polynomial over \mathbb{F}^k . For a weight assignment \mathbf{w} , by $f(\mathbf{x} + t^{\mathbf{w}})$ we denote the polynomial $f(x_1 + t^{w_1}, \dots, x_n + t^{w_n})$. Let $M = \{\mathbf{a} \in \mathbb{N}^n : |\mathbf{a}|_1 \leq d\}$ correspond to the relevant monomials. For every $\mathbf{a} \in M$, $\operatorname{coef}_{\mathbf{x}^a}(f(\mathbf{x} + t^{\mathbf{w}}))$ can be expanded using the binomial expansion, and we get:

$$\sum_{\mathbf{b} \in M} {b \choose \mathbf{a}} \cdot t^{\mathbf{w}(\mathbf{b}) - \mathbf{w}(\mathbf{a})} \cdot \operatorname{coef}_{\mathbf{x}^{\mathbf{b}}}(f(\mathbf{x})). \tag{3}$$

We express this data in matrix form as $F' = D^{-1}TD \cdot F$, where the matrices involved are,

- 1. F and F': rows are indexed by the elements of M and columns are indexed by [k]. In F resp. F' the **a**-th row is $\operatorname{coef}_{\mathbf{x}^{\mathbf{a}}}(f(\mathbf{x}))$ resp. $\operatorname{coef}_{\mathbf{x}^{\mathbf{a}}}(f(\mathbf{x}+t^{\mathbf{w}}))$.
- 2. D: is a diagonal matrix with both the rows and columns indexed by M. For $\mathbf{a} \in M$, $D_{\mathbf{a},\mathbf{a}} := t^{\mathbf{w}(\mathbf{x}^{\mathbf{a}})}$.
- 3. T: both the rows and columns are indexed by M. For $\mathbf{a}, \mathbf{b} \in M$, $T_{\mathbf{a}, \mathbf{b}} := \binom{\mathbf{b}}{\mathbf{a}}$.

We will prove the following combinatorial property of T: For any $B \subseteq M$, there is a cone-closed $A \subseteq M$ such that the submatrix $T_{A,B}$ has full rank. Our proof is an involved double-induction, so we describe the construction of A as Algorithm 1.

Lemma 25 (Comparison). Let B and B' be two nonempty subsets of M such that $B \subseteq B'$. Let A = FIND-CONE-CLOSED(B, n) and A' = FIND-CONE-CLOSED(B', n) in Algo.1. Then $A \subseteq A'$. Moreover, |A| = |B|.

Proof. We prove the lemma using induction on n.

Base case (n = 1): For n = 1, the set A is $\{0, \ldots, |B| - 1\}$ and the other one A' is $\{0, \ldots, |B'| - 1\}$. Since B is a subset of B', $|B| \le |B'|$. So A is also a subset of A'.

Induction step $(n-1 \to n)$: Let ℓ resp. ℓ' be the bounds on the size of preimages of π_n in B resp. B'. To denote the set of all elements in $\mathrm{Img}(\pi_n)$ whose preimage size $\geq i$, we use F_i resp. F'_i . Since $B \subseteq B'$ we have $\ell \leq \ell'$, and for all $i \in [\ell']$, $F_i \subseteq F'_i$. So from induction hypothesis, $S_i \subseteq S'_i$. Since $A = \bigcup_{i=1}^{\ell} S_i \times \{i-1\}$ and $A' = \bigcup_{i=1}^{\ell'} S'_i \times \{i-1\}$, we deduce that $A \subseteq A'$.

Algorithm 1 Finding cone-closed set

```
Input: A subset B of the n-tuples M.
Output: A cone-closed A \subseteq M with full rank T_{A,B}.
function FIND-CONE-CLOSED(B, n)
    if n = 1 then
        s \leftarrow |B|;
     return \{0, ..., s-1\};
        Let \pi_n be the map which projects the set of monomials B on the first n-1 variables;
        Let \ell be the maximum number of preimages under \pi_n;
        \forall i \in [\ell], F_i \text{ collects those elements in } \operatorname{Img}(\pi_n) \text{ whose preimage size} \geq i;
        A_0 \leftarrow \emptyset;
        for i \leftarrow 1 to \ell do
            S_i \leftarrow \text{FIND-CONE-CLOSED}(F_i, n-1);
            A_i \leftarrow A_{i-1} \bigcup S_i \times \{i-1\};
     return A;
    end if
end function
```

Note that |A| = |B| is true when n = 1. Let us prove the induction step from n-1 to n. Since $|A| = \sum_{i \in [\ell]} |S_i|$, and by induction hypothesis $|S_i| = |F_i|$, we deduce that $|A| = \sum_{i \in [\ell]} |F_i|$. From the definition of F_i 's we get that $\operatorname{Img}(\pi_n) = F_1 \supseteq F_2 \supseteq \cdots \supseteq F_\ell$. A monomial $m \in \pi_n(B)$ that has preimage size j, is counted exactly j times in $\sum_{i \in [\ell]} |F_i|$. Thus, $|A| = \sum_{i \in [\ell]} |F_i| = |B|$. \square

Lemma 26 (Closure). Let B be a nonempty subset of M. If A = Find-Cone-closed(B, n) in Algo.1, then A is cone-closed.

(Proved in Sec.C)

We recall a fact that has been used for ROABP PIT. (It requires char $\mathbb{F} = 0$ or large.)

Lemma 27. [GKS16, Clm.3.3] Let a_1, \ldots, a_n be distinct non-negative integers. Let A be an $n \times n$ matrix with, $i, j \in [n]$, $A_{i,j} := \binom{a_j}{i-1}$. Then, A is full rank.

```
Lemma 28 (Full rank). If A = \text{FIND-CONE-CLOSED}(B, n) then T_{A,B} has full rank. (Proved in Sec.C)
```

Now we are ready to prove our main theorem using the transfer matrix equation.

Proof of Thm.5. As we mentioned in Eqn.3, the shifted polynomial $f(\mathbf{x} + t^{\mathbf{w}})$ yields a matrix equation $F' = D^{-1}TD \cdot F$. Let k' be the rank of F. We consider the following two cases.

Case 1 (k' < k): We reduce this case to the other one where k' = k. Let S be a subset of k' columns such that $F_{M,S}$ has rank k'. The matrix $F_{M,S}$ denotes the polynomial $f_S(\mathbf{x}) \in \mathbb{F}[\mathbf{x}]^{k'}$, where $f_S(\mathbf{x})$ is the projection of the 'vector' $f(\mathbf{x})$ on the coordinates indexed by S. So, any linear dependence relation among the coefficients of $f(\mathbf{x})$ is also valid for $f_S(\mathbf{x})$. So \mathbf{w} is also a basis isolating weight assignment for $f_S(\mathbf{x})$. Now from our Case 2, we can claim that $f_S(\mathbf{x}+t^{\mathbf{w}})$ has a cone-closed basis A. Thus, coefficients of the monomials, corresponding to A, in $f(\mathbf{x})$ form a basis of $\operatorname{sp}(f)$. This implies that $f(\mathbf{x}+t^{\mathbf{w}})$ has a cone-closed basis A.

Case 2 (k' = k): Let B be the least basis of $f(\mathbf{x})$ wrt \mathbf{w} and A = FIND-CONE-CLOSED(B, n). We prove that the coefficients of monomials in A form a basis of the coefficient space of $f(\mathbf{x}+t^{\mathbf{w}})$.

To prove this, we show that $\det(F'_{A,[k]}) \neq 0$. Define T' := TDF so that $F' = D^{-1}T'$. Using Cauchy-Binet formula [Zen93], we get that

$$\det(F'_{A,[k]}) = \sum_{C \in \binom{M}{k}} \det(D_{A,C}^{-1}) \cdot \det(T'_{C,[k]}).$$

Since for all $C \in \binom{M}{k} \setminus \{A\}$, the matrix $D_{A,C}^{-1}$ is singular, we have $\det(F'_{A,[k]}) = \det(D_{A,A}^{-1}) \cdot \det(T'_{A,[k]})$. Again applying Cauchy-Binet formula for $\det(T'_{A,[k]})$, we get

$$\det(F'_{A,[k]}) = \det(D_{A,A}^{-1}) \cdot \sum_{C \in \binom{M}{k}} t^{\mathbf{w}(C)} \det(T_{A,C}) \cdot \det(F_{C,[k]}).$$

From Lem.24, we have that for all basis $C \in \binom{M}{k} \setminus \{B\}$, $\mathbf{w}(C) > \mathbf{w}(B)$. The matrix $T_{A,B}$ is nonsingular by Lem.28, and the other one $F_{B,[k]}$ is nonsingular since B is a basis. Hence, the sum is a nonzero polynomial in t. In particular, $\det(F'_{A,[k]}) \neq 0$, which ensures that the coefficients of the monomials corresponding to A form a basis of $\operatorname{sp}_{\mathbb{F}(t)}(f(\mathbf{x}+t^{\mathbf{w}}))$. Since Lem.26 says that A is also cone-closed, we get that $f(\mathbf{x}+t^{\mathbf{w}})$ has a cone-closed basis.

5 Conclusion

We introduce the tiny diagonal depth-4 (resp. tiny variants of depth-3, width-2 ABP and extremely low-arity $\Sigma\Pi\Sigma\wedge$ or $\Sigma\wedge^a\Sigma\Pi$) model with the motivation that its poly-time hitting-set would: (1) solve VP PIT (in quasipoly-time) via a poly-time arity reduction $(n\mapsto\log sd)$, and (2) prove that either $\mathbb{E}\not\subseteq\#P/\text{poly}$ or VNP has polynomials of arithmetic complexity $2^{\Omega(n)}$. Since now we could focus solely on the PIT of \log -arity VP circuits, we initiate a study of properties that are useful in that regime. These are low-cone concentration and cone-closed basis. Using these concepts we solve a special case of diagonal depth-3 circuits. This work throws up a host of tantalizing models and poses several interesting questions:

Could the arity reduction phenomenon in Thm.2 be improved (say, to 2^{2^n})?

Could we show that the g in Lem.6 is in VNP and not merely E-computable? This would strongly relate PIT to VNP \neq VP.

Could we prove nontrivial lower bounds against the tiny models?

Could we solve PIT for size-s $\Sigma\Pi\Sigma \wedge (n)$ in poly $(s, \mu(n))$ -time, for some function μ ?

Could we solve PIT for size-s semantic individual-degree-a' $\Sigma\Pi\Sigma(n)$ circuits in poly($s2^n$, $\mu'(a')$)-time, for some function μ' ?

Could we solve PIT for size-s $\Sigma\Pi\Sigma(n)$ in poly(s, $\mu(n)$)-time, for some function μ ?

Could we do blackbox PIT for ROABP when $n = \omega(1)$? For instance, given oracle $C = \sum_{i \in [k]} \prod_{j \in [n]} f_{i,j}(x_j)$ of size $\leq s$, we want a hitting-set in poly $(s, \mu(n))$ -time, for some function μ . It is known that diagonal depth-3 blackbox PIT reduces to this problem if we demand $\mu(n) = 2^{O(n)}$ [FSS14].

Could we do blackbox PIT for size-s arity-($\log s$) individual-degree-($\log \log s$) ROABPs?

Could we do blackbox PIT for size-s arity $\omega(\log s)$ multilinear ROABPs?

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A Proofs from Sec.2: Other tiny models

If a circuit C computes a polynomial of individual-degree $\leq d_0$ then we say that the *semantic* individual-degree bound of C is d_0 . Recall the definition of the tiny depth-3 model (Defn.12).

THEOREM 14 (RESTATED). [Tiny depth-3] If we have poly-time hitting-sets for a tiny depth-3 model, then for VP circuits we have a poly(sd)-time arity reduction $(n \mapsto O(\log sd))$ that preserves nonzeroness (and proves an exponential lower bound).

Proof. The proof strategy is identical to that of Thm.1. So, we will only sketch the main points here.

Let constant c > 1, and function μ' be given in the hypothesis. Let $\mathcal{C} \subset \mathcal{T}'_{\mu',c}$ be the set of tiny depth-3 circuits of size $\leq s$ and arity $m := \log s$. Assume that \mathcal{C} has a (s^e, s^e) -hsg $\mathbf{f}(y)$ for some constant $e \geq 1$. Then using Lem.6, we have an m-variate polynomial q_m with individual-degree less than some constant 2δ , $\delta^{m-1} = \delta^{\log s-1} > s^e \log s$, and q_m is computable in $s^{O(1)} = \delta^{O(m)}$ time. It has degree $= \delta m$. Importantly, $q_m \notin \mathcal{T}'_{\mu',c}$, thus, no tiny depth-3 circuit of size $\leq s = \delta^{\Theta(m)}$ can compute it (otherwise, $q_m(\mathbf{f}(y)) \neq 0$ which contradicts its definition as an annihilator). Next we show that it is also not computable by any $\delta^{o(m)}$ -size arithmetic circuit.

For the sake of contradiction, assume that q_m has a $\delta^{o(m)}$ -size circuit. Repeat the depthreduction arguments, as in the proof of Thm.1. Let $a := 5^t$ and $b := \delta m/2^t$. Note that we can ensure $a, b = o(\log s), a = \omega(1)$, and we have a shallow circuit for q_m of the form $\Sigma \Pi^a \Sigma \Pi^b$.

It was shown in [GKKS13] that any size-s' $\Sigma\Pi^a\Sigma\Pi^b(n)$ circuit can be transformed to a poly($s'2^{a+b}$)-size $\Sigma\Pi\Sigma^b(n)$ circuit. Applying it here, we get a depth-3 circuit C', computing q_m , of the form $\Sigma\Pi\Sigma$ and size $\delta^{o(m)} \cdot 2^{a+b} = \delta^{o(m)}$. Moreover, the measure $2^m + \mu'(2\delta) = s + O(1) = o(s^c)$. Thus, C' is a tiny depth-3 circuit in $\mathcal{T}'_{\mu',c}$, of size $\delta^{o(m)}$ which is < s. This contradicts the hardness of q_m . Thus, there is no arithmetic circuit for q_m of size $\delta^{o(m)}$.

Now invoking Lem.10 on the hard family $\{q_m\}_{m\geq 1}$, we get our claim. Moreover, by Cor.7 $\{q_m\}_m$ is an E-computable polynomial family that has arithmetic circuit complexity $2^{\Omega(m)}$. \square

We will now consider the polynomials that can be computed by upper-triangular width-2 arithmetic branching programs (ABP).

THEOREM 15 (RESTATED). [Width-2 ABP] If we have $poly(s2^n)$ -time hitting-sets for size-s arity-n width-2 upper-triangular ABP, then for VP circuits we have a poly(sd)-time arity reduction $(n \mapsto O(\log sd))$ that preserves nonzeroness (and proves an exponential lower bound).

Proof. In [SSS09, Thm.3] an efficient transformation was given that rewrites a size-s arity-n depth-3 circuit, times a special product of a linear polynomials, as a poly(s)-size arity-n width-2 upper-triangular ABP. Thus, a poly($s2^n$)-time hitting-set for the latter model gives a poly($s2^n$)-time hitting-set for the former. This by Thm.14 gives us the exponentially hard polynomial

family $\{q_m\}_{m\geq 1}$ that is E-computable. Now invoking Lem.10 on the hard family, we get the arity reduction.

THEOREM 16 (RESTATED). [Multilinear tiny depth-3] If we have poly-time hitting-sets for a multilinear tiny depth-3 model, then for VP circuits we have a poly(sd)-time arity reduction $(n \mapsto O(\log sd))$ that preserves nonzeroness (and proves an exponential lower bound).

Proof. The proof strategy is identical to that of Thm.1. So, we will only sketch the main points here. Let constant c > 1, and function μ' be given in the hypothesis. Let a' = a'(s) be a 'small' unbounded function determined by the constraint $\mu'(a') \leq s$. Assume that size-s arity- $(a' \log s)$ polynomials in $\mathcal{M}_{\mu',c}$ have a (s^e, s^e) -hsg $\mathbf{f}(y)$ for some constant $e \geq 1$.

Let $\mathcal{C} \subset \mathcal{M}_{\mu',c}$ be the set of multilinear tiny depth-3 circuits of size $\leq s$ and arity $m:=(e+2)\log s < a'\log s$. Since \mathcal{C} has the (s^e,s^e) -hsg $\mathbf{f}(y)$, so using Lem.6, we have an arity $m':=(e+1)\log s$ multilinear annihilating polynomial $q'_{m'}(x_1,\ldots,x_{m'})$, which is computable in $s^{O(1)}$ time (note: $2^{m'}=2^{(e+1)\log s}=s^{e+1}>s^e\cdot m'=s^e\cdot (e+1)\log s$). Consider its multiple $q_m:=q'_{m'}\cdot (x_{m'+1}\cdots x_m)$ which is also multilinear and an annihilator. It has degree $\geq (m-m')=\log s$. Importantly, $q_m\notin \mathcal{M}_{\mu',c}$, thus, no multilinear tiny depth-3 circuit of size $\leq s=2^{\Theta(m)}$ can compute it (otherwise, $q_m(\mathbf{f}(y))\neq 0$ which contradicts its definition as an annihilator). Next we show that it is also not computable by any $2^{o(m)}$ -size arithmetic circuit.

For the sake of contradiction, assume that q_m has a $2^{o(m)}$ -size circuit. Repeat the depth-reduction arguments, as in the proof of Thm.1, after cutting at depth $t = \omega(1)$. Let $a := 5^t$ and $b := m/2^t$. Note that we can ensure $a, b = o(m) = o(\log s)$, $a = \omega(1)$, and we have a shallow circuit for q_m of the form $\Sigma \Pi^a \Sigma \Pi^b$.

It was shown in [GKKS13] that any size-s' $\Sigma\Pi^a\Sigma\Pi^b(n)$ circuit can be transformed to a poly($s'2^{a+b}$)-size $\Sigma\Pi\Sigma^b(n)$ circuit. Applying it here, we get a depth-3 circuit C', computing q_m , of the form $\Sigma\Pi\Sigma$ and size $2^{o(m)} \cdot 2^{a+b} = 2^{o(m)}$. Moreover, the measure $\mu'(m/\log s) = \mu'(e+2) = O(1) = o(s^c)$. Thus, C' is a multilinear tiny depth-3 circuit in $\mathcal{M}_{\mu',c}$, of size $2^{o(m)}$ which is < s. This contradicts the hardness of q_m . Thus, there is no arithmetic circuit for q_m of size $2^{o(m)}$.

Now invoking Lem.10 on the hard family $\{q_m\}_{m\geq 1}$, we get our claim. Moreover, by Cor.7 $\{q_m\}_m$ is an E-computable polynomial family that has arithmetic circuit complexity $2^{\Omega(m)}$. \square

Theorem 17 (restated). [Tinier n,a] Fix functions $\mu = \mu(s)$ and μ' . If we have $poly(s,\mu(a),\mu'(n))$ -time hitting-set for size-s $\Sigma \wedge^a \Sigma \Pi(n)$ circuits, then for VP circuits we have a poly(sd)-time arity reduction $(n \mapsto O(\log sd))$ that preserves nonzeroness (and proves an exponential lower bound).

Proof. Suppose we have a poly $(s, \mu(a), \mu'(n))$ -time hitting-set $\mathcal{H}_{s,a,n}$ for size- $s \Sigma \wedge^a \Sigma \Pi(n)$ circuits. Wlog we can assume that $\mu'(n) = \Omega(n)$. Let $a' = a'(s) = \omega(1)$ be a function satisfying $\mu'(a') \leq s$, and define $n' = n'(s) := a' \log s$. Consider a size- $s \Sigma \wedge^a \Sigma \Pi(n')$ circuit $C \neq 0$ computing a multilinear polynomial. We intend to design a hitting-set for C.

Partition the variable set $\{x_1,\ldots,x_{n'}\}$ into a' blocks $B_j, j \in [a']$, each of size $\log s$. Recall the map φ designed in the proof of Thm.3. We have $\varphi(C) \neq 0$ (basically, use the fact that C computes a nonzero multilinear polynomial and φ keeps the multilinear monomials distinct). Finally, $\varphi(C)$ becomes an arity- $a' \sum \wedge^a \sum \Pi$ circuit of size at most $s+s \cdot 2^{\log s} = O(s^2)$. Thus, using $\mathcal{H}_{O(s^2),a,a'}$ we get a hitting-set for $\varphi(C)$ of time-complexity $\operatorname{poly}(s^2,\mu(a),\mu'(a')) = \operatorname{poly}(s,\mu(a))$.

In turn, we get a poly $(s, \mu(a))$ -time hitting-set for $\Sigma \wedge^a \Sigma \Pi$ circuits that compute multilinear polynomials of arity $a' \log s = \omega(\log s)$. Using an argument identical to that in the proof of Thm.16, we will get the usual E-computable polynomial family $\{q_m\}_m$ that has no arithmetic circuit of size $2^{o(m)}$. This finishes the proof by invoking Lem.10 and Cor.7.

B Proofs from Sec.3

An arity-n depth-3 diagonal circuit over \mathbb{F} can be written as $C(\mathbf{x}) = \sum_{i=1}^k c_i \tilde{f}_i^{d_i}$, where \tilde{f}_i 's are linear polynomials. Let f_i be the non-constant part of \tilde{f}_i for all $i \in [k]$. Suppose that $\mathrm{rk}_{\mathbb{F}}\{f_1,\ldots f_k\} =: r$. Wlog, we can assume that f_1,\ldots,f_r is a basis of the space spanned by f_i 's. Then there exists an r-variate polynomial $A(\mathbf{z})$ such that $C(\mathbf{x}) = A(f_1,\ldots,f_r)$. Let $L_{\mathbb{F}[\mathbf{x}]}$, where $\mathbf{x} = (x_1,\ldots,x_n)$, resp. $L_{\mathbb{F}[\mathbf{y}]}$, where $\mathbf{y} = (y_1,\ldots,y_r)$, be the vector space of linear polynomials over \mathbb{F} .

Using the construction of [SS12, Sec.3.2], in poly(knd)-time, we can find a linear transformation $\Psi: L_{\mathbb{F}[\mathbf{x}]} \to L_{\mathbb{F}[\mathbf{y}]}$ such that $\mathrm{rk}_{\mathbb{F}}\{\Psi(f_1), \ldots, \Psi(f_r)\} = r$ and $g_i := \Psi(f_i)$ are linear forms (i.e. homogeneous and degree one). Now we prove the following fact which will ensure the non-zeroness of $C(\Psi(\mathbf{x}))$.

Lemma 29. If $A(g_1, \ldots, g_r) = 0$ then A is the zero polynomial.

Proof. Since g_1, \ldots, g_r are r linearly independent linear forms on (y_1, \ldots, y_r) , we have an invertible linear map τ from $L_{\mathbb{F}[\mathbf{y}]}$ to itself such that $\tau(g_i) = y_i$, equivalently, $\tau^{-1}(y_i) = g_i$. Thus, τ^{-1} induces an \mathbb{F} -automorphism $\tilde{\tau}$ on $\mathbb{F}[\mathbf{y}]$.

Suppose that $A(g_1, \ldots, g_r) = 0$. Then, applying $\tilde{\tau}$ on $A(g_1, \ldots, g_r)$, we get $A(y_1, \ldots, y_r) = 0$, thus $A(\mathbf{z}) = 0$.

Theorem 22 (restated). Let C be the set of all arity-n degree-d size-s depth-3 diagonal circuits. Suppose that, for all $C \in C$, $rk(C) = O(\log sd)$. Then, the blackbox PIT for C can be solved in poly(sd)-time.

Proof. The above description gives us a nonzeroness preserving arity reduction $(n \mapsto \operatorname{rk}(C))$ method that reduces C to an $O(\log(sd))$ -variate degree-d poly(s)-size depth-3 diagonal circuit C'.

Clearly the dimension of the partial derivative space of C' is poly(sd) [For14, Lem.8.4.8]. Hence, Cor.21 gives us a poly(sd)-time hitting-set for C'.

C Proofs from Sec.4

LEMMA 26 (RESTATED). Let B be a nonempty subset of M. If A = Find-Cone-closed(B, n) in Algo.1, then A is cone-closed.

Proof. We prove it by induction on n.

Base case (n=1): For $n=1, A=\{0,\ldots,|B|-1\}$. So A is cone-closed.

Induction step $(n-1 \to n)$: Now $A = \bigcup_{i=1}^{\ell} S_i \times \{i-1\}$. Let **f** be an element in A and $\mathbf{x}^{\mathbf{e}}$ be a submonomial of $\mathbf{x}^{\mathbf{f}}$. We will show that $\mathbf{e} \in A$. Let $\mathbf{f} =: (\mathbf{f}', k)$ and $\mathbf{e} =: (\mathbf{e}', t)$, so that $t \leq k$. We divide our proof into the following two cases.

Case 1 (t = k): We have $\mathbf{f}' \in S_{k+1} = \text{Find-Cone-closed}(F_{k+1}, n-1)$. By induction hypothesis, S_{k+1} is cone-closed. Since $\mathbf{e}' \leq \mathbf{f}'$, we get $\mathbf{e}' \in S_{k+1}$. So, $\mathbf{e} = (\mathbf{e}', k) \in S_{k+1} \times \{k\}$, which implies that it is also in A.

Case 2 (t < k): We have $F_{k+1} \subseteq F_{t+1}$. By Lem.25, we get $S_{k+1} \subseteq S_{t+1}$. So $\mathbf{f}' \in S_{t+1}$. From induction hypothesis, S_{t+1} is a cone-closed set. This implies that $\mathbf{e}' \in S_{t+1}$ and $\mathbf{e} \in S_{t+1} \times \{t\}$. Thus, \mathbf{e} is also in A.

Since e was arbitrary, we deduce that A is cone-closed.

LEMMA 28 (RESTATED). If A = Find-Cone-Closed(B, n) then $T_{A,B}$ has full rank.

Proof. The proof will be by double-induction—outer induction on n and an inner induction on iteration i of the 'for' loop (Algo.1).

Base case: For n = 1, the claim is true due to Lem.27.

Induction step $(n-1 \to n)$: To show $T_{A,B}$ full rank, we prove that for any vector $\mathbf{b} \in \mathbb{F}^{|B|}$: if $T_{A,B} \cdot \mathbf{b} = 0$ then $\mathbf{b} = 0$. For this we show that the following invariant holds at the end of each iteration i of the 'for' loop (Algo.1).

Invariant (arity-n & i-th iteration): For each $\mathbf{f} \in B$ such that the preimage size of $\pi_n(\mathbf{f})$ is at most i, the product $T_{A_i,B} \cdot \mathbf{b} = 0$ implies that $\mathbf{b_f} = 0$.

At the end of iteration i=1, we have the vector $T_{A_1,B} \cdot \mathbf{b}$. Recall that $A_1 = S_1 \times \{0\}$ and $F_1 = \pi_n(B)$. So $T_{A_1,B} \cdot \mathbf{b} = T_{S_1,F_1} \cdot \mathbf{c}$, where for $\mathbf{e} \in F_1$, $\mathbf{c}_{\mathbf{e}} := \sum_{(\mathbf{e},k) \in \pi_n^{-1}(\mathbf{e})} \binom{k}{0} \mathbf{b}_{(\mathbf{e},k)}$. Thus, $T_{A_1,B} \cdot \mathbf{b} = 0$ implies $T_{S_1,F_1} \cdot \mathbf{c} = 0$. Since $S_1 = \text{FIND-CONE-CLOSED}(F_1, n-1)$, using induction hypothesis, we get that $\mathbf{c} = 0$. This means that for $\mathbf{e} \in B$ such that the preimage size of $\pi_n(\mathbf{e})$ is at most 1, we have $\mathbf{c}_{\mathbf{e}} = 0$. This proves our invariant at the end of the iteration i=1.

 $(i-1 \to i)$: Suppose that at the end of (i-1)-th iteration, the invariant holds. We show that it also holds at the end of the *i*-th iteration. For each $j \in [i]$, let \mathbf{v}_j denote the projection of $T_{A_i,B} \cdot \mathbf{b}$ on the coordinates indexed by $S_j \times \{j-1\}$. By focusing on the latter rows of $T_{A_i,B}$, we can see that $\mathbf{v}_j = T_{S_i,F_1} \cdot \mathbf{c}_j$ where the vector \mathbf{c}_j is defined as, for $\mathbf{e} \in F_1$,

$$(\mathbf{c}_j)_{\mathbf{e}} := \sum_{(\mathbf{e},k) \in \pi_n^{-1}(\mathbf{e})} {k \choose j-1} \cdot \mathbf{b}_{(\mathbf{e},k)}. \tag{4}$$

Suppose that $T_{A_i,B} \cdot \mathbf{b} = 0$. All we have to argue is that for every $\mathbf{f} \in B$ such that the preimage size of $\mathbf{e} := \pi_n(\mathbf{f})$ is i, the coordinate $\mathbf{b_f} = 0$.

Since $T_{A_i,B} \cdot \mathbf{b} = 0$, its projection $\mathbf{v}_j = T_{S_j,F_1} \cdot \mathbf{c}_j$ is zero too. By induction hypothesis (on i-1), for each $\mathbf{e} \in F_1$ with preimage size < i, the coordinate $(\mathbf{c}_j)_{\mathbf{e}} = 0$. Thus, the vector $T_{S_j,F_1} \cdot \mathbf{c}_j = T_{S_j,F_j} \cdot \mathbf{c}_j'$ where the vector \mathbf{c}_j' is defined as, for $\mathbf{e} \in F_j$, $(\mathbf{c}_j')_{\mathbf{e}} := \mathbf{c}_{j\mathbf{e}}$. Consequently, $T_{S_j,F_j} \cdot \mathbf{c}_j' = 0$, for $j \in [i]$. By induction hypothesis (on n-1), we know that T_{S_j,F_j} is full rank. So $\mathbf{c}_j' = 0$, which tells us that $\mathbf{c}_j = 0$, for $j \in [i]$.

Fix an $\mathbf{e} \in F_1$, with preimage size = i, and let the preimages be $\{(\mathbf{e}, k_1), \dots, (\mathbf{e}, k_i)\}$ where k_j 's are distinct nonnegative integers. Since $\mathbf{c}_j = 0$, for $j \in [i]$, we get from Eqn.4 and Lem.27 that: $\mathbf{b}_{(\mathbf{e},k_j)} = 0$ for all $j \in [i]$. In other words, for any $\mathbf{f} \in B$ such that the preimage size of $\pi_n(\mathbf{f})$ is i, the coordinate $\mathbf{b}_{\mathbf{f}} = 0$.

 $(i = \ell)$: Since $A = A_{\ell}$, the output of FIND-CONE-CLOSED(B, n), using our invariant at the end of ℓ -th iteration we deduce that $T_{A,B} \cdot \mathbf{b} = 0$ implies $\mathbf{b} = 0$. Thus, $T_{A,B}$ has full rank. \square

D Models with a cone-closed basis

We give a simple proof showing that a typical diagonal depth-3 circuit is already cone-closed. Consider the polynomial $D(\mathbf{x}) = (\mathbf{1} + \mathbf{a}_1 x_1 + \ldots + \mathbf{a}_n x_n)^d$ in $\mathbb{F}^k[\mathbf{x}]$, where \mathbb{F}^k is seen as an \mathbb{F} -algebra with coordinate-wise multiplication.

Lemma 30. $D(\mathbf{x})$ has a cone-closed basis.

Proof. Consider the *n*-tuple $L := (\mathbf{a}_1, \dots, \mathbf{a}_n)$. Then for every monomial $\mathbf{x}^{\mathbf{e}}$, the coefficient of $\mathbf{x}^{\mathbf{e}}$ in D is $L^{\mathbf{e}} := \prod_{i=1}^{n} \mathbf{a}_i^{e_i}$, with some nonzero scalar factor (note: here we seem to need char(\mathbb{F}) zero or large). We ignore this constant factor, since it does not affect linear dependence relations. Consider any proper monomial ordering \prec (eg. deg-lex). Now we prove that the 'least basis' of $D(\mathbf{x})$ with respect to this monomial ordering is cone-closed.

We incrementally devise a monomial set B as follows: Arrange all the monomials in ascending order. Starting from least monomial, put a monomial in B if its coefficient can not be written as a linear combination of its previous (thus, smaller) monomials. From construction, the coefficients of monomials in B form the least basis for the coefficient space of $D(\mathbf{x})$. Now we show that B is cone-closed. We prove it by contradiction.

Let $\mathbf{x^f} \in B$ and let $\mathbf{x^e}$ be its submonomial that is not in B. Then we can write

$$L^{\mathbf{e}} = \sum_{\mathbf{x}^{\mathbf{b}} \prec \mathbf{x}^{\mathbf{e}}} c_{\mathbf{b}} L^{\mathbf{b}} \text{ with } c_{\mathbf{b}}$$
's in \mathbb{F} .

Multiplying by L^{f-e} on both sides, we get

$$L^{\mathbf{f}} = \sum_{\mathbf{x}^{\mathbf{b}} \prec \mathbf{x}^{\mathbf{e}}} c_{\mathbf{b}} L^{\mathbf{b} + \mathbf{f} - \mathbf{e}} = \sum_{\mathbf{x}^{\mathbf{b}'} \prec \mathbf{x}^{\mathbf{f}}} c'_{\mathbf{b}'} L^{\mathbf{b}'}.$$

Note that $\mathbf{x}^{\mathbf{b}'} \prec \mathbf{x}^{\mathbf{f}}$ holds true by the way a monomial ordering is defined. This equation contradicts the fact that $\mathbf{x}^{\mathbf{f}} \in B$, and completes the proof.