

Expander-Based Cryptography Meets Natural Proofs

Igor C. Oliveira* Rahul Santhanam† Roei Tell‡

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Abstract

We introduce new forms of attack on *expander-based cryptography*, and in particular on Goldreich’s pseudorandom generator and one-way function. Our attacks exploit *low circuit complexity* of the underlying expander’s neighbor function and/or of the local predicate. Our two key conceptual contributions are:

1. We put forward the possibility that the *choice of expander* matters in expander-based cryptography. In particular, using expanders whose neighbour function has low circuit complexity might compromise the security of Goldreich’s PRG and OWF in certain settings.
2. We show that the security of Goldreich’s PRG and OWF over arbitrary expanders is closely related to two other long-standing problems: The existence of *unbalanced lossless expanders* with low-complexity neighbor function, and *limitations on circuit lower bounds* (i.e., natural proofs). In particular, our results further motivate the investigation of affine/local unbalanced lossless expanders and of average-case lower bounds against DNF-XOR circuits.

We prove two types of technical results. First, in the regime of quasipolynomial stretch (in which the output length of the PRG and the running time of the distinguisher are quasipolynomial in the seed length) we *unconditionally break Goldreich’s PRG*, when instantiated with a specific expander whose existence we prove, and for a class of predicates that match the parameters of the currently-best “hard” candidates. Secondly, conditioned on the existence of expanders whose neighbor functions have extremely low circuit complexity, we present attacks on Goldreich’s PRG in the *regime of polynomial stretch*. As one corollary, conditioned on the existence of the foregoing expanders, we show that either the parameters of natural properties for several constant-depth circuit classes *cannot be improved, even mildly*; or Goldreich’s PRG is insecure in the regime of a large polynomial stretch for some expander graphs, *regardless of the predicate used*.

*Department of Computer Science, University of Warwick. E-mail: igor.oliveira@warwick.ac.uk

†Department of Computer Science, University of Oxford. E-mail: rahul.santhanam@cs.ox.ac.uk

‡The Institute for Advanced Study and DIMACS, New Jersey. E-mail: roeitell@gmail.com

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1 Introduction

Theoretical results provide strong evidence that if secure cryptography is possible, then many fundamental primitives such as one-way functions (OWF) and pseudorandom generators (PRG) can be implemented with a dramatic level of efficiency and parallelism. Specifically, security against efficient adversaries can be achieved by functions where each output bit only depends on a constant number of input bits (see, e.g., [AIK06], and also [App14] for a survey of recent results).

A concrete type of such construction is a conjectured form of OWF that is based on any *expander graph* and on a *local predicate*. Specifically, about two decades ago, Goldreich [Gol00; Gol11] suggested the following candidate $\text{owf} : \{0,1\}^n \rightarrow \{0,1\}^n$. Fix any bipartite graph $[n] \times [n]$ of right-degree $\ell \leq O(\log(n))$ in which every set $S \subseteq [n]$ of size up to k on the right-hand side has at least (say) $1.01 \cdot |S|$ neighbors, and also fix a predicate $P : \{0,1\}^\ell \rightarrow \{0,1\}$. Then, given input $x \in \{0,1\}^n$, each output bit $\text{owf}(x)_i$ is computed by applying P to the bits of x at the ℓ neighbors of $i \in [n]$. The expected running-time of a naive algorithm for inverting owf is at least $\exp(k)$ (see, e.g., [Gol11, Sec. 3.2] and [App14, Sec. 3.1]), and Goldreich conjectured that for an appropriate predicate P , no algorithm can perform significantly better.

In an extensive subsequent line of research (see, e.g., [Ale11; MST06; ABW10; BQ12; ABR16; BR13; Co0+14; OW14; FPV15; AL18; AR16], and also see [App16] for a related survey), Goldreich’s construction was conjectured to yield not only a one-way function, but also a pseudorandom generator $\text{prg} : \{0,1\}^n \rightarrow \{0,1\}^m$. In fact, in some settings the two conjectures are essentially equivalent (see [AR16, Sec. 3]).

The question of whether Goldreich’s constructions are secure is a long-standing open problem. Much research has focused on necessary requirements from the *predicate* and from the *parameters* in order for the construction to be secure. Let us, for simplicity of presentation, focus on the PRG. In this case, the locality ℓ cannot be too small: If we want a PRG with super-linear stretch, then we must use $\ell \geq 5$ [MST06],¹ and if we want stretch $m = n^k$ then ℓ must be at least (roughly) $3k$ (see [OW14, Thm. II.11]). Also, as shown in [AL18], the predicate must have *high resilience* (i.e., all of the predicate’s Fourier coefficients corresponding to sets of size at most $\Omega(\ell)$ are zero; see Definition 4.8) and high *rational degree* (this is a generalization of the requirement that the degree of the predicate as a polynomial $\mathbb{F}_2^\ell \rightarrow \mathbb{F}_2$ is $\Omega(\ell)$; see Definition 4.9).

The foregoing properties capture most existing attacks in the PRG setting. Indeed, as mentioned above, all these attacks exploit vulnerabilities of the *predicate* and of the *parameters*, but not of the underlying expander. In fact, prior to our work, the PRG was conjectured to be secure for *any underlying expander* with sufficiently good expansion properties. For reference, let us state such a strong form of conjectured security of the OWF, from a recent work by Applebaum and Raykov [AR16].

Definition 1.1 (bipartite expander graphs) *A bipartite graph G is an (n, m, ℓ) -graph if it has n vertices on the left, m vertices on the right, and right-degree ℓ . An (n, m, ℓ) -graph is*

¹This impossibility result holds for *any* construction of a pseudorandom generator in \mathcal{NC}^0 .

a $(k, 1 - \epsilon)$ -expander if for every set $S \subseteq [m]$ on the right-hand side of size at most k , the number of neighbors of S in G is at least $(1 - \epsilon) \cdot \ell \cdot |S|$.² Finally, when we say a graph G is an (n, m, ℓ) -expander we mean that G is an (n, m, ℓ) -graph that is a $(n^{0.99}, 0.99)$ -expander.

Assumption 1.2 (the strong EOWF assumption). For a family $\mathcal{P} = \{P_\ell : \{0, 1\}^\ell \rightarrow \{0, 1\}\}_{\ell \in \mathbb{N}}$ of predicates, the strong EOWF(\mathcal{P}) assumption is the following. For any (n, m, ℓ) -expander with $\ell \leq n^{o(1)}$ and $n \leq m \leq n^{\alpha \cdot \ell}$, where $\alpha > 0$ is a sufficiently small universal constant, Goldreich’s function instantiated with G and P_ℓ cannot be inverted by circuits of size $t \leq \exp(\alpha \cdot n^{.99})$ with success probability $1/t$.

Applebaum and Raykov [AR16] suggested a suitable candidate predicate, which is the predicate $\text{XOR-MAJ}(x) = (\bigoplus_{i=1, \dots, \lfloor \ell/2 \rfloor} x_i) \oplus (\text{MAJ}(x_{\lfloor \ell/2 \rfloor + 1}, \dots, x_\ell))$; this predicate indeed has both high resiliency and high rational degree.

1.1 A high-level digest of our contributions

Our main contribution is a *new form of attack* on Goldreich’s pseudorandom generator, which exploits *computational complexity* properties (and, in particular, circuit complexity properties) of the expander and/or of the predicate on which the generator is based. In particular, our distinguishers are algorithms associated with *natural properties*, in the sense of Razborov and Rudich [RR97]. (Recall that a natural property against a circuit class \mathcal{C} is an efficient algorithm that distinguishes a random string, interpreted as a truth table, from truth tables of \mathcal{C} -circuits.)³

Conceptual implications. We use our new form of attack to break the PRG when it is instantiated with predicates that are sufficiently “strong” to withstand known attacks, but with expanders whose neighbor function has “low” circuit complexity. In high-level, the main conceptual implications of these results are the following:

1. The conjecture that the PRG and OWF are secure with *any expander*, given an appropriate predicate, *might be too naive*. In particular, the security of the constructions might crucially hinge on a choice of expander whose neighbor function has sufficiently high circuit complexity. Alternatively, if the latter is not true (i.e., if the PRG and OWF can be secure given any expander), then the predicate must have sufficiently high circuit complexity for the constructions to be secure in some settings (i.e., when the stretch is quasipolynomial).

Note that a random graph will (with high probability) not only be an expander, but also have a neighbor function with high circuit complexity. Therefore, our

²We stress that lossless expansion (i.e., expansion to $\alpha \cdot \ell \cdot |S|$ vertices for $\alpha > 1/2$) is crucial in the PRG setting. To see this, note that one can duplicate a right-vertex in a $(k, 0.99)$ -expander: This will produce a graph that, on the one hand, has good (but not lossless!) expansion properties, and on the other hand yields a corresponding PRG that is clearly insecure, regardless of the predicate.

³Natural properties are typically used to break *pseudorandom functions*, but the idea of using natural properties to break pseudorandom generators goes back to [RR97, Thm. 4.2]. Nevertheless, implementing this idea in our setting presents specific new challenges; for further discussion see Section 2.4.

results do not put into question the security of the PRG and OWF when instantiated with a random graph.

2. There are significant interdependencies between *the security of Goldreich’s PRG and OWF*, the existence of *unbalanced lossless expanders* with low-complexity neighbor function, and *limitations on circuit lower bounds* (i.e., natural proofs). Moreover (as further explained below), the questions motivated by our results are closely related both to existing results and to long-standing open problems in each area.

Results for quasipolynomial stretch. In the setting of quasipolynomial stretch, the output length m of the PRG is quasipolynomial in the seed length n (i.e., $m = 2^{\text{poly} \log(n)}$) and the running time of the distinguisher is polynomial in the output length (i.e., running time $\text{poly}(m) = 2^{\text{poly} \log(n)}$). This is not the standard setting for cryptographic application, yet it is a natural and appealing setting to consider.

In this setting we *unconditionally* break Goldreich’s PRG when it is instantiated with predicates with *high resilience and rational degree*, but with an expander whose neighbor function can be computed by $AC^0[\oplus]$ circuits of (small) subexponential size. In fact, our predicates are variations on the specific XOR-MAJ predicate mentioned above. Using a known reduction of PRGs to OWFs (by [AR16]), it follows that Assumption 1.2 *does not* hold for some predicates with high resilience and rational degree. To prove this result we actually prove the *existence* of expanders with neighbor function as above; the latter proof, which uses certain *unconditional* PRGs that can be computed in a *strongly explicit* fashion, might be of independent interest. (See Section 1.2.)

Results for polynomial stretch. In the regime of polynomial stretch (i.e., $m = \text{poly}(n)$), we put forward two assumptions about plausible extensions of known expander constructions in which the neighbor functions have even lower circuit complexity (compared to the expander mentioned above). Conditioned on *any* of the two assumptions, we show that exactly one of two options holds: Either the parameters of natural properties for certain restricted constant-depth circuit classes *cannot be improved, even mildly*; or Goldreich’s PRG is insecure for some expanders in the regime of a large polynomial stretch, *regardless of the predicate used*. (See Section 1.3.)

Some important cryptographic applications crucially rely on the security of expander-based PRGs with polynomial, or even linear, stretch (see, e.g., [App16, Sec. 4, “The Stretch”] and the references therein). We stress that our results for the setting of polynomial stretch are conditional on the existence of suitable expanders, and only break the PRG and OWF (when instantiated with these expanders) if there are natural properties for constant-depth circuit classes beyond what is currently known. Thus, further investigation is needed to determine whether our results have implications on the security of the aforementioned applications.

1.2 Unconditional results for quasipolynomial stretch

Our main result for the setting of quasipolynomial stretch is an attack that *unconditionally* breaks Goldreich’s PRG when it is instantiated with a *specific expander that has optimal expansion properties*, and with a class of predicates that have both high resilience and high rational degree. In the following statement, Goldreich’s PRG stretches n bits to $m = n^{\log(n)^k}$ bits, and the distinguisher is a uniform algorithm that gets an m -bit string, runs in time $\text{poly}(m) = n^{O(\log(n)^k)}$, and distinguishes the output of the PRG from uniform (see Definitions 3.2 and 3.3). In more detail:

Theorem 1.3 (*unconditional attack on Goldreich’s PRG with quasipolynomial stretch; informal*). *For every $d \in \mathbb{N}$, sufficiently large $k, c \in \mathbb{N}$, and sufficiently small $\epsilon > 0$, there exists a deterministic polynomial-time algorithm A that satisfies the following. Let $n \in \mathbb{N}$ be sufficiently large, let $m = n^{\log^k(n)}$, and let $\ell = c \cdot \log^k(n)$. Then, there exists an (n, m, ℓ) -expander such that for any predicate $P : \{0, 1\}^\ell \rightarrow \{0, 1\}$ that can be computed by an $\mathcal{AC}^0[\oplus]$ circuit of depth d and size at most 2^{ℓ^ϵ} , when Goldreich’s PRG is instantiated with the expander G and the predicate P , the algorithm A distinguishes the m -bit output of the PRG from a uniform m -bit string (with gap $> 1/2$).*

In fact, we actually prove a more general theorem, which exhibits a trade-off between the locality ℓ and the size of the $\mathcal{AC}^0[\oplus]$ circuit for the predicate P (for a precise statement see Theorem 4.7). That is, we are able to break the generator with a specific expander even with much larger locality (e.g., $\ell = n^{.01}$), at the expense of using a more restricted predicate family, namely that of $\mathcal{AC}^0[\oplus]$ circuits of smaller size (e.g., polynomial size). We stress that even the latter predicate family is rich enough to contain predicates that have both high resilience and high rational degree (see below).

Recall that the property of the expander $[n] \times [m]$ that we exploit in our attack is that its *neighbor functions* (i.e., the functions $\Gamma_i : [m] \rightarrow [n]$ for $i \in [\ell]$) have *low circuit complexity*. The expander in Theorem 1.3 in particular has neighbor functions that can be computed by $\mathcal{AC}^0[\oplus]$ circuits of small sub-exponential size. We prove the existence of lossless expanders with such neighbor functions in Section 4.1, in a result that might be of independent interest (see Section 2.2 for details). We comment that our construction is non-uniform (i.e., we do not obtain a uniform algorithm that constructs the graph).

Breaking Goldreich’s OWE. Combining Theorem 1.3 with Applebaum and Raykov’s reduction of expander-based PRGs to expander-based OWFs [AR16, Thm. 3.1] (i.e., they prove that if an arbitrary instance of Goldreich’s OWE is secure, then a closely related instance of Goldreich’s PRG is also secure), our attack also breaks Goldreich’s OWE when instantiated with a specific expander and with a rich predicate family. Specifically, we say that a predicate $P : \{0, 1\}^\ell \rightarrow \{0, 1\}$ is *sensitive* if it is “fully sensitive” to one of its coordinates (i.e., if for all $x \in \{0, 1\}^\ell$ it holds that $P(x) = x_i \oplus P'(x)$, for some $i \in [\ell]$ and P' that does not depend on x_i). Then:

Corollary 1.4 (unconditional attack on Goldreich’s OWF with quasipolynomial stretch; informal). *There exists a probabilistic polynomial-time algorithm A' that satisfies the following. Let $n \in \mathbb{N}$ be sufficiently large, let $m' = n^{k' = \text{poly} \log(n)}$, and let $\ell = O(k')$. Then, there exists an (n, m', ℓ) -expander such that for any sensitive predicate $P : \{0, 1\}^\ell \rightarrow \{0, 1\}$ that can be computed by an $\mathcal{AC}^0[\oplus]$ circuit of sufficiently small sub-exponential size, when Goldreich’s one-way function is instantiated with the expander G and the predicate P , the algorithm A inverts the function with success probability $\Omega(1/m'n)$.*

As immediate corollaries of Theorem 1.3 and of Corollary 1.4, we deduce that Assumption 1.2 does not hold for any sensitive predicate family that can be computed by $\mathcal{AC}^0[\oplus]$ circuits of sufficiently small sub-exponential size; and similarly, that the “PRG analogue” of Assumption 1.2, denoted $EPRG(\mathcal{P})$ in [AR16], does not hold for any predicate family that can be computed by $\mathcal{AC}^0[\oplus]$ circuits of sufficiently small sub-exponential size.

The predicate family and the expander’s locality. Recall that Applebaum and Raykov suggested the candidate predicate XOR-MAJ; we prove that when replacing majority by *approximate majority* (see Definition 4.10), the resulting predicate XOR-APPROX-MAJ still has both high resilience and high rational degree, and can also be computed by a polynomial-sized $\mathcal{AC}^0[\oplus]$ circuit (see Section 4.3). Thus, the predicate families in Theorem 1.3 and Corollary 1.4 contain predicates with high resilience and high rational degree, and even predicates that are variations on the “hard” candidate XOR-MAJ.⁴

Moreover, the predicate XOR-APPROX-MAJ does not even use the “full power” of the predicate family for which Theorem 1.3 allows us to break Goldreich’s PRG (with specific expanders) – the predicate XOR-APPROX-MAJ is computable by a circuit of polynomial size, whereas we can break the generator when the predicate can be computed by a circuit of sub-exponential size. We use this to our advantage by relying on the more general version of Theorem 1.3 (i.e., Theorem 4.7), which exhibits a trade-off between locality and the predicate class. Specifically, we obtain the following theorem, which breaks the generator even when the locality ℓ is large (e.g., $\ell = n^{\Omega(1)}$) and the predicate has high resilience and rational degree:

Theorem 1.5 (breaking Goldreich’s PRG with XOR-APPROX-MAJ and high locality). *There exists $s > 1$ and $c \in \mathbb{N}$ such that for $k(n) = \log(n)^s$ and $m(n) = n^{k(n)}$ and every time-computable $\ell(n) \in [c \cdot k(n), n^{1/c}]$ there exists a polynomial-time algorithm A satisfying the following. For any sufficiently large $n \in \mathbb{N}$, there exists an $(n, m(n), \ell(n))$ -expander and a predicate $P : \{0, 1\}^{\ell(n)} \rightarrow \{0, 1\}$ with resilience $\Omega(\ell(n))$ and rational degree $\Omega(\ell(n))$ (i.e., the predicate XOR-APPROX-MAJ) such that when Goldreich’s PRG is instantiated with the expander G and the predicate P , the algorithm A distinguishes the m -bit output of the PRG from a uniform m -bit string (with gap $> 1/2$).*

⁴Indeed, the main difference between XOR-MAJ and XOR-APPROX-MAJ seems to be in their *circuit complexity*, which corresponds to our main point that circuit complexity considerations are crucial for the security of Goldreich’s PRG and OWF.

We stress again that in Theorem 1.5, as in Theorem 1.3, the PRG stretches n bits to $m = n^{\log(n)^s}$ bits, and the distinguisher is a uniform algorithm that gets as input an m -bit string and runs in time $\text{poly}(m) = n^{O(\log(n)^s)}$.

A comment about the setting of quasipolynomial stretch. As pointed out by a reviewer, in cryptographic applications the quasipolynomial setting can be thought of as trying to attack a PRG with quasipolynomial stretch in time that is quasipolynomial in the seed length. An alternative perspective, which we elaborate on in Section 2, is to think of Goldreich’s PRG $\{0,1\}^n \rightarrow \{0,1\}^m$ as a pseudorandom function (PRF) with seed length n and domain $\log(m) = \text{poly} \log(n)$ (i.e., the PRF maps seed $x \in \{0,1\}^n$ to a function $g_x: \{0,1\}^{\log(m)} \rightarrow \{0,1\}$), in which case the attack is quasipolynomial in the seed length and polynomial in the size m of the truth-table of the PRF.

1.3 Conditional results for large polynomial stretch

Recall that the conjectured “hardness” of Goldreich’s PRG (i.e., Assumption 1.2) refers both to the regime of polynomial stretch and to the regime of quasipolynomial stretch (as long as the locality is sufficiently large to support the corresponding stretch). Could it be that complexity-based attacks separate these two parameter regimes? That is, could the reason that our attacks from Section 1.2 work be that the stretch of the generator is super-polynomial?

As mentioned in Section 1.1 (and will be explained in Section 2), the underlying technical components in our complexity-based attacks are *unbalanced lossless expanders* $[n] \times [m]$ whose neighbor functions have low circuit complexity, and *natural properties* against weak circuit classes. Our main results for the polynomial-stretch regime are of the following form: If lossless expanders $[n] \times [n^{O(1)}]$ with constant degree and (specific) “very simple” neighbor functions exist, then exactly one of two cases holds:

1. Either the parameters of natural properties for certain well-studied weak circuit classes *cannot be improved*, even mildly; or
2. For a sufficiently large polynomial stretch, Goldreich’s PRG is insecure when instantiated with a specific expander, *regardless of the predicate used*.

We now present two plausible assumptions on existence of suitable expanders, which are essentially improvements or extensions of existing explicit constructions. Conditioned on each assumption, we will contrast the security of Goldreich’s PRG with the possibility of extending natural proofs for some well-studied circuit class.

1.3.1 Affine expanders and DNF-XOR circuits

As motivation for our first assumption, let us recall two well-known *explicit* constructions of unbalanced lossless expanders, which were given by Ta-Shma, Umans, and Zuckerman [TUZ07], and later on by Guruswami, Umans, and Vadhan [GUV09]. We note that these two constructions are inherently different (the relevant construction

from [TUZ07] is combinatorial, whereas the construction of [GUV09] is algebraic), and yet in both constructions the neighbor function of the expander can be computed by *single layer of parity gates* (see Section 5.1 for further details); we will call expanders with such a neighbor function *affine expanders*.

In the two foregoing affine expanders, the right-degree ℓ is polylogarithmic, and it is an open problem to improve the degree to be constant, which matches the degree of a random construction. However, a random construction is not necessarily affine. Our first assumption is that there indeed exists an *affine expander with constant degree*:

Assumption 1.6 (*expanders with an affine neighbor function; informal, see Assumption 5.3*). *There exists $\beta > 3$ such that for every constant $k \in \mathbb{N}$ and sufficiently large $n \in \mathbb{N}$, there exists an (n, m, ℓ) -expander, where $m = n^k$ and $\ell = \beta \cdot k$, whose neighbor function $\Gamma_G : [m] \rightarrow ([n])^\ell$ can be computed by a single layer of parity gates.*

An unconditional proof of Assumption 1.6 will contrast the security of Goldreich’s PRG with the possibility of extending the known natural properties for DNF-XOR circuits of exponential size.⁵ Specifically, known lower bounds for DNF-XOR circuits yield natural properties useful against such circuits of size up to $2^{(1-o(1)) \cdot n}$ (see Section 5.1.2).⁶ Can these natural properties be extended to functions that are *approximated*, in the average-case sense, by DNF-XOR circuits of size $2^{\epsilon \cdot n}$, for some $\epsilon > 0$? This is the natural property that we contrast with the security of Goldreich’s PRG:

Theorem 1.7 (*is Goldreich’s PRG always insecure with certain expanders, or are natural properties for DNF-XOR circuits “non-extendable”?; informal statement*). *Suppose that Assumption 1.6 holds. Then, exactly one of the following two options holds:*

1. *For all $\epsilon > 0$, there does not exist a natural property for the class of functions that can be approximated with success $1/2 + o(1)$ by DNF-XOR circuits of size $2^{\epsilon \cdot n}$.*
2. *For a sufficiently large $k \in \mathbb{N}$ there exists an expander G over $[n] \times [n^k]$ with right-degree $\ell = \beta \cdot k$ such that for every predicate $P: \{0, 1\}^\ell \rightarrow \{0, 1\}$, Goldreich’s PRG is insecure when instantiated with G and P .*

We stress that for any value of $\beta > 3$ such that Assumption 1.6 holds, Theorem 1.7 follows with that value of β . Also note that Cohen and Shinkar [CS16] specifically conjectured that strong average-case lower bounds for DNF-XOR circuits of size $2^{\Omega(n)}$ hold, and proved a similar statement for the related-yet-weaker model of parity decision trees. (Their proof for parity decision trees indeed yields a natural property; see Proposition 5.12.)

⁵Recall that DNF-XOR circuits are depth-3 circuits that consist of a top OR gate, a middle layer of AND gates, and a bottom layer of parities above the inputs.

⁶Some of these natural properties actually run in slightly super-polynomial time, rather than in strictly polynomial time, but this issue is not crucial for our purpose of breaking Goldreich’s PRG.

1.3.2 \mathcal{NC}^0 expanders and weak \mathcal{AC}_4^0 circuits

To motivate our next assumption, recall the recent explicit construction of lossless expanders by Viola and Wigderson [VW17] (which builds on the well-known construction of Capalbo *et al.* [Cap+02]). In this construction the neighbor function can be computed by an \mathcal{NC}^0 circuit, but this construction is only for *balanced* expanders, rather than unbalanced ones (see Theorem 5.19). The following assumption is that such a construction is possible also for unbalanced expanders:

Assumption 1.8 (*expanders with \mathcal{NC}^0 neighbor functions; informal, see Assumption 5.20*). *There exists $\beta > 3$ such that for every constant $k \in \mathbb{N}$ and sufficiently large $n \in \mathbb{N}$, there exists an (n, m, ℓ) -expander, where $m = n^k$ and $\ell = \beta \cdot k$, such that the neighbor function $\Gamma_G : [m] \rightarrow ([n])^\ell$ can be computed by an \mathcal{NC}^0 circuit.*

An unconditional proof of Assumption 1.8 will contrast the security of Goldreich’s PRG with the possibility of extending the known natural properties for the class of exponential-sized \mathcal{AC}^0 circuits of depth four with constant bottom fan-in and top fan-in.

Specifically, relying on Håstad’s switching lemma [Hås87], for any $c = O(1)$ and $\beta \geq 1$ there exists a natural property against depth-four circuits with top fan-in c , bottom fan-in $\log(c)/\beta$, and size $2^{\epsilon \cdot (n/\log(c))}$, for a tiny universal $\epsilon = \epsilon(c, \beta) > 0$ (see Proposition 5.22). Now, suppose that Assumption 1.8 holds for some $\beta > 3$, and for simplicity assume that the locality of the \mathcal{NC}^0 circuits is precisely k . Then, the following theorem contrasts the security of Goldreich’s PRG with the possibility of extending this natural property to work against such circuits of size $2^{\beta \cdot (n/\log(c))}$ instead of $2^{\epsilon \cdot (n/\log(c))}$.

Theorem 1.9 (*is Goldreich’s PRG always insecure with certain expanders, or are natural properties for very restricted \mathcal{AC}^0 circuits “non-extendable”?; informal statement*). *Suppose that Assumption 1.8 holds and that for any $k \in \mathbb{N}$, the locality of the \mathcal{NC}^0 circuit is k . Then, exactly one of the following two options holds:*

1. *For any $c \in \mathbb{N}$, there does not exist a natural property for depth-four \mathcal{AC}^0 circuits with top fan-in c and bottom fan-in $\log(c)/\beta$ and size $O\left(2^{(\beta/\log(c)) \cdot n}\right)$.*
2. *For a sufficiently large $k \in \mathbb{N}$ there exists an expander G over $[n] \times [n^k]$ with right-degree $\ell = \beta \cdot k$ such that for every predicate $P : \{0, 1\}^\ell \rightarrow \{0, 1\}$, Goldreich’s PRG is insecure when instantiated with G and P .*

Recall that Assumption 1.8 is parametrized by β and by the locality of the \mathcal{NC}^0 circuit; we stress that for *any* values of β and t such that Assumption 1.8 holds, we get a corresponding “win-win” theorem such as Theorem 1.9 (for further details see Section 5.2). We also stress that both the natural properties that we can unconditionally prove and the natural properties referred to in Theorem 1.9 are for circuits of exponential size $2^{\Theta(n/\log(c))}$, and the difference is in the universal constant hidden in the Θ -notation.

1.3.3 An important comment about the assumptions in this section

As mentioned in Section 1.1, the *explicit* construction of highly unbalanced lossless expanders is a long-standing open problem, regardless of the circuit complexity of their neighbor function (see, e.g., [Cap+02], [Vad12, Prob. 5.36 & 6.35], and [Wig18, Chap. 8.7]). Assumptions 1.6 and 1.8, however, *do not concern explicit constructions* of expanders, but only assume their *existence*; in particular, the circuit family for the neighbor function of the graph may be *non-uniform*. (This is indeed the case for our construction of expanders in the quasipolynomial stretch regime.)

1.4 Organization

In Section 2 we describe our proof approach, in high-level, and give overviews of some of the proofs. Section 3 contains preliminary definitions. In Section 4 we prove our results for the regime of quasipolynomial stretch, and in Section 5 we prove our results for the regime of polynomial stretch.

2 Overviews of the proofs

2.1 The general form of attack

A natural property for a class \mathcal{F} of functions is a deterministic polynomial-time algorithm that rejects all truth-tables of functions from \mathcal{F} , but accepts the truth-tables of almost all functions.⁷ Indeed, a natural property for \mathcal{F} exists only if almost all functions are *not* in \mathcal{F} . We will show how to use natural properties to break Goldreich’s pseudorandom generator when instantiated with a suitable expander.

The key step in our proofs is to show, for every fixed $x \in \{0,1\}^n$, that $\text{prg}(x)$ is the truth-table of a function from some class \mathcal{F} of “simple” functions (e.g., $\text{prg}(x)$ is the truth-table of a small constant-depth circuit). When we are able to show this, it follows that a natural property for \mathcal{F} can distinguish the outputs of the PRG from uniformly chosen random strings: This is because the natural property rejects any string in the output-set of the PRG (which is the truth-table of a function in \mathcal{F}), but accepts a random string, with high probability. (The general idea of using natural properties to break PRGs in this manner goes back to the original work of [RR97].)

Recall that Goldreich’s PRG (i.e., the function prg) is *always* a very “simple” function, since each output bit depends on a few (i.e., $\ell \ll n$) input bits. However, in order for our idea to work, we need that a *different* function (i.e., not the function prg) will be simple: Specifically, for every *fixed* input x , we want that the function $g_x : \{0,1\}^{\log(m)} \rightarrow \{0,1\}$ such that $g_x(i) = \text{prg}(x)_i$ will be “simple”. That is, for a *fixed* “seed” x for the PRG, the function g_x gets as input an index i of an output bit, and

⁷Throughout the paper, we identify a natural property with the “constructive” algorithm that recognizes the property (see Definition 3.5, which also relaxes the requirement from rejecting “almost all” functions to rejecting “most” functions).

computes the i^{th} output bit of $\text{prg}(x)$ as a function of i . Intuitively, given $i \in [m]$, the function g_x needs to compute three different objects, successively:

- The neighbors $\Gamma_G(i)$ of the vertex $i \in [m]$ in G .
- The projections of the (fixed) string x on locations $\Gamma_G(i)$.
- The output of the predicate P on $x|_{\Gamma_G(i)}$.

The proofs of our main theorems consist of showing instantiations of Goldreich’s PRG (i.e., choices for an expander and a predicate) such that g_x is a function from a class against which we can construct natural properties. Observe that the two crucial components that determine the complexity of g_x are the complexity of the neighbor function Γ_G , and the complexity of the predicate P .

The connection to expander-based pseudorandom functions. An alternative perspective of the construction of g_x above is as giving rise to a collection of *pseudorandom functions* (PRFs) $\left\{g_x : \{0,1\}^{\log(m)} \rightarrow \{0,1\}\right\}_{x \in \{0,1\}^n}$ that are based on (an instantiation of) Goldreich’s PRG. Indeed, the crucial point is that the transformation above of Goldreich’s PRG to a PRF is extremely simple and incurs very little complexity overhead (on top of the complexities of Γ_G and of P).

As pointed out by a reviewer, from this perspective one can view our construction as following an approach first introduced by Applebaum and Raykov [AR16]. They showed how to construct relatively simple PRFs (or, alternatively, very simple weak PRFs) based on Goldreich’s PRG, where a crucial point in deducing the simplicity of the PRFs was that the expander’s neighbor function is local. Similarly to their work, we also transform Goldreich’s PRG to a “candidate PRF”, but since we assume additional structure on the former (i.e., we assume that Γ_G and/or P are computationally simple) we can deduce that the latter is significantly simpler than in their work.

The crucial conceptual difference between our work and [AR16] is that they *assumed* that Goldreich’s PRG is secure when instantiated with a random expander, and *deduced* that relatively simple PRFs exist. In contrast, in this work we *break* the “candidate PRFs” that our transformation yields (or rely on hypotheses that it can be broken), and we thus deduce that the specific corresponding instantiations of Goldreich’s PRG are *insecure*. Indeed, this crucially uses the fact that our “candidate PRFs” are considerably simpler than in [AR16]. For further discussion see Section 2.4.

2.2 The setting of quasipolynomial stretch

The proof of Theorem 1.3 consists of showing that for a suitable expander G , and for any predicate P computable by an $\mathcal{AC}^0[\oplus]$ circuit of sufficiently small sub-exponential size, the function g_x can be computed by an $\mathcal{AC}^0[\oplus]$ circuit of sufficiently small sub-exponential size. Natural properties for such circuits, based on the lower bounds by Razborov and Smolensky [Raz87; Smo87], are well-known (see, e.g., [RR97; CIKK16]).

To describe the instantiations and the construction of an $\mathcal{AC}^0[\oplus]$ circuit for g_x , let $n \in \mathbb{N}$, and let $m = 2^{(\log(n))^k}$, for a sufficiently large k . The first technical component that we need is an expander graph G such that the function $i \mapsto \Gamma_G(i)$ can be computed by a sub-exponential sized $\mathcal{AC}^0[\oplus]$ circuit. We show that there exists such a graph, with essentially optimal parameters:

Theorem 2.1 (strongly explicit lossless expander in $\mathcal{AC}^0[p]$; see Theorem 4.6). *There exists a universal constant $d_G \in \mathbb{N}$ such that the following holds. For any $k \in \mathbb{N}$ and sufficiently large n there exists an (n, m, ℓ) -expander, where $m = 2^{(\log(n))^k}$ and $\ell = O(\log(m) / \log(n))$, and an $\mathcal{AC}^0[\oplus]$ circuit $C_G : \{0, 1\}^{\log(m)} \rightarrow \{0, 1\}^{\ell \cdot \log(n)}$ of depth d_G and size $\text{poly}(n)$ such that for every $i \in [m]$ it holds that $C_G(i)$ outputs the list of ℓ neighbors of i in G .*

We stress that the depth d_G of the circuit in Theorem 2.1 does not depend on the relation between m and n , which is what will allow us to have a natural property for the circuit C_G . Specifically, recall that we have natural properties against $\mathcal{AC}^0[\oplus]$ circuits of depth d_G over $\ell_m = \log(m)$ input bits of sub-exponential size $2^{\Omega(\ell_m^{1/2d_G})}$. The size of C_G is $\text{poly}(n)$, and thus if we take $m = 2^{(\log(n))^k}$, for a sufficiently large k , then the size of C_G is a sufficiently small sub-exponent in its input length $\log(m)$.

In high-level, our construction of the expander in Theorem 2.1 is as follows. Our starting point is the well-known fact that a random graph is, with high probability, a good lossless bipartite expander (see Theorem 3.4). The first step is to construct an *efficient test* that gets as input a string $G \in \{0, 1\}^{m'}$, where $m' = m \cdot \ell \cdot \log(n)$, considers G as the incidence-list of a graph, and decides whether or not G is an $(n^{.99}, .99)$ -expander. We show that such a test can be implemented by a CNF of size 2^n (see Claim 4.2). Hence, a pseudorandom generator for CNFs of size 2^n outputs, with high probability, a good expander. Specifically, we will use the pseudorandom generator of Nisan [Nis91], which has seed length $\text{poly}(n)$. Thus, for some fixed “good” seed s , the output $NW(s) \in \{0, 1\}^{m'}$ of the generator on s is an $(n^{.99}, .99)$ -expander.

Our next step is to show that the expander represented by $NW(s)$ has neighbor functions that can be computed by an $\mathcal{AC}^0[\oplus]$ circuit. In fact, we will show that there exists a circuit that gets as input the index $i \in \{0, 1\}^{\log(m')}$ of a bit in $NW(s)$ and outputs $NW(s)_i$. To do so we can rely, for instance, on the recent work of Carmosino *et al.* [CIKK16], who showed that Nisan’s generator can be made “strongly explicit”: That is, there exists an $\mathcal{AC}^0[\oplus]$ circuit of polynomial size that gets as input a seed z and an index i of an output bit, and computes the i^{th} output bit of the generator on seed z .⁸ By “hard-wiring” a “good” seed s into the latter circuit, we obtain an $\mathcal{AC}^0[\oplus]$ circuit of size $\text{poly}(n)$ that computes the output bits of the expander $NW(s)$. Indeed, a crucial point is that we did not algorithmically look for a good seed s , but rather non-uniformly fixed a “good” seed and “hard-wired” it into the circuit.

Given this expander construction, g_x can compute $i \mapsto \Gamma_G(i)$ in sub-exponential size, and we now need g_x to compute the projections of x on locations $\Gamma_G(i)$. To do so we simply “hard-wire” the entire string x into g_x . Specifically, after computing

⁸A similar observation has appeared in other works, such as in [RR97, Thm. 4.2].

the function $i \mapsto \Gamma_G(i)$, the circuit now has the $\ell \cdot \log(n)$ bits of $\Gamma_G(i)$; it then uses ℓ depth-two formulas, each over $\log(n)$ bits and of size n , to compute the mapping $\Gamma_G(i) \mapsto x \upharpoonright_{\Gamma_G(i)}$ by brute-force. This increases the size of the circuit for g_x by $\ell \cdot n < n^2$ gates, which is minor compared to the size $\text{poly}(n)$ of C_G from Theorem 2.1.

Finally, the circuit g_x has now computed the ℓ bits corresponding to $x \upharpoonright_{\Gamma_G(i)}$, and needs to compute the predicate $P : \{0,1\}^\ell \rightarrow \{0,1\}$ on these bits. To get the circuit to be of sufficiently small sub-exponential size, we require that the predicate can be computed by a sufficiently small sub-exponential sized $\mathcal{AC}^0[\oplus]$ circuit. Specifically, we want that for some d_p , the predicate P can be computed by an $\mathcal{AC}^0[\oplus]$ circuit of depth d_p and size 2^{ℓ^ϵ} , for a sufficiently small $\epsilon < 1/2(d_G + d_p + 2)$. We thus obtain a circuit for g_x of depth $d = d_G + d_p + 2$ and of size $O(2^{\ell^\epsilon}) < 2^{\log(m)^{1/2d}}$,⁹ which is sufficiently small such that we have natural properties against it (see Theorem 3.6).

2.3 The setting of large polynomial stretch

Why are the results in Section 2.2 applicable only to the setting of quasipolynomial stretch? The main bottleneck is the expander construction in Theorem 2.1, which is an $\mathcal{AC}^0[\oplus]$ circuit. Specifically, since we only know of natural properties against $\mathcal{AC}^0[\oplus]$ circuits of at most sub-exponential size, and since the circuit that we obtain is of size at least n (because we hard-wire $x \in \{0,1\}^n$ to the circuit), we were forced to take $m = n^{\text{poly} \log(n)}$ such that n will be a small sub-exponential function of $\log(m)$.

In this section we circumvent this obstacle by using the hypothesized existence of expanders whose neighbor functions have “extremely simple” circuits. For simplicity, in the current high-level overview we present the attacks that are based on the existence of an expander as in Assumption 1.6; that is, a lossless expander $G = ([n], [m = n^k], E)$ of right-degree $\ell = O(k)$ whose neighbor function is an *affine function* (i.e., each output bit is a parity of input bits). The ideas that underlie the attacks that are based on expanders whose neighbor function is an \mathcal{NC}^0 circuit (as in Assumption 1.8) are similar, yet require a slightly more subtle parametrization (see Section 5.2).

Consider an instantiation of Goldreich’s predicate with expander G as above and with a predicate $P : \{0,1\}^\ell \rightarrow \{0,1\}$ that can be computed by a CNF of size $2^{\delta \cdot \ell}$, where δ can be an arbitrarily large constant compared to k (or even $\delta = 1$, which allows for *any* predicate). In this case, for any $x \in \{0,1\}^n$, the output $\text{prg}(x)$ of the generator on x is a truth-table of a function g_x over an input $i \in \{0,1\}^{\log(m)}$ that can be computed as follows. One layer of *parity gates* maps $i \in [m]$ to $\Gamma_G(i) \in \{0,1\}^{\ell \cdot \log(n)}$ (this uses our assumption about the expander). Then, ℓ copies of a DNF over $\log(n)$ bits and of size n map the names of the ℓ vertices to $x \upharpoonright_{\Gamma_G(i)} \in \{0,1\}^\ell$, i.e., we project the bits of x that feed the predicate P (this DNF is essentially a “hard-wiring” of x into g_x). Finally, the CNF that computes P of size $2^{\delta \cdot \ell}$ maps $x \upharpoonright_{\Gamma_G(i)}$ to the value $P(x \upharpoonright_{\Gamma_G(i)})$. After collapsing a layer that connects the top CNF and the DNFs, we obtain an AND-OR-AND-XOR

⁹For this calculation we assumed that 2^{ℓ^ϵ} dominates the size of the circuit (since the size of C_G is already sufficiently small); and we used the fact that $\ell = O(\log(m) / \log(n)) < \log(m)$, and that $\epsilon < 1/2d$ is sufficiently small).

circuit g_x over $\ell_m = \log(m)$ input bits of size $O(\ell \cdot \log(n) + \ell \cdot n + 2^{\ell \cdot \delta}) = O(2^{\ell_m/k})$ with top fan-in $2^{\delta \cdot \ell} = 2^{O(\delta \cdot k)}$.

When $\delta > 0$ is sufficiently small, we are able to unconditionally construct a natural property against circuits as above. However, the main point (i.e., Theorem 1.7) comes when considering the case $\delta = 1$; that is, *any* predicate $P : \{0,1\}^\ell \rightarrow \{0,1\}$. In this case, we first use the discriminator lemma of [HMP+93] to deduce that g_x can be $(1/2 + 1/2^{O(k)})$ -approximated by a DNF-XOR circuit of size $O(2^{\ell_m/k})$. Now (still under Assumption 1.6), exactly one of two options holds. The first option is that there exists a natural property for functions on ℓ_m input bits that can be $(1/2 + o(1))$ -approximated by DNF-XOR circuits of size $2^{\Omega(\ell_m)}$; in this case, by taking k sufficiently large so that $2^{\ell_m/k}$ is sufficiently small, the natural property breaks the generator. The other option is that no such natural property exists, despite the fact that natural properties exist both for functions computed (in the worst-case) by DNF-XOR circuits of size $2^{(1-o(1)) \cdot \ell_m}$, and for functions approximated (even weakly) by parity decision trees of such size. This completes the sketch of the proof of Theorem 1.7.

2.4 The connection to expander-based pseudorandom functions

As mentioned in Section 2.1, our construction of the function $g_x : \{0,1\}^{\log(m)} \rightarrow \{0,1\}$ (i.e., $g_x(i) = P(x|_{\Gamma_G(i)})$) can be viewed as a construction of a collection of *pseudorandom functions* (PRFs) $\{g_x : \{0,1\}^{\log(m)} \rightarrow \{0,1\}\}_{x \in \{0,1\}^n}$ based on (an instantiation of) Goldreich’s PRG. Indeed, crucially, we show (or assume) that g_x is *not* pseudorandom, and thus deduce that the corresponding instantiation of Goldreich’s PRG is *insecure*.

The crucial point in our transformation of Goldreich’s PRG to a PRF is that the resulting PRF can have very low circuit complexity, depending essentially only on the complexity of the expander’s neighbor function and of the predicate. In contrast, previously known transformations of Goldreich’s PRG to a PRF incur a significant overhead. Specifically, the transformation of Goldreich, Goldwasser, and Micali [GGM86] yields a circuit with super-constant depth; whereas the constructions of Applebaum and Raykov [AR16] either yield only a *weak* PRF (which is not broken by natural properties, in general) or require complicated computations, which they implement using majority gates (i.e., the resulting function is in the class \mathcal{TC}^0 , for which natural properties are neither known nor conjectured to exist). The reason that we are able to obtain “candidate PRFs” that are so simple is that we rely on specific expanders with neighbor functions whose (non-uniform) complexity is small.

As pointed out by a reviewer, our techniques can also be viewed as extending the proof approach of another result from [AR16], in which an algorithm for learning \mathcal{AC}^0 circuits from examples was used to break a weak PRF computable by \mathcal{AC}^0 circuits. (Indeed, in [AR16] the weak PRF was conjectured to be secure, and thus they concluded that no such learning algorithm exists.) Our attacks on the simple “candidate PRFs” that we construct use *natural properties* for the corresponding circuit classes, and natural properties are generalizations of learning algorithms (i.e., an algorithm for learning from examples is in particular a natural property).

As pointed out by Applebaum,¹⁰ a transformation of Goldreich’s PRG to a *weak* PRF from [AR16] can be used to break the PRG when it is instantiated with a *random graph* and with a predicate with sufficiently low circuit complexity; this attack uses an algorithm for learning from random examples. Specifically, assume that Goldreich’s PRG is secure when instantiated with a random graph $[n] \times [m]$ of right-degree ℓ and a predicate $P : \{0, 1\}^\ell \rightarrow \{0, 1\}$. Using the argument that appears in [AR16, Sec. 1.2.1] it follows that the function $g_x : \{0, 1\}^{\ell \cdot \log(n)} \rightarrow \{0, 1\}$ that considers its input as a set S of ℓ vertices in $[n]$, and outputs $g_x(S) = P(x|_S)$, is a weak PRF against adversaries that make m (uniformly chosen) queries. The complexity of g_x is essentially determined by the complexity of the predicate P .¹¹ Thus, if the latter is sufficiently small such that there exists an algorithm for learning g_x from $m - 1$ random examples, then g_x cannot be a weak PRF for adversaries that make m queries (since such an adversary can use the learning algorithm to predict the m^{th} evaluation of the function at a random point, using the first $m - 1$ evaluations at random points). This contradicts the hypothesis that Goldreich’s PRG is secure when instantiated with P and a random graph $[n] \times [m]$.

Loosely speaking, the argument above implies that Goldreich’s PRG is not secure when the stretch is quasipolynomial (and the locality is polylogarithmic and sufficiently large), the graph is random, and the predicate is computable by an \mathcal{AC}^0 circuit of sufficiently small sub-exponential size; this relies on the learning algorithm of Linial, Mansour, and Nisan [LMN93].¹² However, the latter class of predicates is much weaker than the class of predicates to which our main unconditional result applies (i.e., than the class of $\mathcal{AC}^0[\oplus]$ circuits of sufficiently small sub-exponential size, from Theorem 1.3). For example, such predicates have “low” resilience $o(\ell)$, because the Fourier weight of depth- d \mathcal{AC}^0 circuits over ℓ bits of size 2^{ℓ^ϵ} is $.01$ -concentrated on sets of size at most $O(\ell^{\epsilon \cdot (d-1)}) = o(\ell)$ (see [LMN93; Tal17]); therefore, such predicates do not withstand the attacks from [AL18]. Finally, recall that it is currently an open problem to understand the learnability of $\mathcal{AC}^0[\oplus]$ circuits from random examples.

3 Preliminaries

We use $[n]$ to denote the set $\{1, \dots, n\}$. For functions $f, g : \{0, 1\}^t \rightarrow \{0, 1\}$ and $\gamma \in [0, 1]$, we say that f γ -approximates g if $\Pr_{x \sim \mathbf{u}_t}[f(x) = g(x)] \geq \gamma$, where \mathbf{u}_t denotes the uniform distribution over $\{0, 1\}^t$.

¹⁰Personal communication.

¹¹More specifically, the circuit g_x can be implemented by $2 \cdot \ell$ depth-two formulas of size $O(n)$ to compute the mapping $S \mapsto x|_S$, and then a circuit for P to compute $P(x|_S)$.

¹²Specifically, let $n \in \mathbb{N}$, and let $\ell = \log^k(n)$ for some $k \in \mathbb{N}$. Assume that the predicate $P : \{0, 1\}^\ell \rightarrow \{0, 1\}$ is computable by an \mathcal{AC}^0 circuit of depth $d \in \mathbb{N}$ and size $s = 2^{\ell^\epsilon}$, where $\epsilon \leq 1/(d+1)$. Then, g_x can be implemented by a circuit of size $s' = O(\ell \cdot n + s)$ and depth $d+1$. The algorithm of [LMN93] learns g_x with error $1/s'$ from $m = n^{O(\log(s'))^d} = o(n^\ell)$ random examples in time $\text{poly}(m)$, where the last bound on m is since $\epsilon \leq 1/(d+1)$. Therefore, Goldreich’s PRG is not secure when the locality is $\ell = \log^k(n)$, the stretch is $m = o(n^\ell)$, and the ℓ -bit predicate is an \mathcal{AC}^0 circuit of depth d and size 2^{ℓ^ϵ} .

3.1 Expander graphs and Goldreich's PRG

Throughout the paper, when we refer to expander graphs, we will mean highly unbalanced bipartite lossless expanders, as formally defined in Definition 1.1. In our constructions, we assume an ordering of the neighbors of each right-vertex of the graph, and allow parallel edges among vertices. We can thus define the notion of a neighbor function of a graph:

Definition 3.1 (*neighbor function of a bipartite graph*). Let G be an (n, m, ℓ) -graph. The neighbor function $\Gamma_G : \{0, 1\}^{\log(m)} \rightarrow \{0, 1\}^{\ell \cdot \log(n)}$ of G is the function that maps any $i \in [m]$ to its ℓ neighbors in $[n]$ in G .

We can now formally define Goldreich's pseudorandom generator. The definition refers to an arbitrary (n, m, ℓ) -graph, but the construction is intended to be instantiated over an (n, m, ℓ) -expander (e.g., as in Assumption 1.2).

Definition 3.2 (*Goldreich's PRG*). Let $n \in \mathbb{N}$, let $m = m(n)$ be a stretch parameter, and let $\ell = \ell(n)$ be a locality parameter. Let $P : \{0, 1\}^\ell \rightarrow \{0, 1\}$ be a predicate, and let G be an (n, m, ℓ) -graph. Then, Goldreich's PRG using expander G and predicate P is the function $\text{prg} : \{0, 1\}^n \rightarrow \{0, 1\}^m$ defined as follows: For every $x \in \{0, 1\}^n$ and $i \in [m]$, the i^{th} output bit of prg equals $P(x|_{\Gamma_G(i)})$, where $\Gamma_G(i)$ is the set of neighbors of $i \in [m]$ in the graph G .

Our notion of attack on the PRG is the standard one in which the distinguisher gets as input an m -bit string, which may either be a uniform output of the PRG or a uniform string, and runs in time proportional to m (ideally, polynomial in m).

Definition 3.3 (*distinguisher*). Let $G : \{0, 1\}^n \rightarrow \{0, 1\}^m$ a function. We say that an algorithm A distinguishes the output of G from a uniform string with gap $\epsilon > 0$ if

$$\left| \Pr_{s \in \{0, 1\}^n} [A(G(s)) = 1] - \Pr_{x \in \{0, 1\}^m} [A(x) = 1] \right| \geq \epsilon.$$

We consider the running time of A as a function of its input length m .

Lastly, we will use the well-known fact that a random graph is, with high probability, an expander; indeed, this fact holds also for the definition of expansion that we use. In more detail:

Theorem 3.4 (*a random graph is a good lossless expander*). Let $n \leq m \in \mathbb{N}$ and $\ell \in \mathbb{N}$ such that $c \cdot \frac{\log(m)}{\log(n)} \leq \ell \leq n^{1/c}$, where c is a sufficiently large constant. Let G be an (n, m, ℓ) -graph chosen at random by choosing the ℓ neighbors of each $i \in [m]$ uniformly in $[n]$ (allowing repetitions). Then, with probability at least $1 - 1/\text{poly}(n)$ it holds that G is an $(n^{0.99}, 0.99)$ -expander.¹³

The proof of Theorem 3.4 is a straightforward probabilistic argument, and in fact a more general theorem has been proved in [AR16, Lem. 2.5]. For completeness, we include a proof of Theorem 3.4 in Appendix A.

¹³The polynomial power in the success probability depends on c and can be made arbitrarily large.

3.2 Circuit classes and natural properties

Our circuit model throughout the paper is that of Boolean circuits over the De Morgan basis (i.e., with AND, OR, and NOT gates), and when explicitly stated we also allow for \oplus gates. We will frequently refer to the classes \mathcal{AC}^0 and $\mathcal{AC}^0[\oplus]$ of circuit families of constant depth with gates of unbounded fan-in (for the standard definitions see, e.g., [Juk12, Sections 12.3 and 12.5, respectively]). When we refer to \mathcal{NC}^0 we refer to Boolean function with multiple output bits where each output bit depends on a constant number of input bits.

Let us recall the definition of natural properties by Razborov and Rudich [RR97]. In the definition below, the class \mathcal{F} can be thought of as the class of functions computable by a certain class of circuits (such as $\mathcal{AC}^0[\oplus]$).

Definition 3.5 (*natural properties*). *Let $\mathcal{F} \subseteq \{\{0,1\}^* \rightarrow \{0,1\}\}$ be a class of Boolean functions. A deterministic polynomial-time algorithm is a natural property algorithm for \mathcal{F} if for every sufficiently large $n \in \mathbb{N}$, the algorithm satisfies the following:*

1. *The algorithm rejects every truth-table of $f \in \mathcal{F}$ over n input bits (when f is given to the algorithm as a string in $\{0,1\}^{2^n}$).*
2. *The algorithm accepts more than half of the strings in $\{0,1\}^{2^n}$.*

We will sometimes slightly abuse Definition 3.5 by referring to natural properties that run in super-polynomial time (e.g., in quasipolynomial time), or that accept less than half of the strings in 2^n (say, a constant fraction of the strings). We will clearly indicate the deviation from Definition 3.5 when we do so.

It is well-known that, relying on the results of Razborov [Raz87] and Smolensky [Smo87], there exist natural properties for $\mathcal{AC}^0[\oplus]$ of subexponential size (see, e.g., [RR97; CIKK16]). Since we will crucially use these natural properties in Section 4, let us formally state the parameters of the natural properties:

Theorem 3.6 (*natural properties for $\mathcal{AC}^0[\oplus]$ of subexponential size; [RR97; CIKK16]*). *For any constant $d \in \mathbb{N}$, let $\mathcal{F} \subseteq \{\{0,1\}^* \rightarrow \{0,1\}\}$ be the class of functions that satisfy the following: For every $\ell \in \mathbb{N}$, all functions in $\mathcal{F} \cap \{\{0,1\}^\ell \rightarrow \{0,1\}\}$ are computable by circuits of depth d with parity gates of size at most $2^{\eta \cdot \ell^{1/2d}}$, where $\eta > 0$ is a small universal constant. Then, there exists a natural property algorithm for \mathcal{F} .*

4 Unconditional results in the setting of quasipolynomial stretch

Our goal in this section is to prove Theorem 1.3, which shows that Goldreich’s PRG can be broken in the regime of quasipolynomial stretch with a specific expander for a large class of predicates. As a first step, in Section 4.1 we will prove Theorem 2.1, which asserts the existence of an expander whose “neighbor functions” can be computed by relatively small $\mathcal{AC}^0[\oplus]$ circuits. In Section 4.2, we prove Theorem 1.3. And finally, in Section 4.3 we prove that the class of predicates for which we break the generator

is quite rich, and in particular contains predicates with high rational degree and high resilience.

4.1 Expanders with $\mathcal{AC}^0[\oplus]$ neighbor functions

In this section we construct a (non-explicit) bipartite lossless expander whose “neighbor function” can be computed by a sub-exponential sized $\mathcal{AC}^0[\oplus]$ circuit. Throughout the section we consider an incidence-list representation of expanders; that is

Definition 4.1 (*incidence-list representation*). For $n, m, \ell \in \mathbb{N}$ such that $\ell \leq n$, let $m' = m \cdot \ell \cdot \lceil \log(n) \rceil$. We identify any string $G \in \{0, 1\}^{m'}$ with a bipartite graph $[n] \times [m]$ of right-degree ℓ such that for $v \in [m]$ and $i \in [\ell]$, the j^{th} neighbor of v is encoded by the bits in $G_{(v-1) \cdot \ell \cdot \lceil \log(n) \rceil + 1, \dots, G_{v \cdot \ell \cdot \lceil \log(n) \rceil}}$.

Let us start the proof by showing that for many natural settings of the parameters (e.g., $m \leq 2^{n^{0.1}}$ and $\ell \leq n^{0.1}$) we can test whether a string $G \in \{0, 1\}^{m'}$ is an $(n^{0.99}, .99)$ -expander using a CNF of size at most 2^n :

Claim 4.2 (*testing whether a graph is an expander*). Let $n, m, \ell \in \mathbb{N}$ such that $m \geq n$ and $\log(m) + 2\ell \cdot \lceil \log(n) \rceil \leq n^{0.1}$, and let $m' = m \cdot \ell \cdot \lceil \log(n) \rceil$. Then, there exists a CNF of size at most 2^n that gets as input a string $G \in \{0, 1\}^{m'}$ and accepts if and only if G is an incidence-list representation of an (n, m, ℓ) -expander.

Proof. The CNF is a conjunction of the following set of tests:

- For every $k = 1, \dots, n^{0.99}$,
- For every set $S \subseteq [m]$ of size at most k ,
- The number of neighbors of S is at least $0.99 \cdot \ell \cdot |S|$.

For a fixed $S \subseteq [m]$ of size $k \leq n^{0.99}$, the decision of whether or not $|\Gamma(S)| \geq 0.99 \cdot \ell \cdot |S|$ is a function of $k \cdot \ell \cdot \lceil \log(n) \rceil$ bits, and can therefore be implemented by a CNF of size $2^{k \cdot \ell \cdot \lceil \log(n) \rceil}$. There are at most $k \cdot \binom{m}{k}$ sets to consider, and therefore the final CNF is of size at most $k \cdot \binom{m}{k} \cdot 2^{k \cdot \ell \cdot \lceil \log(n) \rceil} < 2^{\log(k) + k \cdot \log(m) + k \cdot \ell \cdot \lceil \log(n) \rceil} \leq 2^n$. ■

Our next step is to use the construction of Carmosino *et al.* [CIKK16] to show that Nisan’s generator can be made “strongly explicit”:

Proposition 4.3 (*a strongly explicit NW-generator*). There exists a constant d such that the following holds. Let $n, m' \in \mathbb{N}$ such that $n \leq m' \leq 2^n$, let $t = \text{poly}(n)$ for a sufficiently large polynomial, and let $\epsilon = 2^{-n}$. Then, there exists a pseudorandom generator $NW : \{0, 1\}^t \rightarrow \{0, 1\}^{m'}$ with error ϵ for CNFs of size 2^n that satisfies the following. There exists a polynomial-sized circuit $E : \{0, 1\}^t \times \{0, 1\}^{\lceil \log(m') \rceil} \rightarrow \{0, 1\}$ of depth d with parity gates such that for every $s \in \{0, 1\}^t$ and $i \in [m']$ it holds that $E(s, i)$ outputs the i^{th} bit of $NW(s)$.

Proof. Our generator $NW : \{0,1\}^t \rightarrow \{0,1\}^{m'}$ will be the Nisan-Wigderson generator. Specifically, for a sufficiently large $k \geq 6$ and $r = n^k$, let $f : \{0,1\}^r \rightarrow \{0,1\}$ be the parity function such that f is (ϵ/m') -average-case hard for circuits of depth five and of size $2^{O(n)}$.¹⁴ We will use a combinatorial design $S_1, \dots, S_{m'} \subseteq [t]$ with parameters as in the following lemma, which is due to Carosino *et al.* [CIKK16].

Definition 4.4 (combinatorial design) *A collection of sets $S_1, \dots, S_{m'} \subseteq [t]$ is an (r, k) -design if for every $i \in [m']$ it holds that $|S_i| = r$, and for every distinct $i \neq j$ in $[m']$ it holds that $|S_i \cap S_j| \leq k$.*

Lemma 4.5 (strongly explicit NW-designs in $\mathcal{AC}^0[p]$; see [CIKK16, Thm. 3.3]) *Let p be any prime. There exists a constant $d_{MX} \geq 1$ such that for any $r \in \mathbb{N}$ and $m' < 2^r$ there exists an $(r, \lceil \log(m') \rceil)$ -design $S_1, \dots, S_{m'} \subseteq [t]$, where $t = O(r^2)$,¹⁵ such that the function $MX_{NW} : \{0,1\}^t \times \{0,1\}^{\lceil \log(m') \rceil} \rightarrow \{0,1\}^r$ defined by $MX_{NW}(s, i) = s|_{S_i}$ is computable by an $\mathcal{AC}^0[p]$ circuit of size $O(\log(m') \cdot r^3 \cdot \log(r))$ and depth d_{MX} .*

By a standard argument that follows Nisan and Wigderson [NW94], the generator G instantiated with the function f and the design in Lemma 4.5 is ϵ -pseudorandom for CNFs of size $2^{O(n)}$. The circuit $E : \{0,1\}^t \times \{0,1\}^{\lceil \log(m') \rceil} \rightarrow \{0,1\}$ acts as follows. Given inputs (s, i) , the circuit E first feeds (s, i) to the circuit from Lemma 4.5 to compute $s|_{S_i}$, and then computes the parity of $s|_{S_i}$ (using a single parity gate). The depth of E is $d = d_{MX} + 1$, and its size is $\text{poly}(t, \log(m')) = \text{poly}(n)$. ■

Finally, we prove Theorem 2.1 by combining Claim 4.2 and Proposition 4.3, and “hard-wiring” a good seed s into the circuit E from Proposition 4.3. That is:

Theorem 4.6 (strongly explicit lossless expander in $\mathcal{AC}^0[p]$). *There exist constants $d_G, c \in \mathbb{N}$ such that the following holds. Let $n \leq m \in \mathbb{N}$ such that $m \leq 2^{n^{O(1)}}$, and let $\ell \in \mathbb{N}$ such that $c \cdot \frac{\log(m)}{\log(n)} \leq \ell \leq n^{1/c}$. Then, there exists an (n, m, ℓ) -expander G and an $\mathcal{AC}^0[\oplus]$ circuit $C_G : \{0,1\}^{\log(m)} \rightarrow \{0,1\}^{\ell \cdot \log(n)}$ of depth d_G and size $\text{poly}(n)$, such that for every $i \in [m]$ it holds that $C_G(i)$ outputs the list of ℓ neighbors of i in G .*

Proof. Let $m' = m \cdot \ell \cdot \lceil \log(n) \rceil$. By Theorem 3.4, a random string in $\{0,1\}^{m'}$ is an $(n^{0.99}, 0.99)$ -expander, with high probability. By Claim 4.2, such expanders can be recognized by CNFs of size 2^n . Thus, considering the pseudorandom generator NW from Proposition 4.3 for such CNFs, with high probability over choice of seed $s \in \{0,1\}^{\text{poly}(n)}$ for NW it holds that $NW(s)$ is an $(n^{0.99}, 0.99)$ -expander.

Let us now fix such a good seed $s \in \{0,1\}^{\text{poly}(n)}$, and a corresponding expander $G = NW(s)$. When we “hard-wire” the seed s into the circuit E from Proposition 4.3, we obtain a circuit $E_s : \{0,1\}^{\lceil \log(m') \rceil} \rightarrow \{0,1\}$ that on input $j \in [m']$ outputs the j^{th} bit in the representation of G as a bit-string. Thus, our final circuit C_G gets as input

¹⁴For $S = 2^{O(n)}$ and a sufficiently large integer $k \geq 6$, the correlation of a size- S circuit of depth five with the parity of $v = n^k$ variables is at most $2^{-\Omega(v/(\log(S))^5)} \leq \epsilon/m'$ (see [Hås14]).

¹⁵The O -notation hides a universal constant.

$i \in \{0,1\}^{\lceil \log(m) \rceil}$, and uses $\ell \cdot \lceil \log(n) \rceil$ copies of E_s to output the $\ell \cdot \lceil \log(n) \rceil$ bits in the representation of G that correspond to the neighbors of $i \in [m]$ (i.e., $C_G(i) = E_s((i-1) \cdot \lceil \log(n) \rceil), \dots, E_s(i \cdot \ell \cdot \lceil \log(n) \rceil - 1)$). The size of C_G is $\ell \cdot \lceil \log(n) \rceil$ times the size of E_s , and is thus upper-bounded by $\text{poly}(n)$; the depth d_G of C_G is just the depth of E_s , which is the universal constant d from Proposition 4.3. ■

4.2 Proof of Theorem 1.3

Relying on Theorem 4.6, we now prove Theorem 1.3. As explained in Section 2.2, the main step is to show that, when Goldreich's PRG is instantiated with the expander from Theorem 4.6 and with any predicate that can be computed by an $\mathcal{AC}^0[\oplus]$ circuit of sufficiently small sub-exponential size, the output of the generator (on any seed) is the truth-table of an $\mathcal{AC}^0[\oplus]$ circuit of small sub-exponential size.

Theorem 4.7 (Theorem 1.3, restated). *For any $d_P \in \mathbb{N}$ and sufficiently small $\epsilon = \epsilon(d_P) > 0$ and sufficiently large $k = k(d_P) > 1$, there exists a deterministic polynomial-time algorithm A that satisfies the following. For any sufficiently large $n \in \mathbb{N}$, let $m = 2^{(\log(n))^k}$, and let $c \cdot (\log(n)^{k-1}) \leq \ell \leq n^{1/c}$, where c is a sufficiently large constant. Then, there exists an (n, m, ℓ) -expander such that the following holds: For any predicate $P : \{0,1\}^\ell \rightarrow \{0,1\}$ that can be computed by an $\mathcal{AC}^0[\oplus]$ circuit of depth d_P and size at most $2^{(\log(m))^\epsilon}$, when Goldreich's PRG is instantiated with the expander G and the predicate P , the algorithm A distinguishes (with high probability) between a uniform string and the output of the generator.*

Proof. Let $G = ([n], [m], E)$ be the expander graph from Theorem 4.6, with right-degree ℓ . Let $\text{prg} : \{0,1\}^n \rightarrow \{0,1\}^m$ be Goldreich's PRG, instantiated with the graph G and with the predicate P . For any fixed $x \in \{0,1\}^n$, we show that $\text{prg}(x)$ is the truth-table of an $\mathcal{AC}^0[\oplus]$ circuit $\{0,1\}^{\log(m)} \rightarrow \{0,1\}$ of depth d and size $2^{o(\log(m)^{1/2d})}$. Assuming such a circuit indeed exists for any fixed x , the natural property algorithm from Theorem 3.6 rejects all m -bit strings in the image of prg , but accepts a random m -bit string with probability at least $1/2$.

Thus, let us fix x and construct a circuit $g_x : \{0,1\}^{\log(m)} \rightarrow \{0,1\}$ whose truth-table is $\text{prg}(x)$. Given input $i \in [m]$, the circuit g_x :

- Uses the circuit $C_G : \{0,1\}^{\log(m)} \rightarrow \{0,1\}^{\ell \cdot \log(n)}$ from Theorem 4.6 to compute $\Gamma_G(i) \in \{0,1\}^{\ell \cdot \log(n)}$.
- Let $\Phi_x : \{0,1\}^{\log(n)} \rightarrow \{0,1\}$ be a depth-two formula of size n such that for any $i \in [n]$ it holds that $\Phi_x(i)$ is the i^{th} bit of x . The circuit g_x uses ℓ copies of Φ_x to map $\Gamma_G(i) \in \{0,1\}^{\ell \cdot \log(n)}$ to $x|_{\Gamma_G(i)} \in \{0,1\}^\ell$.
- Finally, the circuit maps $x|_{\Gamma_G(i)} \in \{0,1\}^\ell$ to an output bit $P(x|_{\Gamma_G(i)})$, using the $\mathcal{AC}^0[\oplus]$ circuit of depth d_P for P .

The total depth of the circuit for g_x is $d = d_G + d_P + 2$, and its size is at most

$$\text{poly}(n) + \ell \cdot n + 2^{(\log(m))^\epsilon} = 2^{o((\log(m))^{1/2d})},$$

where we relied on the hypothesis that k is sufficiently large (to deduce that $n^{O(1)} = 2^{o(\log(n)^{k/2d})}$) and on the hypothesis that ϵ is sufficiently small. ■

4.3 Hard predicates and the class of subexponential sized $\mathcal{AC}^0[\oplus]$ circuits

Recall that the known attacks on Goldreich's PRG relied either on low resilience or on low rational degree of the predicate (we will define these notions in a moment; see [AL18] for further details). Our goal in this section is to prove that the class of $\mathcal{AC}^0[\oplus]$ circuits of small sub-exponential size (and even of polynomial size) contains predicates with high rational degree and high resilience. Thus, the predicates that we present avoid known attacks but are still susceptible to our attacks.

Definition 4.8 (*resilience*). We say that P has resilience k if $\widehat{P}(\gamma) = 0$ for every $\gamma \in \{0,1\}^\ell$ of Hamming weight at most k . In other words, P is uncorrelated with parities of size at most k .

Definition 4.9 (*rational degree*). For a predicate $P: \{0,1\}^\ell \rightarrow \{0,1\}$, we let the rational degree of P , denoted by $\text{rdeg}_{\mathbb{F}_2}(P)$, be the minimum $e \in \mathbb{N}$ such that there exist predicates $Q, R: \{0,1\}^\ell \rightarrow \{0,1\}$, not both identically zero, such that the degree of Q and of R as \mathbb{F}_2 -polynomials is at most e , and $\text{deg}_{\mathbb{F}_2}(Q), \text{deg}_{\mathbb{F}_2}(R) \leq e$ and

$$P(z)Q(z) = R(z) \text{ for every } z \in \{0,1\}^\ell.$$

A candidate predicate that avoids the previously known attacks, suggested in [AL18], is $\text{XOR-MAJ}_{a,b}(z_1, \dots, z_{a+b}) = (z_1 \oplus \dots \oplus z_a) \oplus \text{MAJ}(z_{a+1}, \dots, z_{a+b})$, for suitable choices of a and b . We consider the modification XOR-APPROX-MAJ in which the majority function is replaced by an approximate majority. Towards defining the predicate, let us first recall the standard definition of an *approximate majority* predicate, and the fact that there exist \mathcal{AC}^0 circuits computing this predicate. For a string $x \in \{0,1\}^*$, let $|x|_1$ denote its Hamming weight. Then:

Definition 4.10 (*approximate majority*) We say that a function $h: \{0,1\}^b \rightarrow \{0,1\}$ is an ϵ -approximate majority if $h(x) = 0$ for every input x such that $|x|_1 \leq (1/2 - \epsilon)b$, and $h(x) = 1$ for every input x such that $|x|_1 \geq (1/2 + \epsilon)b$.

Theorem 4.11 (*approximate majority in \mathcal{AC}^0* ([Ajt90; Vio09])) For every $\epsilon > 0$, there exists a uniform family $\{D_b\}_{b \geq 1}$ of polynomial size depth-3 circuits that compute an ϵ -approximate majority over b input bits.

We define the XOR-APPROX-MAJ predicate using the particular approximate majority function that can be computed by \mathcal{AC}^0 circuits as in Theorem 4.11.

Definition 4.12 (*the XOR-APPROX-MAJ predicate*) Let $a, b \geq 1$ and $\epsilon > 0$. Let $h: \{0,1\}^b \rightarrow \{0,1\}$ be the ϵ -approximate majority function whose existence is asserted in Theorem 4.11, and let $\text{XOR-}\epsilon\text{-APPROX-MAJ}_{a,b}: \{0,1\}^{a+b} \rightarrow \{0,1\}$ be the predicate given by $(z_1 \oplus \dots \oplus z_a) \oplus h(z_{a+1}, \dots, z_{a+b})$.

The result presented next asserts that the XOR-APPROX-MAJ predicate behaves similarly to XOR-MAJ with respect to resilience and rational degree. (In particular, it also avoids the attacks on Goldreich's PRG from [AL18].)

Proposition 4.13 (*properties of the XOR-APPROX-MAJ predicate*). *Let $a, b \geq 1$, $\varepsilon > 0$, and let $\text{XOR-}\varepsilon\text{-APPROX-MAJ}_{a,b}: \{0,1\}^{a+b} \rightarrow \{0,1\}$ be the predicate from Definition 4.12. Then P has resilience $\geq a - 1$ and rational degree $> (1/2 - \varepsilon)b$.*

Proof. For the resilience lower bound, let $\gamma \in \{0,1\}^{a+b}$, $|\gamma|_1 \leq a - 1$, and $h: \{0,1\}^b \rightarrow \{0,1\}$ be the corresponding ε -approximate majority function. Clearly, there exists $i \in \{1, \dots, a\}$ such that $\gamma_i = 0$. Consequently,

$$\begin{aligned} \mathbb{E}_{z \in \{0,1\}^{a+b}} [(-1)^{P(z) + \langle z, \gamma \rangle}] &= \mathbb{E}_z [(-1)^{(\oplus_{j=1}^a z_j) \oplus h(z_{a+1}, \dots, z_{a+b}) \oplus \langle z, \gamma \rangle}] \\ \text{(using independence)} &= E_z [(-1)^{z_i}] \cdot \mathbb{E}_z [(-1)^{(\oplus_{j \in [a] \setminus \{i\}} z_j) \oplus h(z_{a+1}, \dots, z_{a+b}) \oplus \langle z, \gamma \rangle}] \\ &= 0, \end{aligned}$$

where we have used the fact that $\gamma_i = 0$ to decompose the expectation into a product of expectations.

Turning to the rational degree lower bound, first we argue the claim for the ε -approximate majority function $h: \{0,1\}^b \rightarrow \{0,1\}$. Let $d = \lfloor (1/2 - \varepsilon)b \rfloor$, and suppose that $\text{rdeg}_{\mathbb{F}_2}(h) \leq d$. We will rely on the following lemma:

Lemma 4.14 (*see, e.g., [Car10; AL18]*). *A predicate $h: \{0,1\}^b \rightarrow \{0,1\}$ has rational degree $e \geq 0$ if and only if there exists a non-zero predicate $\tilde{h}: \{0,1\}^b \rightarrow \{0,1\}$ with $\text{deg}_{\mathbb{F}_2}(\tilde{h}) \leq e$ such that one of the following conditions hold:*

- (a) *For every $z \in \{0,1\}^b$, if $h(z) = 0$ then $\tilde{h}(z) = 0$; or*
- (b) *For every $z \in \{0,1\}^b$, if $h(z) = 1$ then $\tilde{h}(z) = 0$.*

Assume that Case (a) from Lemma 4.14 holds (the proof of the other case is very similar), and let \tilde{h} be the non-zero predicate from the lemma. Let $B(0^b, d) \subseteq \{0,1\}^b$ be the set of strings of Hamming distance at most d from 0^b , and note that by the definition of h it holds that $B(0^b, d) \subseteq h^{-1}(0)$. It follows that \tilde{h} vanishes on $B(0^b, d)$ (i.e., for every $z \in B(0^b, d)$ it holds that $\tilde{h}(z) = 0$). However, this contradicts the following lemma:

Lemma 4.15 (*see [KS12]*). *Let $\tilde{h}(x_1, \dots, x_b) \in \mathbb{F}_2[x_1, \dots, x_b]$ be a non-zero polynomial of degree at most d , and let $S_{\tilde{h}} = \{x \in \{0,1\}^b \mid \tilde{h}(x) \neq 0\}$. Then, for every $y \in \{0,1\}^b$ it holds that $S_{\tilde{h}} \cap B(y, d) \neq \emptyset$.*

Finally, note that this rational degree lower bound also holds for the predicate P , since it is easy to see using Lemma 4.14 that the rational degree of a function cannot increase by taking a restriction. ■

5 Conditional results in the setting of polynomial stretch

In Section 5.1 we present our results that are conditioned on the existence of expanders with a neighbor function that can be computed by a layer of parity gates, and in Section 5.2 we present our results that are conditioned on the existence of expanders with a neighbor function that can be computed in \mathcal{NC}^0 .

5.1 Expanders with affine neighbor functions

In this section we present results that are conditioned on the existence of expanders in which the neighbor functions are affine functions (i.e., can be computed by a layer of parity gates). In Section 5.1.1 we formally present the assumption that suitable expanders exist. In Section 5.1.2 we prove the existence of natural properties that are useful against DNF-XOR circuits and related functions. In Section 5.1.3 we show that if the aforementioned expanders exist, then an improvement in the parameters of the foregoing natural properties would allow to break the generator with these expanders and *any* predicate, for the setting of a large polynomial stretch.

5.1.1 The affine expander assumption

Towards presenting the assumption regarding the existence of expanders with affine neighbor functions, let us first formally define affine functions:

Definition 5.1 (*multi-output affine functions*). We say that a function $f : \{0, 1\}^r \rightarrow \{0, 1\}^{r'}$ is affine if for each output bit y_i of $f(x)$ there exists a set $S_i \subseteq [r]$ and $b_i \in \{0, 1\}$ such that $y_i = b_i + \sum_{j \in S_i} x_j \pmod{2}$.

To motivate our assumption, let us recall *known explicit constructions* of unbalanced lossless expanders in which the neighbor function is affine. The first construction is that of Guruswami, Umans, and Vadhan [GUV09]. When their construction is instantiated over a field of characteristic two, with a specific a choice of parameters, it yields an expander that is affine (see, e.g., [Che05, Cor. 2.23]). In particular, it yields an affine expander with the following parameters:

Theorem 5.2 ([GUV09, Thm. 1.3]). Let $n \in \mathbb{N}$, let $m = n^2$, and let $\ell = \text{poly log}(n)$. Then, there exists an (n, m, ℓ) -expander such that for each $i \in [\ell]$, the neighbor function $\Gamma_i : [m] \rightarrow [n]$ is affine.

A different construction of an expander with affine neighbor functions arises from the work of Ta-Shma, Umans, and Zuckerman [TUZ07, Thm 1.7]. Instantiating their construction with Trevisan's extractor [Tre01], it yields an (n, m, ℓ) -graph for an arbitrarily large polynomial $m = \text{poly}(n)$ and again with $\ell = \text{poly log}(n)$, that is a $(n^{1/(\log n)^{0.1}}, 0.99)$ -expander (rather than an $(n^{0.99}, 0.99)$ -expander). Interestingly, the two constructions are of very different nature (i.e., the construction from [TUZ07] is combinatorial whereas the construction from [GUV09] is algebraic).

The degree of the expander in both constructions above is polylogarithmic, whereas the degree of a random construction is constant $\ell = O(k)$ (see Theorem 3.4). Indeed, it is an open problem to improve the degree in these constructions to match the one of a random construction. The following assumption asserts that expanders as above exist, but with small right-degree $\ell = O(k)$:

Assumption 5.3 (*affine expander assumption*). *There exists a constant $\beta > 3$ such that for every constant $k \in \mathbb{N}$ and sufficiently large $n \in \mathbb{N}$ there exists an (n, m, ℓ) -expander, where $m = n^k$ and $\ell = \beta \cdot k$, that satisfies the following: The function $\Gamma_G : \{0, 1\}^{\log(m)} \rightarrow \{0, 1\}^{\ell \cdot \log(n)}$ that gets as input $i \in [m]$ and outputs the ℓ neighbors of i in G is affine.*

Under Assumption 5.3, there exists an expander G such that when Goldreich's PRG is instantiated with G and with any predicate P , the output of the generator on any seed is the truth-table of a depth-four circuit with constant top fan-in and a bottom layer of parity gates. In the following claim, we consider the complexity of the ℓ -bit predicate P as a CNF of size $2^{\delta \cdot \ell}$, where $\delta \in (0, 1]$ may be small but may also equal 1 (in which case there is no restriction on the complexity P).

Claim 5.4. *Suppose that Assumption 5.3 holds. Then, for every $k \in \mathbb{N}$ and sufficiently large $n \in \mathbb{N}$ there exists an (n, m, ℓ) -expander G , where $m = n^k$ and $\ell = \beta \cdot k$, that satisfies the following. For every $\delta \in (0, 1]$ and every predicate $P : \{0, 1\}^\ell \rightarrow \{0, 1\}$ that can be computed by a CNF of size $2^{\delta \cdot \ell}$, when Goldreich's PRG is instantiated with G and P , the output of the generator on any fixed $x \in \{0, 1\}^n$ is the truth-table of a function $g_x : \{0, 1\}^{\log(m)} \rightarrow \{0, 1\}$ such that:*

1. *The function g_x can be computed by an AND-DNF-XOR circuit of top fan-in $2^{\delta \cdot \beta \cdot k}$ and size at most $O\left(2^{(1/k) \cdot \log(m)}\right)$ containing at most $2 \cdot \beta \cdot \log m$ parity gates.*
2. *The function g_x can be $(1/2 + 1/2^{\delta \cdot \beta \cdot k})$ -approximated by a DNF-XOR circuit of size at most $O\left(2^{(1/k) \cdot \log(m)}\right)$ containing at most $2 \cdot \beta \cdot \log m$ parity gates.*

Proof. For $k \in \mathbb{N}$ and sufficiently large $n \in \mathbb{N}$, let $G = ([n], [m = n^k], E)$ be the expander from Assumption 5.3, let $P : \{0, 1\}^\ell \rightarrow \{0, 1\}$ be any predicate that can be computed by a CNF of size $2^{\delta \cdot \ell}$, and let $\text{prg} : \{0, 1\}^n \rightarrow \{0, 1\}^m$ be Goldreich's PRG, instantiated with the graph G and with the predicate P .

For any fixed $x \in \{0, 1\}^n$, we construct a circuit $g_x : \{0, 1\}^{\log(m)} \rightarrow \{0, 1\}$ whose truth-table is $\text{prg}(x)$. Given input $i \in [m]$, the circuit g_x :

- Uses a single layer of at most $2 \cdot \ell \cdot \log n$ parity gates to compute the string $\Gamma_G(i) \in \{0, 1\}^{\ell \cdot \log(n)}$ and its negation.
- Let $\Phi_x : \{0, 1\}^{\log(n)} \rightarrow \{0, 1\}$ be a DNF of size at most n such that for any $i \in [n]$ it holds that $\Phi_x(i)$ is the i^{th} bit of x ; we view this DNF as a De Morgan formula over input literals $z_1, \dots, z_{\log(n)}, \neg z_1, \dots, \neg z_{\log(n)}$. The circuit g_x uses ℓ copies of Φ_x to map $\Gamma_G(i) \in \{0, 1\}^{\ell \cdot \log(n)}$ to $x|_{\Gamma_G(i)} \in \{0, 1\}^\ell$, and similarly uses ℓ copies of a DNF of size at most n for $\neg \Phi_x$ to compute the negations of $x|_{\Gamma_G(i)} \in \{0, 1\}^\ell$.

- Finally, the circuit g_x computes the value of the predicate P on the input string $x \upharpoonright_{\Gamma_G(i)}$ by using a CNF of size $2^{\delta \cdot \ell}$ for P over the corresponding $\ell = \beta \cdot k$ input variables.

After collapsing two layers (of the top CNF with the DNFs below it), we obtain an exact AND-OR-AND-XOR circuit of size $O(2^{(1/k) \cdot \log(m)})$ that computes $g_x: \{0, 1\}^{\log(m)} \rightarrow \{0, 1\}$, where negation gates appear next to input variables only. Moreover, the number of parity gates is at most $2 \cdot \ell \cdot \log n = 2 \cdot \ell \cdot \log(m)/k = 2 \cdot \beta \cdot \log(m)$, and the top fan-in of the circuit is $2^{\delta \cdot \ell} = 2^{\delta \cdot \beta \cdot k}$.

The claimed DNF-XOR circuit that $(1/2 + 1/2^{O(k)})$ -approximates g_x follows from the Discriminator Lemma of [HMP+93] using that the top gate of g_x computes a symmetric gate over at most $2^\ell = 2^{\beta \cdot k}$ input wires. ■

5.1.2 Natural properties against DNF-XOR circuits

Our goal in this section is to show natural properties that are useful against DNF-XOR circuits of exponential size, against conjunctions of a constant number of DNF-XOR circuits of exponential size, and against functions that can be approximated by exponential-sized parity decision trees (which are related to DNF-XOR circuits, but weaker).

DNF-XOR circuits of exponential size. The first natural property that we show is useful against DNF-XOR circuits over ℓ_m bits of exponential size $2^{(1/2 - o(1)) \cdot \ell_m}$:

Proposition 5.5 (*natural property against exponential size DNF-XOR circuits*). *There exists a natural property against the class of functions $f: \{0, 1\}^{\ell_m} \rightarrow \{0, 1\}$ that can be computed by DNF-XOR circuits of size $2^{(1/2 - o(1)) \cdot \ell_m}$.*

Proof. We rely on the following result from [Aka+14], which shows that a DNF-XOR circuit of bounded size has a Fourier coefficient of relatively large magnitude.

Proposition 5.6 ([Aka+14]) *Let $g: \{0, 1\}^{\ell_m} \rightarrow \{0, 1\}$ be computed by a DNF-XOR circuit of top fan-in s . Then there exists $S \subseteq [\ell_m]$ such that $|\widehat{g}(S)| \geq 1/(2s + 1)$.*

In particular, if $s = o\left(2^{\ell/2 - \frac{1}{2} \cdot \log(\ell_m)}\right)$, then some Fourier coefficient of g is of magnitude $\omega\left(\sqrt{\ell_m} \cdot 2^{-\ell_m/2}\right)$. On the other hand, if $h: \{0, 1\}^{\ell_m} \rightarrow \{0, 1\}$ is a random Boolean function, it follows from a standard concentration bound that for any fixed $S \subseteq [\ell_m]$, with probability at least $1 - 2 \cdot \exp(-10 \cdot \ell_m)$ it holds that $|\widehat{h}(S)| \leq 10 \cdot \sqrt{\ell_m} \cdot 2^{-\ell_m/2}$. By taking a union bound over all such $S \subseteq [\ell_m]$, we get:

Proposition 5.7 (see, e.g. [OS08]) *With probability $1 - 2^{-\ell_m}$ over choice of a random Boolean function $h: \{0, 1\}^{\ell_m} \rightarrow \{0, 1\}$, for every $S \subseteq [\ell_m]$ it holds that $|\widehat{h}(S)| \leq O(\sqrt{\ell_m} \cdot 2^{-\ell_m/2})$.*

The natural property claimed in the statement of the lemma can therefore be defined as follows: Given the truth table of a Boolean function f , exactly compute each Fourier coefficient of f , and accept the input function if and only if every coefficient has magnitude bounded by $O(\sqrt{\ell_m} \cdot 2^{-\ell_m/2})$. This computation can be done in time $2^{O(\ell_m)}$, which is polynomial in the size of the truth table. ■

Extending Proposition 5.5, we can get a property that is useful against larger circuits of size $2^{(1-o(1)) \cdot \ell_m}$, at the cost of having a slightly super-polynomial running time. This natural property is based on a result of Cohen and Shinkar [CS16]:

Proposition 5.8 (natural property against strongly exponential size DNF-XOR circuits). *There exists a natural property computed in time $T(2^{\ell_m}) = 2^{O(\ell_m \cdot \log(\ell_m))}$ against the class of functions $f : \{0,1\}^{\ell_m} \rightarrow \{0,1\}$ that can be computed by DNF-XOR circuits of strongly exponential size $2^{(1-o(1)) \cdot \ell_m}$.*

Proof. An *affine disperser* for dimension k is a function $h : \{0,1\}^{\ell_m} \rightarrow \{0,1\}$ with the following property: For every affine subspace $U \subseteq \{0,1\}^{\ell_m}$ of dimension k , the restriction of h to U is non-constant. A standard argument shows that a random function is, with probability more than $1/2$, an affine disperser for dimension $k = \log(\ell_m) + \log \log(\ell_m) + C$, where C is a large enough constant.

On the other hand, Cohen and Shinkar [CS16] established the following result. For a function g , let $\text{DNF-XOR}(g)$ be the number of gates in the smallest DNF-XOR circuit for g . Then, if g is an affine disperser for dimension k , $\max(\text{DNF-XOR}(g), \text{DNF-XOR}(1-g)) = \Omega(2^{\ell_m - k})$. An immediate consequence is that for $k^* = \log(\ell_m) + \log \log(\ell_m) + C$, by checking if an input truth-table or its negation is a k^* affine disperser it is possible to distinguish a random function from a function of DNF-XOR circuit complexity at most $2^{\ell_m} / (\ell_m)^2$. ■

Conjunctions of DNF-XOR circuits of exponential size. We now turn to show a natural property that is useful against conjunctions of $c = O(1)$ DNF-XOR circuits over ℓ_m input bits of size $2^{(\epsilon/2) \cdot \ell_m}$, for any $\epsilon < 1/c$. Specifically:

Proposition 5.9 (a natural property against conjunctions of c DNF-XOR's of size $2^{(1/2c - \Omega(1)) \cdot \ell_m}$). *For any $c \in \mathbb{N}$ and $\epsilon < 1/c$, there exists a natural property against the class of functions $f : \{0,1\}^{\ell_m} \rightarrow \{0,1\}$ that can be computed by a conjunction of c DNF-XOR circuits of size $2^{(\epsilon/2) \cdot \ell_m}$.*

Proof. We prove Proposition 5.9 using Proposition 5.5. To do so, note that any conjunction of c DNF-XOR circuits of size s can be computed by a single DNF-XOR circuit of size s^c (using the distributivity of the top AND gate with the OR gates below it). In particular, if $s = 2^{(\epsilon/2) \cdot \ell_m}$, where $\epsilon < 1/c$, then the conjunction can be computed by a single DNF-XOR circuit of size $s^c = 2^{(\epsilon \cdot c/2) \cdot \ell_m}$. Since $\epsilon \cdot c/2 < 1/2$, the natural property from Proposition 5.5 works against such a function. ■

The natural property from Proposition 5.9 can be extended to work against conjunctions of c DNF-XOR circuits of size $2^{\epsilon \cdot \ell_m}$ (rather than $2^{(\epsilon/2) \cdot \ell_m}$), for any $\epsilon < 1/c$, at the cost of a slightly super-polynomial running time. Specifically:

Proposition 5.10 (an almost-natural property against conjunctions of c DNF-XOR's of size $2^{(1/c - \Omega(1)) \cdot \ell_m}$). *For any $c \geq 2$ and $\epsilon < 1/c$, there exists a natural property that runs in time $T(2^{\ell_m}) = 2^{O(\ell_m \cdot \log(\ell_m))}$ for the class of functions $\{0,1\}^{\ell_m} \rightarrow \{0,1\}$ that can be computed by a conjunction of c DNF-XOR circuits of size at most $2^{\epsilon \cdot \ell_m}$.*

Proof. To present this property we first need the following structural lemma for conjunctions of DNF-XOR circuits. (The proof of the lemma is reminiscent of the proof of [CS16, Second Item of Thm. 1.1].)

Lemma 5.11 (*a structural property of conjunctions of DNF-XOR circuits*). Let $g_x : \{0, 1\}^{\ell_m} \rightarrow \{0, 1\}$ be computed by a conjunction of $c \geq 1$ DNF-XOR circuits, where each DNF-XOR circuit is of size at most $s(\ell_m) = 2^{\epsilon \cdot \ell_m}$.¹⁶ Then, for every $k \in \mathbb{N}$, at least one of the following holds:

1. The function g_x is constant on a subspace of dimension $c \cdot k$.
2. The acceptance probability of g_x is at most $2^{\ell_m \cdot (\epsilon - 1/c) + k}$.

Proof. Let $g_x(z) = \bigwedge_{i \in [c]} h_i(z)$, where each h_i is a DNF-XOR circuit of size at most $s(\ell_m)$. For each $i \in [c]$, we view h_i as the union of at most $s(\ell_m)$ affine subspaces, and denote by $\dim(h_i)$ the maximal dimension of an affine subspace in h_i ; that is, if $h_i(n) = \bigcup_{j \in [s(n)]} H_j$, where each $H_j \subseteq \{0, 1\}^n$ is an affine subspace, then $\dim(h_i) = \max_{j \in [s(n)]} \dim(H_j)$. We consider two separate cases:

Case 1: $\min_{i \in [c]} \{\dim(h_i)\} > \ell_m - \ell_m/c + k$. In this case it holds that g_x is constant on a subspace of dimension $c \cdot k$. Specifically, for each $i \in [c]$ let $H^{(i)}$ be an affine subspace in h_i such that $\dim(H^{(i)}) > \ell_m - \ell_m/c + k$, and let $H = \bigcap_{i \in [c]} H^{(i)}$. Then, the co-dimension of H is less than $c \cdot (\ell_m/c - k) = \ell_m - c \cdot k$, and for every $z \in H$ it holds that $g_x(z) = 1$.

Case 2: $\min_{i \in [c]} \{\dim(h_i)\} \leq \ell_m - \ell_m/c + k$. In this case it holds that g_x has acceptance probability at most. To see this, let $i \in [c]$ be such that $\dim(h_i) \leq \ell_m - \ell_m/c + k$, and note that the acceptance probability of g_x is upper-bounded by the acceptance probability of h_i . However, since h_i is the union of at most $s(\ell_m)$ subspaces of dimension $\ell_m - \ell_m/c + k$, the number of inputs accepted by h_i is at most

$$s(\ell_m) \cdot 2^{\ell_m - \ell_m/c + k} = 2^{\ell_m \cdot (1 - 1/c + \epsilon) + k} . \quad \square$$

Now, let $k = \log(\ell_m)$. When given the truth-table of a function $f : \{0, 1\}^n \rightarrow \{0, 1\}$, the algorithm iterates over all affine subspaces in $\{0, 1\}^{\ell_m}$ of dimension $c \cdot k$, and rejects if f is the constant one function on any such subspace. Also, the algorithm rejects if the acceptance probability of f is less than $1/3$.

To see that the algorithm rejects any conjunction of c DNF-XOR circuits of size at most $2^{\epsilon \cdot \ell_m}$, note that by Lemma 5.11, any such function is either constant on a subspace of dimension $c \cdot k$, or has acceptance probability at most $2^{\ell_m \cdot (\epsilon - 1/c) + k} = 2^{-\Omega(\ell_m)}$; in both cases, the algorithm rejects. To see that the algorithm accepts a random function, note a random function is very likely (with probability $1 - \exp(-\ell_m)$) to have

¹⁶Here we consider the size of a DNF-XOR circuit to be its top fan-in (the number of affine subspaces such that the DNF-XOR circuit is a union of the subspaces).

acceptance probability more than $1/3$. Also, for any fixed subspace of dimension $c \cdot k$, the probability that a random function is the constant one function on the subspace is $2^{-2^{c \cdot k}} \leq 2^{-\ell_m^2}$. Since there are at most $2^{(k+1) \cdot \ell_m} = 2^{O(\ell_m \cdot \log(\ell_m))}$ affine subspaces of dimension k , by a union-bound, with probability more than $1/2$, a random function is not constant on any subspace of such dimension k .

Finally, the algorithm is deterministic, and its runtime is polynomial in the number of affine subspaces of dimension $k = \log(\ell_m)$, and is thus at most $2^{O(\ell_m \cdot \log(\ell_m))}$. ■

Functions that are approximated by parity decision trees Finally, consider natural properties that are useful against functions that can be *approximated*, in the average-case sense, by simple functions such as DNF-XOR circuits. Specifically, we consider approximation by *parity decision trees* (PDTs), a model that is known to be related to but strictly weaker than DNF-XOR circuits. Recall that a parity decision tree is simply a decision tree where the nodes can query the value of any parity function of the input variables. We measure the size of such a decision tree by its number of nodes. (See [CS16] for a comparison of PDTs and DNF-XOR circuits.) Then:

Proposition 5.12 (*average-case natural property against PDTs*). *There exists a function $\gamma(\ell_m) \rightarrow 0$ and a natural property computed in time $T(2^{\ell_m}) = 2^{O(\ell_m \cdot \log(\ell_m))}$ against the class of functions $f : \{0,1\}^{\ell_m} \rightarrow \{0,1\}$ that can be $(1/2 + \gamma(\ell_m))$ -approximated by a parity decision tree of size $2^{(1-o(1)) \cdot \ell_m}$.*

Proof. A (k, ϵ) -affine extractor is a function $h : \{0,1\}^{\ell_m} \rightarrow \{0,1\}$ with the following property: For every affine subspace $U \subseteq \{0,1\}^{\ell_m}$ of dimension k , the restriction of h to U has bias at most ϵ . A standard argument shows that a random function is a (k, ϵ) -affine extractor with probability at least $1/2$, where $k = \log(\ell_m / \epsilon^2) + \log \log(\ell_m / \epsilon^2) + C$, and C is a large enough constant.

Let $\gamma(\ell_m) = (\ell_m)^{-1/4}$, and let \mathcal{A}_{ℓ_m} be the class of ℓ_m -bit Boolean functions that compute a (k, γ) -affine extractor, where $k = 2 \cdot \log(\ell_m)$. First, \mathcal{A}_{ℓ_m} is a dense property for every large enough ℓ_m , by the result mentioned above. Secondly, arguing as in the proof of Proposition 5.8, \mathcal{A}_{ℓ_m} can be decided in time $2^{O(\ell_m \cdot \log(\ell_m))}$. Finally, we claim that \mathcal{A}_{ℓ_m} does not accept functions that can be approximated by PDTs. We use the following result from [CS16].

Theorem 5.13 ([CS16]) *Let $f : \{0,1\}^{\ell_m} \rightarrow \{0,1\}$ be a (k, ϵ) -affine extractor. For any function $g : \{0,1\}^{\ell_m} \rightarrow \{0,1\}$ of parity decision tree complexity $\leq \epsilon \cdot 2^{\ell_m - k}$, we have $\Pr_x[f(x) = g(x)] < 1/2 + 4\epsilon$.*

It follows from Theorem 5.13 and from our choice of the parameters k and γ that any Boolean function that can be $(1/2 + \gamma(\ell_m))$ -approximated by a parity decision tree g of size $\leq 2^{\ell_m} / \ell_m^{10}$ cannot be in \mathcal{A}_{ℓ_m} . This completes the proof. ■

5.1.3 Natural properties that would allow to break the generator with any predicate, under Assumption 5.3

In this section we consider two “mild strengthenings” of natural properties that were presented in Section 5.1.2. We then show that if Assumption 5.3 holds and any of the two “mildly stronger” natural properties exist, then Goldreich’s PRG is insecure for the setting of large polynomial stretch with *any* predicate P .

First setting: Worst-case natural property. Recall that in Proposition 5.10 we showed an almost-natural property against conjunctions of $c = O(1)$ DNF-XOR circuits, each of which is of size at most $2^{\epsilon \cdot \ell_m}$, where $\epsilon < 1/c$. We first consider the setting in which there exists a natural property against conjunctions of $c = O(1)$ DNF-XOR circuits, each of which is of size at most $2^{\epsilon \cdot \ell_m}$, where $\epsilon > \Omega(1/\log(c))$. (Indeed, in the foregoing sentence as well as in the following statements we denote the number of inputs to the circuit by ℓ_m , where the notation alludes to the fact that we will use these circuits to compute the function over $\ell_m = \log(m)$ bits whose m -bit truth table is the output of Goldreich’s PRG.)

Assumption 5.14 (mildly strengthening the natural property in Proposition 5.9) *The stronger natural property for AND-DNF-XOR assumption with parameter $\beta > 3$ is the following: For some $k \in \mathbb{N}$ there exists a natural property against the class of Boolean functions over ℓ_m input bits that are computed by AND-DNF-XOR circuits of top fan-in $2^{\beta \cdot k}$ and size at most $O\left(2^{(1/k) \cdot \ell_m}\right)$ containing at most $2 \cdot \beta \cdot \ell_m$ parity gates.*

Theorem 5.15 (breaking Goldreich’s PRG with certain expanders for any predicate, under affine expanders and a stronger natural property for AND-DNF-XOR). *Suppose that Assumption 5.3 holds with some $\beta > 3$, and that Assumption 5.14 holds for the same parameter β . Then, there exists a polynomial-time algorithm A that satisfies the following: For every $n \in \mathbb{N}$ there exists an (n, m, ℓ) -expander G , where $m = n^k$ and $\ell = \beta \cdot k$, such that for every predicate $P : \{0, 1\}^\ell \rightarrow \{0, 1\}$, the algorithm A distinguishes the outputs of Goldreich’s PRG instantiated with G and P from random strings (with constant gap $\Omega(1)$).*

Proof. Let k be as in the hypothesis, let n be sufficiently large, let G be the expander from Assumption 5.3 with right-degree $\ell = \beta \cdot k$, and let $P : \{0, 1\}^\ell \rightarrow \{0, 1\}$ be an arbitrary predicate.

We instantiate Goldreich’s PRG with the expander G and predicate P . By Claim 5.4, instantiated with the parameter value $\delta = 1$ (since P can be trivially computed by a CNF of size 2^ℓ), every output string g_x of the generator can be computed by an AND-DNF-XOR circuit of top fan-in $2^{\beta \cdot k}$ and size at most $O\left(2^{(1/k) \cdot \log(m)}\right)$ containing at most $2 \cdot \beta \cdot \log m$ parity gates. Thus, the natural property from our hypothesis distinguishes the output of the generator from a random string with probability at least $1/2$. ■

Second setting: Average-case natural property. Recall that in Proposition 5.12 we showed a natural property (based on [CS16]) that is useful against functions that can

be approximated by parity decision trees. We now consider the existence of natural properties with similar parameters, but that are useful against the class of functions approximated by DNF-XOR circuits, rather than by PDTs.¹⁷

Assumption 5.16 (another strengthening of the natural property in Proposition 5.9) *The average-case natural property for DNF-XOR assumption with parameter $\beta > 3$ is the following: For some function $\gamma(\ell_m) \rightarrow 0$ and a fixed $\epsilon > 0$, there exists a natural property computable in quasipolynomial time against the class of Boolean functions on ℓ_m input bits that can be $(1/2 + \gamma(\ell_m))$ -approximated by DNF-XOR circuits of size $2^{\epsilon \cdot \ell_m}$ containing at most $2 \cdot \beta \cdot \ell_m$ parity gates at the bottom layer.*

Theorem 5.17 (breaking Goldreich’s PRG for any predicate, under affine expanders and an average-case natural property for DNF-XOR). *Suppose that Assumption 5.3 holds with some $\beta > 3$, and that Assumption 5.16 holds for the same parameter β . Then, for every $k > 1/\epsilon$ there exists a quasipolynomial-time algorithm A that satisfies the following. For a sufficiently large $n \in \mathbb{N}$ there exists an (n, m, ℓ) -expander G , where $m = n^k$ and $\ell = \beta \cdot k$, such that for every predicate $P : \{0, 1\}^\ell \rightarrow \{0, 1\}$, the algorithm A distinguishes the outputs of Goldreich’s PRG instantiated with G and P from random strings (with constant gap $\Omega(1)$).*

Proof. Let $k > 1/\epsilon$, let n be sufficiently large, let G be the expander from Assumption 5.3 with right-degree $\ell = \beta \cdot k$, and let $P : \{0, 1\}^\ell \rightarrow \{0, 1\}$ be an arbitrary predicate.

We instantiate Goldreich’s PRG with the expander G and predicate P . By Claim 5.4, every output string g_x of the generator can be $(1/2 + \Omega_k(1))$ -approximated by a DNF-XOR circuit over $\ell_m = \log(m)$ input variables and of size $O(2^{(1/k) \cdot \ell_m}) \leq 2^{\epsilon \cdot \ell_m}$. In addition, the number of parity gates in g_x is at most $2 \cdot \beta \cdot \ell_m$, and we can assume that $\gamma(\ell_m)$ is sufficiently small (since n is sufficiently large). Thus, the natural property from our hypothesis distinguishes the output of the generator from a random string with probability at least $1/2$. ■

5.2 Expanders with local neighbor functions

In this section we present results that are conditioned on the existence of expanders whose the neighbor functions are computable in \mathcal{NC}^0 . In Section 5.2.1 we formally present the assumption that such expanders exist, and in Section 5.2.2 we show that, conditioned on this assumption, a mild improvement in the parameters of known natural properties allows to break the generator with these expanders and *any* predicate for the setting of a large polynomial stretch.

5.2.1 The local expander assumption

To motivate our assumption, we recall the recent construction of expanders with local neighbor functions, by Viola and Wigderson [VW17] (building on the work of Capalbo

¹⁷To our knowledge, the same natural property behind Proposition 5.12 might also be useful against functions that are approximated by DNF-XOR circuits; we refer to [CS16] for related open problems.

et al. [Cap+02], which used the Zig-Zag product).

Definition 5.18 (multi-output local functions). We say that a function $f : \{0,1\}^r \rightarrow \{0,1\}^{r'}$ is t -local if each output bit of f depends on at most t input bits.

Theorem 5.19 ([VW17, Thm. 4]) There exist $\ell, t \geq 1$ such that for every $n \geq 1$ there exists an (n, n, ℓ) -expander satisfying the following: For each $i \in [\ell]$, the neighbor function $\Gamma_i : [n] \rightarrow [n]$ is t -local.

The construction in Theorem 5.19 works for balanced expanders (i.e., expanders over $[n] \times [n]$), whereas we are interested in unbalanced expanders with $m = \text{poly}(n)$ vertices on the right side. The following assumption asserts that there exists a construction of unbalanced expanders with local neighbor functions as in Theorem 5.19:

Assumption 5.20 (local expander assumption). There is a function $t : \mathbb{N} \rightarrow \mathbb{N}$ and a constant $\beta > 3$ such that for every $k \in \mathbb{N}$ the following holds. For every $n \in \mathbb{N}$ there exists an (n, m, ℓ) -expander, where $m = n^k$ and $\ell = \beta \cdot k$, such that the function $\Gamma_G : \{0,1\}^{\log(m)} \rightarrow \{0,1\}^{\ell \cdot \log(n)}$ that gets as input $i \in [m]$ and outputs the ℓ neighbors of i in G is $t(k)$ -local.

Note that the locality $t(k)$ in Assumption 5.20 is constant, depending only on the polynomial power $k \in \mathbb{N}$. As pointed out by an anonymous reviewer, such expanders can be constructed only when t is at least linear in k (i.e., $t = \Omega(k)$).¹⁸ However, the assumption yields interesting consequences also for larger functions $t(k)$; specifically, the improvement to known natural properties that we need in order to break Goldreich's PRG will depend on t (see Theorem 5.24).

5.2.2 Natural properties that would suffice to break the generator with any predicate, under Assumption 5.20

Under Assumption 5.20, for any $k \in \mathbb{N}$ there exists an expander G over $[n] \times [n^k]$ such that when Goldreich's PRG is instantiated with G and with any predicate P , the output of the generator on any seed is the truth-table of a depth-four circuit with constant top fan-in $2^{\Theta(k)}$ and constant bottom fan-in $t(k)$. As in Claim 5.4, in the following claim we consider the complexity of $P : \{0,1\}^\ell \rightarrow \{0,1\}$ as a CNF of size $2^{\delta \cdot \ell}$, where $\delta \in (0,1]$ (and $\delta = 1$ represents the case of an arbitrary predicate).

Claim 5.21. Supposed that Assumption 5.20 holds with parameters $t : \mathbb{N} \rightarrow \mathbb{N}$ and $\beta > 3$. Then, for every $k \in \mathbb{N}$, and every $n \in \mathbb{N}$ there exists an (n, m, ℓ) -expander, where $m = n^k$ and $\ell = \beta \cdot k$, that satisfies the following: For every $\delta \in (0,1]$ and every predicate $P : \{0,1\}^\ell \rightarrow \{0,1\}$ that can be computed by a depth-two circuit of size $2^{\delta \cdot \ell}$ the following holds, when Goldreich's PRG is instantiated with expander G and predicate P , the output of the generator

¹⁸To see this, consider the bipartite graph $[\log(m)] \times [\ell \cdot \log(n)]$ underlying the local mapping Γ_G . The average degree of left vertices in this graph is $\Delta = \frac{t \cdot \ell \cdot \log(n)}{k \cdot \log(n)} = t \cdot \beta$, and hence there exists a left vertex $i \in [\log(m)]$ of degree at most Δ . Take an input $x \in \{0,1\}^{\log(m)}$ and let x' be the input obtained by flipping the i^{th} bit in x . Then, the strings $\Gamma_G(x)$ and $\Gamma_G(x')$ disagree on at most Δ locations, which means that the set $\{x, x'\}$ expands in G to at most $\ell + \Delta = (1 + t \cdot \beta / \ell) \cdot \ell$ different vertices.

on any fixed x is the truth-table of an \mathcal{AC}^0 circuit $g_x : \{0,1\}^{\log(m)} \rightarrow \{0,1\}$ of depth four, with top fan-in $2^{\delta \cdot \ell} = 2^{\beta \cdot \delta \cdot k}$, bottom fan-in $t = t(k)$, and size $O\left(2^{(1/k) \cdot \log(m)}\right)$.

Proof. For $k \in \mathbb{N}$ and $n \in \mathbb{N}$, let G be the (n, m, ℓ) -expander from Assumption 5.20, let $P : \{0,1\}^\ell \rightarrow \{0,1\}$ be any predicate that can be computed by a DNF of size $2^{\delta \cdot \ell}$, and let $\text{prg} : \{0,1\}^n \rightarrow \{0,1\}^m$ be Goldreich's PRG, instantiated with the graph G and with the predicate P . (We assumed that P can be computed by a DNF of size $2^{\delta \cdot \ell}$ only for simplicity; the proof of the case where P can be computed by a CNF of such size is very similar.)

For any fixed $x \in \{0,1\}^n$, we construct a circuit $g_x : \{0,1\}^{\log(m)} \rightarrow \{0,1\}$ whose truth-table is $\text{prg}(x)$. Given input $i \in [m]$, the circuit g_x :

- Uses $\ell \cdot \log(n)$ DNFs over t bits (each of size $\leq 2^t$) to compute $\Gamma_G(i) \in \{0,1\}^{\ell \cdot \log(n)}$. Additionally, g_x uses the same number of DNFs to compute the negation of each output bit.
- Let $\Phi_x : \{0,1\}^{\log(n)} \rightarrow \{0,1\}$ be a CNF of size at most n such that for any $i \in [n]$ it holds that $\Phi_x(i)$ is the i^{th} bit of x ; we view this CNF as a De Morgan formula over input literals $z_1, \dots, z_{\log(n)}, \neg z_1, \dots, \neg z_{\log(n)}$. The circuit g_x uses ℓ copies of Φ_x to map $\Gamma_G(i) \in \{0,1\}^{\ell \cdot \log(n)}$ to $x|_{\Gamma_G(i)} \in \{0,1\}^\ell$, and similarly uses ℓ copies of a CNF of size at most n for $\neg\Phi_x$ to compute the negations of $x|_{\Gamma_G(i)} \in \{0,1\}^\ell$.
- Finally, the circuit g_x maps $x|_{\Gamma_G(i)} \in \{0,1\}^\ell$ to an output bit $P(x|_{\Gamma_G(i)})$, using a DNF of size $2^{\delta \cdot \ell}$. This DNF is viewed as a de Morgan formula over input literals $y_1, \dots, y_\ell, \neg y_1, \dots, \neg y_\ell$.

The total depth of the circuit for g_x is four (after collapsing two layers), its bottom fan-in is t , and its top fan-in (which is the size of the DNF that computes P) is $c \leq 2^{\delta \cdot \ell} = 2^{\delta \cdot \beta \cdot k}$. Finally, the size of the circuit for g_x is less than

$$2 \cdot \ell \cdot \log(n) \cdot 2^t + 2 \cdot \ell \cdot n + 2^\ell = O(2^{(1/k) \cdot \log(m)}),$$

for a constant k and as a function of n . ■

Claim 5.21 implies that to break Goldreich's PRG (with the expander given by Assumption 5.20 and with an ℓ -bit predicate computable by a depth-two circuit of size $2^{\delta \cdot \ell}$) it suffices to have a natural property against depth-four circuits over ℓ_m input bits whose top fan-in is $c = 2^{\beta \cdot \delta \cdot k}$, bottom fan-in is $t(\log(c)/\beta)$ and size is $O\left(2^{(\beta/\log(c)) \cdot \ell_m}\right)$.

Known results yield a natural property for depth four circuits over ℓ_m bits with constant top fan-in c and constant bottom fan-in t whose size is at most $2^{\epsilon \cdot \ell_m}$, where $\epsilon = \epsilon(c, t) \approx \frac{1/t}{\log(c)}$ (rather than $\frac{\beta}{\log(c)}$). Specifically:

Proposition 5.22 (natural property for depth-four circuits with constant top fan-in and constant bottom fan-in) *There exists a polynomial-time algorithm that is given as input the truth-table of a Boolean function $h : \{0,1\}^{\ell_m} \rightarrow \{0,1\}$ and satisfies the following:*

1. If h can be computed by a depth four circuits of top fan-in c , bottom fan-in t , and size at most $2^{\epsilon(t,c) \cdot \ell_m}$, where $\epsilon(t,c) = \frac{1}{3 \cdot 5^2 \cdot 2^{24} \cdot t \cdot \log(c)}$, then the algorithm rejects.
2. The algorithm accepts a constant fraction of all functions.

The proof of Proposition 5.22 relies on Håstad’s switching lemma [Hås87] as well as on the sensitivity bound of Amano [Ama11]; we defer the details to Appendix B.

The implication of the foregoing discussion is that a natural property that is *mildly* stronger than the known one in Proposition 5.22 would break Goldreich’s PRG with the expander from Assumption 5.20 and *any possible predicate*. That is:

Assumption 5.23 (mildly strengthening the natural property in Proposition 5.22) *The stronger natural property for depth-four circuits assumption with parameters $t: \mathbb{N} \rightarrow \mathbb{N}$ and $\beta > 3$ is the following: For a sufficiently large constant $c > 2^\beta$, there exists a natural property against the class of Boolean functions on ℓ_m input bits that are computed by depth four circuits of top fan-in c , bottom fan-in $t(\log(c)/\beta)$, and size $O\left(2^{(\beta/\log(c)) \cdot \ell_m}\right)$, where the O -notation hides an arbitrarily large constant.*

Theorem 5.24 (breaking Goldreich’s PRG for any predicate, under local expanders and a stronger natural property for depth-four circuits). *Suppose that Assumption 5.20 holds with parameters $t: \mathbb{N} \rightarrow \mathbb{N}$ and $\beta > 3$, and that Assumption 5.23 holds for the same t and β . Then, for a sufficiently large constant $k \in \mathbb{N}$, there exists a polynomial-time algorithm A that satisfies the following. For every $n \in \mathbb{N}$ there exists an (n, m, ℓ) -expander G , where $m = n^k$ and $\ell = \beta \cdot k$, such that for every predicate $P: \{0, 1\}^\ell \rightarrow \{0, 1\}$, the algorithm A distinguishes the outputs of Goldreich’s PRG instantiated with G and P from random strings (with constant gap $\Omega(1)$).*

Proof. Let $k \in \mathbb{N}$ be sufficiently large such that $c = 2^{\beta \cdot k}$ is sufficiently large to satisfy the hypothesis regarding the natural property, let $n \in \mathbb{N}$ be sufficiently large, let G be the expander from Assumption 5.20 with right-degree $\ell = \beta \cdot k$, and let $P: \{0, 1\}^\ell \rightarrow \{0, 1\}$ be an arbitrary predicate.

We instantiate Goldreich’s PRG with the expander G and predicate P . By Claim 5.21, every output string g_x of the generator can be computed by a depth four circuit over $\ell_m = \log(m)$ input variables with top fan-in $c = 2^{\beta \cdot k}$, bottom fan-in $t(k) = t(\log(c)/\beta)$, and size $O\left(2^{(1/k) \cdot \ell_m}\right) = O\left(2^{(\beta/\log(c)) \cdot \ell_m}\right)$. Thus, the natural property from our hypothesis distinguishes the output of the generator from a random string with probability at least $1/2$. ■

We remind the reader that the parameter $t(\cdot)$ in Assumption 5.3 and in Theorem 5.17 quantifies the locality of the \mathcal{NC}^0 circuits in the local expander. (As explained after Assumption 5.3, an interesting small value to consider is $t(k) = \Theta(k)$.)

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Appendix A Proof of Theorem 3.4: Unbalanced Expanders

Let us recall the statement of Theorem 3.4 and detail the proof.

Theorem A.1 (*a random graph is a good lossless expander*). Let $n \leq m \in \mathbb{N}$ and $d \in \mathbb{N}$ such that $c \cdot \frac{\log(m)}{\log(n)} \leq \ell \leq n^{1/c}$, where c is a sufficiently large constant. Let $G = ([n], [m], E)$ be a random bipartite graph of right-degree ℓ ; that is, the ℓ neighbors of each $i \in [m]$ are chosen uniformly in $[n]$ (allowing repetitions). Then, with probability at least $1 - 1/\text{poly}(n)$ it holds that G is an $(n^{0.99}, 0.99)$ -expander.¹⁹

Proof. We say that a set $S \subseteq [m]$ expands if $|\Gamma(S)| \geq 0.99 \cdot \ell \cdot |S|$. For any fixed $k \leq n^{0.99}$, we show that with probability $1 - 1/\text{poly}(n)$ it holds that all sets of size k expand (and the theorem follows by union-bounding over all values of k).

To do so, fix $k \leq n^{0.99}$, and fix a set $S \subseteq [m]$ of size $|S| = k$. For any fixed set $T \subseteq [n]$ of size less than $0.99 \cdot \ell \cdot k$, the probability that $\Gamma(S) \subseteq T$ is $\left(\frac{|T|}{n}\right)^{\ell \cdot k}$. Union-bounding

¹⁹The polynomial power depends on c and can be arbitrarily large.

over all $T \subseteq [n]$ of such size, the probability that S does not expand is at most

$$\binom{n}{0.99 \cdot \ell \cdot k} \cdot \left(\frac{|T|}{n}\right)^{\ell \cdot k} < \left(\frac{e \cdot n}{0.99 \cdot \ell \cdot k}\right)^{0.99 \cdot \ell \cdot k} \cdot \left(\frac{|T|}{n}\right)^{\ell \cdot k} < \left(\frac{O(|T|)}{n}\right)^{0.01 \cdot \ell \cdot k}.$$

Union-bounding over all sets $S \subseteq [m]$ of size $|S| = k$, the probability that there exists a non-expanding set of size k is at most

$$\begin{aligned} \binom{m}{k} \cdot \left(\frac{O(|T|)}{n}\right)^{0.01 \cdot \ell \cdot k} &< \left(\frac{e \cdot m}{k}\right)^k \cdot \left(O(\ell) \cdot \frac{k}{n}\right)^{0.01 \cdot \ell \cdot k} \\ &= 2^{O(\ell \cdot \log(\ell) \cdot k)} \cdot \left(\frac{m}{k}\right)^k \cdot \left(\frac{k}{n}\right)^{0.01 \cdot \ell \cdot k} \\ &< 2^{O(\ell \cdot \log(\ell) \cdot k)} \cdot n^{k\ell/c - 0.01 \cdot \ell \cdot k} \cdot k^{0.01 \cdot \ell \cdot k}, \end{aligned} \quad (\text{A.1})$$

where the last inequality relied on the fact that $m < n^{\ell/c}$ (since $\ell \geq c \cdot (\log(m)/\log(n))$). The expression in Eq. (A.1) is exponential in

$$\begin{aligned} O(k \cdot \ell) \cdot \left(\log(\ell) + (1/c - 0.01) \cdot \log(n) + 0.01 \cdot \log(k)\right) \\ \leq O(k \cdot \ell) \cdot \left(2/c - 10^{-4}\right) \cdot \log(n), \end{aligned}$$

where the last inequality relied on the fact that $k \leq n^{99}$ and on the fact that $c \leq \ell \leq n^{1/c}$. Assuming that c is sufficiently large such that $2/c - 0.01^2 < -10^{-5}$, the probability that there exists a set of size k that does not expand is at most $n^{-10^{-5} \cdot c}$. As mentioned above, the theorem follows by union-bounding over $k = 1, \dots, n^{0.99}$. ■

We note that the right-degree bound provided by Theorem A.1 does not contradict the impossibility results discussed for instance in [GUV09]. This is because the expanding sets in Theorem A.1 are of size at most $n^{1-\Omega(1)}$ instead of $\Omega(n)$.

Appendix B A natural property for depth-four circuits

Our goal in this appendix is to construct the following natural property:

Proposition B.1 (natural property for depth-four circuits with constant top fan-in and constant bottom fan-in) *There exists a polynomial-time algorithm that is given as input the truth-table of a Boolean function $h: \{0, 1\}^{\ell_m} \rightarrow \{0, 1\}$ and satisfies the following:*

1. *If h can be computed by a depth four circuits of top fan-in c , bottom fan-in t , and size at most $2^{\epsilon(t,c) \cdot \ell_m}$, where $\epsilon(t, c) = \frac{1}{3 \cdot 5^2 \cdot 2^{24} \cdot t \cdot \log(c)}$, then the algorithm rejects.*
2. *The algorithm accepts a constant fraction of all functions.*

In high-level, to prove Proposition B.1 we first show that any depth-four circuit as in the first item simplifies to a depth-two circuit of small width (say, width 10) under a random restriction that keeps significantly more variables alive in expectation (say, 1000); the proof of this result is a standard application of Håstad's switching lemma. Consequently, using the bound of Amano [Ama11], we deduce that under a random restriction such a circuit has average sensitivity that is much smaller than the expected average sensitivity of a random function. Our natural property simply enumerates over all restrictions and computes the average sensitivity of the restricted function.

Let us now formally prove the result. For the first part, recall Håstad's switching lemma. For $p \in [0, 1]$ and $\ell_m \in \mathbb{N}$, we denote by \mathcal{R}_p the distribution over restrictions in $\{0, 1, \star\}^{\ell_m}$ that is obtained by independently setting each coordinate to \star with probability p , and to a uniformly chosen bit otherwise. Recall that for a function $f: \{0, 1\}^{\ell_m} \rightarrow \{0, 1\}$ and a restriction $\rho \in \{0, 1, \star\}^{\ell_m}$ we define $f|_{\rho}$ as the function that takes input $z \in \rho^{-1}(\star)$ and outputs $f(x)$ where $x_i = \begin{cases} z_i & i \in \rho^{-1}(\star) \\ \rho_i & o.w. \end{cases}$. Then,

Theorem B.2 (Håstad's switching lemma [Hås87]). *Let $g: \{0, 1\}^{\ell_m} \rightarrow \{0, 1\}$ be a function computed by a depth-2 circuit of bottom fan-in w , and let $\rho \sim \mathcal{R}_p$. Then, for any $s \geq 1$, the probability that $g|_{\rho}$ cannot be computed by a decision tree of depth s is at most $(5pw)^s$.*

Lemma B.3. *Let $f: \{0, 1\}^{\ell_m} \rightarrow \{0, 1\}$ be computed by a circuit of depth four and size at most $S = 2^{\epsilon \cdot \ell_m}$ that has top fan-in $2 \leq c \leq O(1)$ and bottom fan-in $t = O(1)$ such that $\epsilon \leq \frac{1}{3 \cdot 5^2 \cdot 2^{24} \cdot t \cdot \log(c)}$. Then, for $p = \frac{1/\epsilon}{5^2 \cdot 2^{22} \cdot t} \cdot \frac{1}{\ell_m}$, with probability at least $1 - 4/c$ over choice of restriction $\rho \sim \mathcal{R}_p$ it holds that:*

1. *The restricted function $f|_{\rho}$ can be computed by a depth-two circuit with bottom fan-in $\log(c)/10$.*
2. *The restriction ρ keeps at least $\log(c)$ variables alive.*

Proof. We prove the first item using Theorem B.2. Specifically, we first apply a restriction with $p = 1/20t$. For each gate of distance two from the inputs we use Theorem B.2 with values $s = \log(S)$ and $w = t$, and deduce that with probability $1 - 1/S^2$ the gate can be computed by a decision tree of depth $\log(S)$. After union-bounding over at most S such gates, we deduce that with probability $1 - 1/S = 1 - o(1)$ each of these gates can be computed by a decision tree of depth s . We transform each decision tree whose parent gate is an AND gate (resp., OR gate) to a CNF (resp., DNF) of size $S = 2^s$ and bottom fan-in s , and then combine these gates with the gates in the middle layer above them. Note that the number of gates in the middle layer is still c , and that in the bottom layer we now have S^2 gates (each of the S gates that were in the original bottom layer contributed at most S terms/clauses), each of fan-in $\log(S)$.

Next, we apply another restriction with $p = 1/(5 \cdot 2^{20} \cdot \log(S))$. Again, for each gate of distance two from the inputs we use Theorem B.2 with values $w = \log(S)$ and $s = \log(c)/10$, and deduce that with probability $1 - 1/c^2$ the gate can be computed by

a decision tree of depth $\log(c)/10$. After union-bounding over the c (or less) gates in the middle layer, with probability at least $1 - 1/c$, after the restriction the circuit is of depth two with at most $c^{1+1/10}$ gates in the bottom layer, each of fan-in $\log(c)/10$.

To prove the second item, note that by the hypothesis that ϵ is sufficiently small, the expected number of living variables under $\rho \sim \mathcal{R}_p$ is at least $2 \log(c)$. (Recall that the expected fraction of living variables after applying two independent restrictions from \mathcal{R}_{p_1} and from \mathcal{R}_{p_2} is $p_1 \cdot p_2$.) By a Chernoff bound (see, e.g., [Gol08, Apdx. D.1.2.3]), the probability that less than $\log(c)$ variables remain alive is at most $2 \exp(-1/3p) \cdot (p^2/4) \cdot \ell_m = 2/c$. ■

For the second part of the proof we formally define the average sensitivity of a function, and deduce (as a corollary of Lemma B.3 and of [Ama11]) that the restricted circuit has relatively small average sensitivity. As pointed out by an anonymous reviewer, this result can be viewed as a refinement of Boppana's [Bop97] bound on the average sensitivity of \mathcal{AC}^0 .

Definition B.4 (*average sensitivity*). We say that two strings $x, y \in \{0, 1\}^r$ are neighbors if x and y disagree on a single bit. The average sensitivity of a function $f : \{0, 1\}^r \rightarrow \{0, 1\}$ is defined as the average, over $x \in \{0, 1\}^r$, of the number of neighbors y of x such that $f(x) \neq f(y)$.

Corollary B.5. Let $f : \{0, 1\}^{\ell_m} \rightarrow \{0, 1\}$ be computed by a circuit of depth four and size at most $S = 2^{\epsilon \cdot \ell_m}$ that has top fan-in $c = O(1)$ and bottom fan-in $t = O(1)$, where $\epsilon(t, c) = \frac{1}{3 \cdot 5^2 \cdot 2^{24} \cdot t \cdot \log(c)}$ and c is sufficiently large. Then, for $p = \frac{1/\epsilon}{5^2 \cdot 2^{22} \cdot t} \cdot \frac{1}{\ell_m}$ it holds that:

1. The expected value over $\rho \sim \mathcal{R}_p$ of the average sensitivity of $f \upharpoonright_\rho$ is less than $\frac{1}{10} \cdot \log(c)$.
2. With probability $\Omega(1)$ over choice of random function g , the expected value over $\rho \sim \mathcal{R}_p$ of the average sensitivity of $g \upharpoonright_\rho$ is at least $\frac{1}{5} \cdot \log(c)$.

Proof. To prove the first item we use Lemma B.3 as well as the fact (which was proved by Amano [Ama11]) that depth-two circuits with bottom fan-in $\log(c)/10$ have average sensitivity at most $\log(c)/10$. Thus, the expected average sensitivity of $f \upharpoonright_\rho$ is at most $(4/c + 1/10) \cdot \log(c) < \frac{3}{20} \cdot \log(c)$, assuming c is sufficiently large.

For the second item, let us denote by $\text{sens}(g \upharpoonright_\rho)$ the average sensitivity of $g \upharpoonright_\rho$. Then, we have that:

Fact B.5.1. It holds that $\mathbb{E}_{g, \rho \sim \mathcal{R}_p}[\text{sens}(g \upharpoonright_\rho)] \geq (1 - 4/c)^2 \cdot \log(c)/4$.

Proof. Condition on any restriction ρ that keeps at least $\log(c)$ variables alive. Note that it suffices to show that the expected average sensitivity of g is at least $\log(c)/2$ and that with probability at least $1 - 4/c$, the actual average sensitivity of g is at least $\log(c)/4$. (The statement then follows since the choices of ρ and of g are independent.) To show this, suppose g is a random function over $n' = \log c$ input variables, and let $E = n'2^{n'}/2$ be the number of edges in the n' -dimensional hypercube. Let X_e for $e \in [E]$ be the 0/1-random variable that is 1 if and only if g flips its value along edge

e. Then, for $X = \sum X_e$, we get $\mathbb{E}[X] = E/2$. Note that the average sensitivity of the random function g is precisely $2X/2^{n'}$. Therefore, it is enough for us to upper bound $\Pr[|X - E/2| \geq E/4]$, since if this event does not happen then the average sensitivity of g is at least $(1/2^{n'-1})(E/2 - E/4) \geq n'/4 = \log(c)/4$. For that, it suffices to use Chebyshev's inequality together with the pairwise independence of $\{X_e\}_{e \in [E]}$: $\mu_X = E/2$, $\sigma_X^2 = E/4$, and consequently the aforementioned probability is at most $4/E = 8/(\log(c) \cdot c) \leq 4/c$, where we have used $c \geq 4$. \square

Relying on Fact B.5.1, the probability over g that $\mathbb{E}_\rho[\text{sens}(g|_\rho)] < \log(c)/5$ is at most $\frac{1-(1-4/c)^2/4}{4/5}$, which is a constant smaller than one for a sufficiently large c . \blacksquare

Finally, to prove Proposition B.1, consider the algorithm that, when given a truth-table $h \in \{0, 1\}^{2^{\ell_m}}$, enumerates over all restrictions $\rho \in \{0, 1, \star\}^{\ell_m}$, computes the average sensitivity of $h|_\rho$, and accepts if and only if $\mathbb{E}_\rho[\text{sens}(h|_\rho)] > \log(c)/10$ (note that the algorithm weighs the restrictions according to their probabilities in \mathcal{R}_ρ , which can be done in time $\text{poly}(2^{\ell_m})$). Indeed, this algorithm rejects every h in the relevant circuit class, but accepts an $\Omega(1)$ -fraction of all functions.