

Probabilistically Checkable Reconfiguration Proofs and Inapproximability of Reconfiguration Problems

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Abstract

Motivated by the inapproximability of reconfiguration problems, we present a new PCP-type characterization of PSPACE, which we call a *probabilistically checkable reconfiguration proof* (PCRP): Any PSPACE computation can be encoded into an exponentially long sequence of polynomially long proofs such that every adjacent pair of the proofs differs in at most one bit, and every proof can be probabilistically checked by reading a constant number of bits.

Using the new characterization, we prove PSPACE-completeness of approximate versions of many reconfiguration problems, such as the MAXMIN 3-SAT RECONFIGURA-TION problem. This resolves the open problem posed by Ito, Demaine, Harvey, Papadimitriou, Sideri, Uehara, and Uno (ISAAC 2008; Theor. Comput. Sci. 2011) as well as the Reconfiguration Inapproximability Hypothesis by Ohsaka (STACS 2023) affirmatively. We also present PSPACE-completeness of approximating the MAXMIN CLIQUE RECON-FIGURATION problem to within a factor of n^{ε} for some constant $\varepsilon > 0$.

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1 Introduction

Reconfiguration problems ask to decide whether there exists a sequence of operations that transform one feasible solution to another. A canonical example is the 3-SAT RECON-FIGURATION problem, which is known to be PSPACE-complete [GKMP09].

Definition 1.1 (3-SAT RECONFIGURATION [GKMP09]). Given a 3-CNF formula φ and its two satisfying assignments σ^{start} and σ^{goal} , we are required to decide if there is a sequence of satisfying assignments to φ , ($\sigma^{(1)}, \ldots, \sigma^{(T)}$), such that $\sigma^{(1)} = \sigma^{\text{start}}$, $\sigma^{(T)} = \sigma^{\text{goal}}$, and $\sigma^{(t)}$ and $\sigma^{(t+1)}$ differ in at most one variable for every $t \in \{1, \ldots, T-1\}$.

Example 1.2. Suppose we are given a 3-CNF formula $\varphi := (\overline{x_1} \lor \overline{x_2} \lor x_3) \land (\overline{x_1} \lor x_2 \lor \overline{x_3}) \land (x_1 \lor \overline{x_2} \lor \overline{x_3})$ made up of three clauses over three variables x_1, x_2, x_3 and its two satisfying assignments $\sigma_1^{\text{start}} := (1,0,0)$ and $\sigma_1^{\text{goal}} := (0,1,0)$. Then, $(\varphi, \sigma_1^{\text{start}}, \sigma_1^{\text{goal}})$ is a YES instance of 3-SAT RECONFIGURATION: there exists a sequence ((1,0,0),(0,0,0),(0,1,0)) from σ_1^{start} to σ_1^{goal} that meets the requirement. On the other hand, if we are given a pair of two satisfying assignments $\sigma_2^{\text{start}} := (1,0,0)$ and $\sigma_2^{\text{goal}} := (1,1,1)$, then $(\varphi, \sigma_2^{\text{start}}, \sigma_2^{\text{goal}})$ is a NO instance because any sequence from σ_2^{start} to σ_2^{goal} must run through (0,1,1), (1,0,1), or (1,1,0), neither of which satisfy φ .

It is natural to consider its approximate variant, whose complexity was posed as an open problem in [IDHPSUU11].

Definition 1.3 (MAXMIN 3-SAT RECONFIGURATION [IDHPSUU11]). Given a 3-CNF formula φ over *m* clauses C_1, \ldots, C_m and its two satisfying assignments σ^{start} and σ^{goal} , we are required to find a sequence of assignments, $(\sigma^{(1)}, \ldots, \sigma^{(T)})$, such that $\sigma^{(1)} = \sigma^{\text{start}}, \sigma^{(T)} = \sigma^{\text{goal}}, \sigma^{(t)}$ and $\sigma^{(t+1)}$ differ in at most one variable for every $t \in \{1, \ldots, T-1\}$, and the following objective value is maximized:

$$\min_{1 \le t \le T} \frac{1}{m} \left| \left\{ j \in \{1, \dots, m\} \middle| \sigma^{(t)} \text{ satisfies } C_j \right\} \right|.$$
(1.1)

Example 1.4. Consider again the same 3-CNF formula φ and its two satisfying assignments $\sigma_2^{\text{start}} = (1,0,0)$ and $\sigma_2^{\text{goal}} = (1,1,1)$. There is a sequence ((1,0,0),(1,1,0),(1,1,1)) from σ_2^{start} to σ_2^{goal} , which is a feasible solution to MAXMIN 3-SAT RECONFIGURATION and whose objective value is $\frac{2}{3}$.

The main contribution of this paper is to prove PSPACE-completeness of approximating the MAXMIN 3-SAT RECONFIGURATION problem within a constant factor, which answers the open problem of [IDHPSUU11]. In what follows, we present the background of this result and then the details of our results.

1.1 Background

Given a *source problem* that asks the existence of a feasible solution, *reconfiguration problems* are defined as a problem of deciding the existence of a *reconfiguration sequence*, that is, a step-by-step transformation between a pair of feasible solutions while always preserving the feasibility of solutions. For example, 3-SAT RECONFIGURATION [GKMP09] is defined from 3-SAT as a source problem. Many reconfiguration problems can be defined from Boolean satisfiability, constraint satisfaction problems, graph problems, and others. Studying reconfiguration problems may help elucidate the structure of the solution space [GKMP09], which is motivated by, e.g., the application to the behavior analysis of SAT solvers, such as DPLL [ABM04]. From a different point of view, reconfiguration problems may date back to motion planning [HSS84] and classical puzzles, including 15 puzzles [JS79] and Rubik's Cube.

Typically, a reconfiguration problem becomes PSPACE-complete if its source problem is intractable (say, NP-complete); e.g., 3-SAT [GKMP09], INDEPENDENT SET [HD05, HD09], SET COVER [IDHPSUU11], and 4-COLORING [BC09]. On the other hand, a source problem in P frequently leads to a reconfiguration problem in P, e.g., MATCHING [IDHPSUU11] and 2-SAT [GKMP09]. Some exceptions are known: whereas 3-COLORING is NP-complete, its reconfiguration problem is solvable in polynomial time [CvJ11]; SHORTEST PATH on a graph is tractable, but its reconfiguration problem is PSPACE-complete [Bon13]. We refer the readers to the surveys by Nishimura [Nis18] and van den Heuvel [van13] for algorithmic and hardness results and the Combinatorial Reconfiguration wiki [Hoa23] for an exhaustive list of related articles.

A common way to cope with intractable problems is to consider approximation problems. Relaxing the feasibility of intermediate solutions, we can formalize *approximate variants* for reconfiguration problems, which are also motivated by the situation wherein there does not exist a reconfiguration sequence for the original decision problem. For example, in MAXMIN 3-SAT RECONFIGURATION [IDHPSUU11], we are allowed to include any *non-satisfying* assignment in a reconfiguration sequence, but required to maximize the *minimum fraction of satisfied clauses*. Solving this problem may result in a reasonable reconfiguration sequence consisting of *almost-satisfying* assignments, e.g., each violating at most 1% of clauses. Intriguingly, a different trend regarding the approximability has been observed between a source problem and its reconfiguration analogue; e.g., SET COVER is NPhard to approximate within a factor better than ln *n* [DS14, Fei98, LY94], whereas MINMAX SET COVER RECONFIGURATION admits a 2-factor approximation algorithm [IDHPSUU11]. Other reconfiguration problems whose approximability was investigated include: SUBSET SUM RECONFIGURATION has a PTAS [ID14]; SUBMODULAR RECONFIGURATION [OM22] and POWER SUPPLY RECONFIGURATION [IDHPSUU11] are constant-factor approximable.

Little is known about the hardness of approximation for reconfiguration problems. Using the fact that source problems (e.g., MAX 3-SAT) are NP-hard to approximate [ALMSS98, AS98], Ito, Demaine, Harvey, Papadimitriou, Sideri, Uehara, and Uno [IDHPSUU11] proved that several reconfiguration problems (e.g., MAXMIN 3-SAT RECONFIGURATION) are NPhard to approximate; however, most reconfiguration problems are in PSPACE, and thus their NP-hardness results are not optimal. It was left open to improve the NP-hardness results to PSPACE-hardness. We here stress the significance of showing PSPACE-hardness compared to NP-hardness:

- 1. PSPACE-hardness is *tight* because most reconfiguration problems belong to PSPACE [Nis18];
- 2. it disproves the existence of a polynomial-length witness (in particular, a polynomiallength reconfiguration sequence) assuming NP ≠ PSPACE;
- 3. it rules out any polynomial-time algorithm under the weak assumption that $P \neq PSPACE$.

In order to improve the NP-hardness of approximation to PSPACE-hardness of approximation, it is crucial to develop a reconfiguration analogue of the PCP theorem [ALMSS98, AS98]. As indicated by the gap in the approximation factors of SET COVER and its reconfiguration counterpart, the required theory must be different and tailored to PSPACE. Ohsaka [Ohs23b] recently postulated a reconfiguration analogue of the PCP theorem, called the *Reconfiguration Inapproximability Hypothesis* (RIH), under which a bunch of popular reconfiguration problems are shown to be PSPACE-hard to approximate. The major open question is whether RIH holds.

1.2 Our Results

Our contribution is to present a new PCP-type characterization of PSPACE, which we call a *probabilistically checkable reconfiguration proof* (PCPR), and thereby affirmatively resolve the open problem posed by Ito, Demaine, Harvey, Papadimitriou, Sideri, Uehara, and Uno [IDHPSUU11] and confirm RIH of Ohsaka [Ohs23b].

Our characterization of PSPACE encodes any PSPACE computation into an exponentially long reconfiguration sequence of polynomial-length proofs, each of which can be probabilistically checked by reading a constant number of bits. A *reconfiguration sequence from* π^{start} *to* π^{goal} *over* $\{0,1\}^n$ is a sequence $(\pi^{(1)}, \dots, \pi^{(T)}) \in (\{0,1\}^n)^*$ such that $\pi^{\text{start}} = \pi^{(1)}, \pi^{\text{goal}} = \pi^{(T)},$ and $\pi^{(t)}$ and $\pi^{(t+1)}$ differ in at most one bit for every $t \in \{1, \dots, T-1\}$.

Theorem 1.5 (Probabilistically Checkable Reconfiguration Proof (PCRP); see also Theorem 5.1). A language L is in PSPACE if and only if there exist a randomized polynomial-time verifier V with randomness complexity $O(\log n)$ and query complexity O(1) on inputs of length n and polynomial-time algorithms π^{start} and π^{goal} with the following properties:

1. (Completeness) If $x \in L$, then there exists a reconfiguration sequence $(\pi^{(1)}, \dots, \pi^{(T)})$ from $\pi^{\text{start}}(x)$ to $\pi^{\text{goal}}(x)$ over $\{0, 1\}^{\text{poly}(n)}$ such that for every $t \in \{1, \dots, T\}$,

$$\mathbb{P}\mathbb{P}\left[V^{\pi^{(t)}}(x)=1\right]=1.$$

2. (Soundness) If $x \notin L$, then for every reconfiguration sequence $(\pi^{(1)}, \dots, \pi^{(T)})$ from $\pi^{\text{start}}(x)$ to $\pi^{\text{goal}}(x)$, for some $t \in \{1, \dots, T\}$,

$$\mathbb{Pr}\Big[V^{\pi^{(t)}}(x)=1\Big]<\frac{1}{2}.$$

Here, $V^{\pi^{(t)}}(x)$ denotes the output of V on input x given oracle access to $\pi^{(t)}$, and the probabilities are over the $O(\log n)$ random bits of the verifier V.

The verifier V can be regarded as a $\forall \cdot \operatorname{coRP-type}$ verifier: The verifier co-nondeterministically guesses $t \in \{1, \dots, T\}$ and probabilistically checks the *t*-th proof $\pi^{(t)}$. This verifier should be compared with the standard coRP-type PCP verifier V' for PSPACE-complete problems, which can be obtained from the PCP theorem for NEXP \supseteq PSPACE [BFL91]. The number of random bits used by V' is $n^{\Theta(1)}$, whereas the number of random bits of our verifier V is $O(\log n)$. The latter is crucial for the application to inapproximability of reconfiguration problems. The standard verifier V' uses only random bits, whereas our verifier V co-nondeterministically guesses t. Given that V' does not use any nondeterministic choice, it is natural to wonder whether V can be improved to a coRP-type verifier that chooses $t \in \{1, \dots, T\}$ randomly; however, such an extension is impossible (see Observation 5.9), and thus our characterization is one of the "best" characterizations in this direction.

As a corollary of Theorem 5.1 and [Ohs23a, Ohs23b], we obtain that a host of reconfiguration problems are PSPACE-complete to approximate.

Corollary 1.6 (from Theorem 5.1 and [Ohs23a, Ohs23b]). For some universal constant $\varepsilon_0 \in (0, 1)$, the following approximate variants of reconfiguration problems are PSPACE-hard to approximate within a factor of $(1 - \varepsilon_0)$.

- MAXMIN k-SAT RECONFIGURATION for all $k \ge 2$;
- MAXMIN q-CSP RECONFIGURATION for all $q \ge 2$;
- MAXMIN INDEPENDENT SET RECONFIGURATION on bounded-degree graphs;
- MINMAX VERTEX COVER RECONFIGURATION on bounded-degree graphs;
- MAXMIN CLIQUE RECONFIGURATION;
- MINMAX DOMINATING SET RECONFIGURATION;
- MINMAX SET COVER RECONFIGURATION;
- MAXMIN NONDETERMINISTIC CONSTRAINT LOGIC.

Moreover, we improve an inapproximability factor of MAXMIN CLIQUE RECONFIGURA-TION to a polynomial; that is, MAXMIN CLIQUE RECONFIGURATION is PSPACE-hard to approximate within a factor of n^{ε} for some constant $\varepsilon > 0$, where *n* is the number of vertices (Theorem 6.2). This is the first polynomial-factor inapproximability result for approximate variants of reconfiguration problems (to the best of our knowledge).¹

¹See Section 3 for discussion of existing hardness-of-approximation results for reconfiguration problems.

2 **Proof Overview**

Here, we present a proof sketch of Theorem 1.5.

A naïve attempt for the proof of Theorem 1.5 would be to develop reconfiguration counterparts for the simple proof of the PCP theorem by Dinur [Din07]. The proof of the PCP theorem consists of repeated applications of the three steps — a degree reduction (the preprocessing lemma [Din07, Lemma 1.9]), gap amplification and an alphabet reduction. Counterparts of some of the steps have been developed in the recent literature of reconfiguration problems [Ohs23a, Ohs23b]. For example, Ohsaka [Ohs23b] presented a degree reduction for reconfiguration problems, i.e., a reduction that converts a graph that represents a PCRP (probabilistically checkable reconfiguration proof) system with soundness error $1 - \varepsilon$ into another graph whose degree Δ is small. However, the parameter achieved in [Ohs23b] is weaker than that of [Din07, PY91]: $\Delta \leq \text{poly}(1/\varepsilon)$. If $\varepsilon = o(1)$, the degree Δ can be $\omega(1)$, which is not sufficient for Dinur's proof to go through. It appears to be very difficult to construct PCRPs based on this approach.

Our actual approach is much simpler. We use existing machinery developed in the literature of PCP theorems in a black-box way. The main ingredient for our proof is the *PCP* of *Proximity* (PCPP) [BGHSV06, DR06]. A PCPP for a language $L \in NP$ allows us to approximately verify that $x \in L$ by reading a constant number of bits from x and a proof. In particular, by encoding x by an error-correcting code, we can reliably check whether $x \in L$ efficiently.

To construct a PCRP for every problem in PSPACE, it suffices to construct a PCRP for some PSPACE-complete problem. We consider the PSPACE-complete problem called SUC-CINCT GRAPH REACHABILITY. In what follows, we first explain how this problem can be regarded as a reconfiguration problem, and then explain how to construct a PCRP system for SUCCINCT GRAPH REACHABILITY.

2.1 SUCCINCT GRAPH REACHABILITY as Reconfiguration Problems

SUCCINCT GRAPH REACHABILITY is the following problem. The input consists of a circuit which succinctly represents an exponentially large graph G = (V, E) and two vertices v^{start} and $v^{\text{goal}} \in V$, and the task is to decide whether there exists a path from v^{start} to v^{goal} in G. Each vertex is represented by an n-bit string; i.e., $V = \{0, 1\}^n$. For simplicity of notation, throughout this section, we assume that every vertex in G has a self-loop, i.e., $(x, x) \in E$ for every $x \in V$. For two strings x and y, we denote by $x \circ y$ the concatenation of x and y.

SUCCINCT GRAPH REACHABILITY can be naturally regarded as the following reconfiguration problem. Given a (succinctly described) graph G and two vertices $v^{\text{start}}, v^{\text{goal}} \in V$, the task is to decide whether there exists a sequence $(x_1 \circ y_1, \dots, x_T \circ y_T) \in (\{0, 1\}^{2n})^*$ from $v^{\text{start}} \circ v^{\text{start}}$ to $v^{\text{goal}} \circ v^{\text{goal}}$ such that

- 1. every configuration $x_t \circ y_t \in \{0,1\}^{2n}$ satisfies the constraint that $(x_t, y_t) \in E$, and
- 2. each adjacent pair of configurations satisfy $x_t = x_{t+1}$ or $y_t = y_{t+1}$.

In other words, this is the reconfiguration problem which asks to decide whether the token that initially placed at the edge ($v^{\text{start}}, v^{\text{start}}$) can be moved to the edge ($v^{\text{goal}}, v^{\text{goal}}$) by a sequence of operations that move the token from an edge to one of its adjacent edges.

In Theorem 1.5, each adjacent pair of proofs differs in at most one bit. In terms of reconfiguration problems, this means that the operations which we are allowed to perform are to change *one bit* of a configuration instead of *one vertex* of the token placed at an edge. By introducing a special symbol " \perp ", we can regard SUCCINCT GRAPH REACHABILITY as the following reconfiguration problem in which operations are restricted to changing one bit of configurations: Given a (succinctly described) graph *G* and two vertices $v^{\text{start}}, v^{\text{goal}} \in V$, the task is to decide whether there exists a sequence $(x_1 \circ y_1, \dots, x_T \circ y_T) \in (\{0, 1, \bot\}^{2n})^*$ from $v^{\text{start}} \circ v^{\text{start}}$ to $v^{\text{goal}} \circ v^{\text{goal}}$ such that

- 1. $(x_t, y_t) \in E$ or $(x_t \in \{0, 1\}^n \text{ and } y_t \in \{0, 1\}^n)$, and
- 2. each adjacent pair $(x_t \circ y_t, x_{t+1} \circ y_{t+1})$ differs in at most one position.

Informally, the existence of the symbol \perp indicates that we are on the way of the transition, and we are allowed to include \perp in at most one of x_t or y_t (that is, we do not allow to change both vertices of the token simultaneously). This reconfiguration problem "simulates" SUC-CINCT GRAPH REACHABILITY in the following sense: If a token placed at an edge $(x, y_1) \in E$ is moved to another edge $(x, y_2) \in E$, then in the new reconfiguration problem, we may consider a sequence of operations that first transform $x \circ y_1$ into $x \circ \perp^n$ by replacing each bit of y_1 with \perp one by one, and then transform $x \circ \perp^n$ into $x \circ y_2$ by replacing \perp with a bit of y_2 one by one.

2.2 PCRP System for SUCCINCT GRAPH REACHABILITY

The main idea for constructing a PCRP for SUCCINCT GRAPH REACHABILITY is to probabilistically check item 1, i.e., the condition that $(x_t, y_t) \in E$ or $(x_t \in \{0, 1\}^n \text{ and } y_t \in \{0, 1\}^n)$, by reading a constant number of bits. To this end, we encode each vertex by a locally testable error-correcting code Enc: $\{0, 1\}^n \to \{0, 1\}^\ell$ and use the PCPP to check whether the encoded pair of vertices (x_t, y_t) satisfies $(x_t, y_t) \in E$. Specifically, let V_{PCPP} be a PCPP verifier for the language $L_G = \{\text{Enc}(x) \circ \text{Enc}(y) \mid (x, y) \in E\}$. This verifier takes random access to $f \circ g$ and a proof $\pi \in \{0, 1\}^p$ and checks whether $f \circ g$ is close to some $\text{Enc}(x) \circ \text{Enc}(y) \in L_G$. Then, given a sequence of proofs $(\sigma^{(1)}, \dots, \sigma^{(T)}) \in (\{0, 1, \bot\}^{2\ell + p})^*$, we probabilistically check each proof $\sigma^{(t)} = f \circ g \circ \pi$ as follows:

- 1. Using the local tester for Enc, we check that both f and g are close to some codewords Enc(x) and Enc(y), respectively. If both are far from codewords, then we reject.
- 2. By random sampling, we test whether either f or g contains many \perp symbols. If so, we accept.
- 3. Finally, by running V_{PCPP} for $(f \circ g, \pi)$, we check that $(x, y) \in E$. We accept if and only if V_{PCPP} accepts.

The first item ensures that either f or g is close to some codewords Enc(x) and Enc(y). The second item checks whether we are on the way of the transition from one edge to another, in which case we accept. We run the test of the third item only if either f or g is close to some codewords, and both f and g do not contain many \bot symbols. Using the PCPP, we check that f and g encode x and y such that $(x, y) \in E$.

We note that the size of alphabets $\{0, 1, \bot\}$ of the PCRP system is 3. This can be reduced to 2 by using a simple alphabet reduction of Ohsaka [Ohs23b], which transforms any PCRP system with *perfect completeness* over alphabets of constant size into a PCRP system over the binary alphabets $\{0, 1\}$.

It is thus important to make sure that the PCRP system has perfect completeness, i.e., in the YES case, the verifier accepts with probability 1. For this reason, in the actual proof, we need to modify the PCPP verifier V_{PCPP} so that it immediately accepts if a \perp symbol in f or g is queried by V_{PCPP} . Details can be found in Section 5.

3 Related Work

Another characterization of PSPACE is *probabilistically checkable debate systems* due to Condon, Feigenbaum, Lund, and Shor [CFLS95], which can be used to show PSPACE-hardness of approximating QUANTIFIED BOOLEAN FORMULA and the problem of selecting as many finite-state automata as possible that accept a common string. These results are incomparable to our PCRP because the underlying structure of the problems is different from each other.

We summarize approximate variants of reconfiguration problems whose inapproximability was investigated. MAXMIN CLIQUE RECONFIGURATION and MAXMIN SAT RECON-FIGURATION are NP-hard to approximate [IDHPSUU11]. SHORTEST PATH RECONFIGU-RATION is PSPACE-hard to approximate with respect to its objective value [GJKL22]. The obejctive value called the *price* is determined based on the number of vertices in a path changed at a time, which is fundamentally different from those of reconfiguration problems listed in Corollary 1.6. SUBMODULAR RECONFIGURATION is constant-factor inapproximable [OM22], whose proof resorts to inapproximability results of SUBMODULAR FUNC-TION MAXIMIZATION [FMV11].

We note that approximability of reconfiguration problems frequently refers to that of *the shortest sequence* [BHIKMMSW20, IKKKO22, KMM11, MNORTU16], which seems orthogonal to the present study.

The *pebble game* [PH70] is a single-player game, which models the trade-off between the memory usage and running time of a computation and is recently used in the context of proof complexity [Nor13]. This game can be thought of as a reconfiguration problem, whose objective function, called the *pebbling price*, is defined as the maximum number of pebbles at any time required to place a pebble to the unique sink. The pebbling price is known to be PSPACE-hard to approximate within an additive $n^{\frac{1}{3}-\varepsilon}$ term for the graph size n [CLNV15, DL17]. We leave open whether our PCRPs can be used to derive PSPACEhardness of approximating the pebbling price within a multiplicative factor.

4 Preliminaries

4.1 Notations

For a nonnegative integer $n \in \mathbb{N}$, let $[n] := \{1, 2, ..., n\}$. A sequence \mathscr{C} of a finite number of elements $E^{(1)}, ..., E^{(T)}$ is denoted by $(E^{(1)}, ..., E^{(T)})$, and we write $E \in \mathscr{C}$ to indicate that E appears in \mathscr{C} . The symbol \circ stands for a concatenation of two strings. Let Σ be a finite set called *alphabet*. For a length-*n* string $f \in \Sigma^n$ and index set $I \subseteq [n]$, we use $f|_I$ to denote the restriction of f to I. We write 0^n and 1^n for $\underbrace{0 \cdots 0}_{n \text{ times}}$ and $\underbrace{1 \cdots 1}_{n \text{ times}}$, respectively. The *relative distance* between two strings $f, g \in \Sigma^n$, denoted $\Delta(f, g)$, is defined as the fraction of positions

on which *f* and *g* differ; namely, $\Delta(f,g) = \mathbb{P}\mathbb{F}_{i\sim[n]}[f_i \neq g_i] = \frac{|\{i\in[n]|f_i\neq g_i\}|}{n}$. We say that *f* is ε -close to *g* if $\Delta(f,g) \leq \varepsilon$ and ε -far from *g* if $\Delta(f,g) > \varepsilon$. For a set of strings $S \subseteq \Sigma^n$, analogous notions are defined; e.g., $\Delta(f,S) \coloneqq \min_{g\in S} \Delta(f,g)$ and *f* is ε -close to *S* if $\Delta(f,S) \leq \varepsilon$.

4.2 Error-Correcting and Locally Testable Codes

Here, we introduce error-correcting and locally testable codes.

Definition 4.1 (Error-correcting codes). For any $\rho \in [0,1]$, a function Enc: $\{0,1\}^n \to \{0,1\}^\ell$ is an *error-correcting code with relative distance* ρ if $\Delta(\text{Enc}(\alpha), \text{Enc}(\beta)) > \rho$ for every $\alpha \neq \beta \in \{0,1\}^n$. We call Enc(α) for each $\alpha \in \{0,1\}^n$ a *codeword* of Enc. Denote by Enc(\cdot) the set of all codewords of Enc.

Definition 4.2 (Locally testable codes; e.g., Goldreich and Sudan [GS06]). For any $q \in \mathbb{N}$ and $\kappa > 0$, an error-correcting code Enc: $\{0,1\}^n \to \{0,1\}^\ell$ is said to be (q,κ) -locally testable if there exists a probabilistic polynomial-time algorithm M that, given oracle access to a string $f \in \{0,1\}^\ell$, makes at most q nonadaptive queries of f and satisfies the following conditions:

- (Completeness) If $f \in Enc(\cdot)$, then *M* always accepts; namely, $\mathbb{Pr}[M^f \text{ accepts}] = 1$.
- (Soundness) If $f \notin Enc(\cdot)$, then M rejects with probability at least $\kappa \cdot \Delta(f, Enc(\cdot))$; namely, $\mathbb{Pr}[M^f \text{ rejects}] \ge \kappa \cdot \Delta(f, Enc(\cdot)).$

Such an algorithm *M* is called a (q, κ) -local tester for Enc.

Theorem 4.3 ([BGHSV06, BSVW03]). There exist $\rho, \kappa > 0$ and $q \in \mathbb{N}$ such that for infinitely many n's, there exists a polynomial-time construction of a (q, κ) -locally testable errorcorrecting code $\operatorname{Enc}_n: \{0,1\}^n \to \{0,1\}^{\ell(n)}$ with relative distance ρ and $\ell(n) = n^{1+o(1)}$. Moreover, if the code $\operatorname{Enc}_n: \{0,1\}^n \to \{0,1\}^{\ell(n)}$ of the desired property exists for integer $n \in \mathbb{N}$, then the next integer $n' \in \mathbb{N}$ for which $\operatorname{Enc}_{n'}: \{0,1\}^{n'} \to \{0,1\}^{\ell(n')}$ exists is at most $n^{1+o(1)}$.

4.3 Probabilistically Checkable Proofs of Proximity

We formally define the notion of verifier.

Definition 4.4 (Verifier). A *verifier* with *randomness complexity* $r: \mathbb{N} \to \mathbb{N}$ and *query complexity* $q: \mathbb{N} \to \mathbb{N}$ is a probabilistic polynomial-time algorithm V that given an input $x \in \{0,1\}^*$, tosses r = r(|x|) random bits R and use R to generate a sequence of q = q(|x|) queries $I = (i_1, \ldots, i_q)$ and a circuit $D: \{0,1\}^q \to \{0,1\}$. We write $(I,D) \sim V(x)$ to denote the random variable for a pair of the query sequence and circuit generated by V on input $x \in \{0,1\}^*$. Denote by $V^{\pi}(x) := D(\pi|_I)$ the output of V on input x given oracle access to a proof $\pi \in \{0,1\}^*$. We say that V(x) accepts a proof π if $V^{\pi}(x) = 1$; i.e., $D(\pi|_I) = 1$ for $(I,D) \sim V(x)$.

We proceed to the definition of *PCPs of proximity* [BGHSV06] (a.k.a. *assignment testers* [DR06]). For any pair language $L \subseteq \{0,1\}^* \times \{0,1\}^*$, we define $L(x) := \{y \in \{0,1\}^* \mid (x,y) \in L\}$ for an input $x \in \{0,1\}^*$.

Definition 4.5 (PCP of proximity [BGHSV06, DR06]). A *PCP of proximity* (PCPP) verifier for a pair language $L \subseteq \{0, 1\}^* \times \{0, 1\}^*$ with *proximity parameter* $\delta \in (0, 1)$ and *soundness error* $s \in (0, 1)$ is a verifier *V* such that for every pair of an explicit input $x \in \{0, 1\}^*$ and an implicit input oracle $y \in \{0, 1\}^*$, the following conditions hold:

• (Completeness) If $(x, y) \in L$, there exists a proof $\pi \in \{0, 1\}^*$ such that V(x) accepts $y \circ \pi$ with probability 1; namely,

$$\exists \pi \in \{0,1\}^*, \quad \Pr_{(I,D) \sim V(x)} \Big[D((y \circ \pi)|_I) = 1 \Big] = 1.$$
(4.1)

• (Soundness) If y is δ -far from L(x), for every alleged proof $\pi \in \{0, 1\}^*$, V(x) accepts $y \circ \pi$ with probability less than s; namely,

$$\forall \pi \in \{0,1\}^*, \quad \Pr_{(I,D) \sim V(x)} \Big[D((y \circ \pi)|_I) = 1 \Big] < s.$$
(4.2)

Subsequently, we introduce smooth PCPP verifiers. We say that a verifier is *smooth* if each position in its proof is equally likely to be queried.

Definition 4.6 (Smoothness). A PCPP verifier *V* is *smooth* if *V* queries each position in implicit input *y* and proof π with equal probability; namely, there exists $p \in (0, 1)$ such that

$$p = \Pr_{(I,D) \sim V(x)} \left[i \in I \right]$$
(4.3)

for every position *i* of $y \circ \pi$.

Theorem 4.7 (Smooth PCPP [BGHSV06, Par21]). For every pair language L in NP and every number $\delta \in (0, 1)$, there exists a smooth PCPP verifier V for L with randomness complexity $r(n) = O(\log n)$, query complexity q(n) = O(1), proximity parameter δ , and soundness error $s = 1 - \Omega(\delta)$. Moreover, for every pair $(x, y) \in L$, a proof $\pi \in \{0, 1\}^{\text{poly}(n)}$ such that V(x)always accepts $y \circ \pi$ can be constructed in polynomial time.

4.4 Constraint Satisfaction Problems

Here, we review constraint satisfaction problems.

Definition 4.8. A *q*-ary constraint system over variable set N and alphabet Σ is defined as a collection of *q*-ary constraints, $\Psi = (\psi_j)_{j \in [m]}$, where each constraint $\psi_j \colon \Sigma^N \to \{0, 1\}$ depends on *q* variables of N; namely, there exist $i_1, \ldots, i_q \in N$ and $f \colon \Sigma^q \to \{0, 1\}$ such that $\psi_j(A) = f(A(i_1), \ldots, A(i_q))$ for every $A \colon N \to \Sigma$.

For an assignment $A: N \to \Sigma$, we say that A satisfies constraint ψ_j if $\psi_j(A) = 1$, and A satisfies Ψ if it satisfies all constraints of Ψ . Moreover, we say that Ψ is satisfiable if Ψ is satisfied by some assignment. For an assignment $A: N \to \Sigma$, its value is defined as the fraction of constraints of Ψ satisfied by A; namely,

$$\operatorname{val}_{\Psi}(A) \coloneqq \frac{1}{|\Psi|} \cdot \left| \left\{ \psi_j \in \Psi \mid A \text{ satisfies } \psi_j \right\} \right|.$$

$$(4.4)$$

We refer to the equivalence between a PCP system and GAP q-CSP (see, e.g., [AB09, Section 11.3]), whose proof is included for the sake of completeness.

Proposition 4.9. Let V be a verifier with randomness complexity $O(\log n)$, query complexity O(1), and alphabet Σ , and let $x \in \{0,1\}^*$ be an input. Then, one can construct in polynomial time a constraint system $\Psi = (\psi_j)_{j \in [m]}$ over poly(|x|) variables and alphabet Σ such that $\operatorname{val}_{\Psi}(\pi) = \Pr[V^{\pi}(x) = 1]$ for every proof $\pi \in \Sigma^{\operatorname{poly}(|x|)}$.

On the other hand, for a q-ary constraint system Ψ over variable set N and alphabet Σ , one can construct in polynomial time a verifier V with randomness complexity $O(\log n)$, query complexity O(1), and alphabet Σ such that $\mathbb{Pr}[V^A = 1] = \operatorname{val}_{\Psi}(A)$ for every assignment $A: N \to \Sigma$.

Proof. Let *V* be a verifier with randomness complexity r(n) = O(n), query complexity $q(n) = q \in \mathbb{N}$, and alphabet Σ . Given an input $x \in \{0, 1\}^*$, we can assume the proof length for *V* to be poly(|x|). We construct a *q*-ary constraint system Ψ over variable set N := [poly(|x|)] and alphabet Σ as follows:

- for every possible sequence $R \in \{0,1\}^{r(|x|)}$ of r(|x|) random bits, we run V(x) to generate a query sequence $I_R = (i_1, \dots, i_q)$ and a circuit $D_R : \Sigma^q \to \{0,1\}$ in polynomial time.
- create a new constraint ψ_i such that

$$\psi_j(A) \coloneqq D_R(A(i_1), \dots, A(i_q)) \tag{4.5}$$

for every assignment $A: N \to \Sigma$.

Note that the construction of Ψ completes in polynomial time; in particular, the size of Ψ is polynomial in |x|. Observe that for any proof $\pi \in \Sigma^N$, which can be thought of as an assignment to Ψ ,

$$\operatorname{val}_{\Psi}(\pi) = \Pr_{\psi_j \sim \Psi} \left[\psi_j(\pi) = 1 \right] = \Pr_{(I,D) \sim V(S)} \left[D(\pi|_I) = 1 \right] = \operatorname{Pr} \left[V^{\pi}(S) = 1 \right], \tag{4.6}$$

completing the proof of the first statement. The second statement is omitted as can be shown similarly. $\hfill \square$

5 Probabilistic Checkable Reconfiguration Proofs

In this section, we prove the main result of this paper, i.e., a PCRP verifier for a PSPACEcomplete reconfiguration problem. For any pair of proofs $\pi^{\text{start}}, \pi^{\text{goal}} \in \Sigma^n$, a *reconfiguration sequence from* π^{start} *to* π^{goal} *over* Σ^n is a sequence $(\pi^{(1)}, \ldots, \pi^{(T)}) \in (\Sigma^n)^*$ such that $\pi^{(1)} = \pi^{\text{start}}, \pi^{(T)} = \pi^{\text{goal}}$, and $\pi^{(t+1)}$ differ in at most one symbol for every $t \in [T-1]$.

Theorem 5.1 (Probabilistic Checkable Reconfiguration Proof (PCRP)). A language L is in PSPACE if and only if there exists a verifier V with randomness complexity $r(n) = O(\log n)$ and query complexity q(n) = O(1), coupled with polynomial-time computable proofs $\pi^{\text{start}}, \pi^{\text{goal}}$: $\{0,1\}^* \to \{0,1\}^*$ such that the following hold for every input $x \in \{0,1\}^*$:

• (Completeness) If $x \in L$, there exists a reconfiguration sequence $\pi = (\pi^{(1)}, \dots, \pi^{(T)})$ from $\pi^{\text{start}}(x)$ to $\pi^{\text{goal}}(x)$ over $\{0, 1\}^{\text{poly}(n)}$ such that V(x) accepts every proof with probability 1; namely,

$$\forall t \in [T], \ \mathbb{Pr}\left[V^{\pi^{(t)}}(x) = 1\right] = 1.$$
 (5.1)

• (Soundness) If $x \notin L$, every reconfiguration sequence $\pi = (\pi^{(1)}, \dots, \pi^{(T)})$ from $\pi^{\text{start}}(x)$ to $\pi^{\text{goal}}(x)$ over $\{0, 1\}^{\text{poly}(n)}$ includes a proof that is rejected by V(x) with probability more than $\frac{1}{2}$; namely,

$$\exists t \in [T], \ \mathbb{P}\mathbb{P}\Big[V^{\pi^{(t)}}(x) = 1\Big] < \frac{1}{2}.$$
(5.2)

5.1 **PSPACE-completeness of SUCCINCT GRAPH REACHABILITY**

We first introduce a canonical PSPACE-complete problem called SUCCINCT GRAPH REACH-ABILITY, for which we design a PCRP system.

Problem 5.2. For a polynomial-size circuit $S: \{0,1\}^n \to \{0,1\}^n$ promised that $S(1^n) = 1^n$, SUCCINCT GRAPH REACHABILITY requests to decide if there is a sequence of a finite number of assignments, $(\alpha^{(1)}, \ldots, \alpha^{(T)})$, from 0^n to 1^n such that $\alpha^{(t)} = \alpha^{(t+1)}$, $S(\alpha^{(t)}) = \alpha^{(t+1)}$, or $S(\alpha^{(t+1)}) = \alpha^{(t)}$ for all $t \in [T-1]$.

Similar variants were formulated previously, e.g., [GW83, PY86]. PSPACE-completeness of SUCCINCT GRAPH REACHABILITY is shown below for the sake of completeness.

Proposition 5.3. SUCCINCT GRAPH REACHABILITY is PSPACE-complete.

Proof. For the sake of convenience, we first show that a circuit $S: \{0,1\}^n \to \{0,1\}^n$ is a YES instance of SUCCINCT GRAPH REACHABILITY if and only if

$$\exists m \in \mathbb{N} \text{ such that } \underbrace{S \circ \cdots \circ S}_{m \text{ times}} (0^n) = 1^n.$$
(5.3)

The "if" direction is obvious because whenever $\underbrace{S \circ \cdots \circ S}_{m \text{ times}}(0^n) = 1^n$, the sequence $(\alpha^{(1)}, \dots, \alpha^{(m+1)})$ such that $\alpha^{(t)} = \underbrace{S \circ \cdots \circ S}_{m \text{ times}}(0^n)$ for all $t \in [m+1]$ satisfies the desired property. Suppose

such that $\alpha^{(t)} = \underbrace{S \circ \cdots \circ S}_{t-1 \text{ times}}(0^n)$ for all $t \in [m+1]$ satisfies the desired property. Suppose we have a sequence $(\alpha^{(1)}, \ldots, \alpha^{(T)})$ from 0^n to 1^n such that $\alpha^{(t)} = \alpha^{(t+1)}$, $S(\alpha^{(t)}) = \alpha^{(t+1)}$, or $S(\alpha^{(t+1)}) = \alpha^{(t)}$. It is not hard to see that for each t, $\underbrace{S \circ \cdots \circ S}_{m_t \text{ times}}(\alpha^{(t)}) = 1^n$ for some $m_t \in \mathbb{N}$; in

particular, this is the case for $a^{(1)} = 0^n$, completing the "only if" direction.

We now show the PSPACE-completeness of SUCCINCT GRAPH REACHABILITY. Membership in PSPACE follows from the fact that SUCCINCT GRAPH REACHABILITY \in NPSPACE and Savitch's theorem [Sav70]. Consider the following PSPACE-complete problem: Given a deterministic Turing machine M, input $x \in \{0,1\}^*$, and 1^n , does M accept x in space n? Note that all possible configurations of M having space n on input x are specified by $\{0,1\}^{\alpha n}$ for some constant α depending on M. Let $c_{init} \in \{0,1\}^{\alpha n}$ denote the initial configuration of M on input x, and $M(x,c) \in \{0,1\}^{\alpha n}$ denote the next configuration of M following $c \in \{0,1\}^{\alpha n}$. Define now a circuit $S: \{0,1\}^{2+\alpha n} \to \{0,1\}^{2+\alpha n}$ of polynomial size (in |M|, |x|, and n) as follows:

$$S(00 \circ 0^{\alpha n}) \coloneqq 01 \circ c_{\text{init}},$$

$$S(01 \circ c) \coloneqq \begin{cases} 11 \circ 1^{\alpha n} & \text{if } c \text{ is any accepting configuration,} \\ 00 \circ 0^{\alpha n} & \text{if } c \text{ is any rejecting configuration,} \\ 01 \circ M(x, c) & \text{otherwise,} \end{cases}$$

$$S(11 \circ 1^{\alpha n}) \coloneqq 11 \circ 1^{\alpha n}.$$
(5.4)

Observe easily that $\underbrace{S \circ \cdots \circ S}_{m \text{ times}}(0^{2+\alpha n}) = 1^{2+\alpha n}$ for some $m \in \mathbb{N}$ if and only if M accepts x in space n, completing the proof.

5.2 PCRP System for SUCCINCT GRAPH REACHABILITY

Here, we will construct a PCRP system for SUCCINCT GRAPH REACHABILITY. We first encode the assignment to a circuit by an error-correcting code. For a polynomial-size circuit $S: \{0,1\}^n \to \{0,1\}^n$, let Enc: $\{0,1\}^n \to \{0,1\}^{\ell(n)}$ be an $(O(1),\kappa)$ -locally testable error-correcting code with relative distance ρ such that $\rho \in (0,1)$, $\kappa \in \mathbb{N}$, and $\ell(n) = n^{1+o(1)}$ by Theorem 4.3.² Let M be an $(O(1),\kappa)$ -local tester for Enc. Consider the following pair language $L_{ckt} \subseteq \{0,1\}^* \times$ $\{0,1\}^*$: Given a polynomial-size circuit $S: \{0,1\}^n \to \{0,1\}^n$ and two strings $f,g \in \{0,1\}^{\ell(n)}$, we define $(S, f \circ g) \in L_{ckt}$ if and only if $f = \text{Enc}(\alpha)$ and $g = \text{Enc}(\beta)$ for a pair $\alpha, \beta \in \{0,1\}^n$ such that $\alpha = \beta$, $S(\alpha) = \beta$, or $S(\beta) = \alpha$. Intuitively, L_{ckt} determines the adjacency relation of codewords of Enc with respect to S. Observe easily that L_{ckt} is in NP, and by Proposition 5.3, S is a YES instance of SUCCINCT GRAPH REACHABILITY if and only if there exists a sequence of

²Without loss of generality, we can assume Theorem 4.3 holds for given *n* because we can find $n' = n^{1+o(1)}$ for which Theorem 4.3 holds and construct a slightly larger circuit $S': \{0,1\}^{n'} \to \{0,1\}^{n'}$ such that S' is a YES instance if and only if so is S.

strings, $(f^{(1)}, \ldots, f^{(T)})$, from $\text{Enc}(0^n)$ to $\text{Enc}(1^n)$ such that $(S, f^{(t)} \circ f^{(t+1)}) \in L_{\text{ckt}}$ for all $t \in [T-1]$. We will use $L_{\text{ckt}}(S)$ to denote the set of all strings $f \circ g \in \{0, 1\}^{2\ell(n)}$ such that $(S, f \circ g) \in L_{\text{ckt}}$; namely,

$$L_{\rm ckt}(S) \coloneqq \left\{ f \circ g \in \{0,1\}^{2\ell(n)} \middle| (S,f \circ g) \in L_{\rm ckt} \right\}$$

= $\left\{ {\rm Enc}(\alpha) \circ {\rm Enc}(\beta) \middle| \alpha = \beta \lor S(\alpha) = \beta \lor S(\beta) = \alpha \right\}.$ (5.5)

Let V_{ckt} denote a smooth PCPP verifier for L_{ckt} , having randomness complexity $r(n) = O(\log n)$, query complexity $q(n) = q \in \mathbb{N}$, proximity parameter $\delta_{ckt} \coloneqq \frac{\rho}{4} \in (0, 1)$, and soundness error $s_{ckt} \coloneqq 1 - \Omega(\delta_{ckt}) \in (0, 1)$ obtained by Theorem 4.7. Note that the proof length can be bounded by some polynomial p(n) in the input length n. Hereafter, we use a new symbol " \perp " that is neither 0 nor 1. We will write $f \circ g \circ \pi$ for a string provided to $V_{ckt}(S)$, where $f \circ g \in \{0, 1, \perp\}^{\ell(n)} \times \{0, 1, \perp\}^{\ell(n)}$ and $\pi \in \{0, 1, \perp\}^{p(n)}$ is an alleged proof.

5.2.1 Verifier Description

Our verifier V is given a polynomial-size circuit $S: \{0,1\}^n \to \{0,1\}^n$ and oracle access to $f \circ g \circ \pi \in \{0,1,\bot\}^{2\ell(n)+p(n)}$, and is designed as follows:

- 1. V ensures that f or g must be a codeword of Enc by running the local tester M on f and g separately. Note that M rejects whenever it reads \perp at least once, which still ensures that $\Pr[M^f \text{ rejects}] \ge \kappa \cdot \Delta(f, \operatorname{Enc}(\cdot))$.
- 2. V allows $f \circ g$ to contain \bot , enabling f or g to transform between different codewords of Enc. Specifically, V accepts with probability equal to the fraction of \bot in f or g, which can be done by testing whether $f_i = \bot$ or $g_j = \bot$ for independently and uniformly chosen $i, j \in [\ell(n)]$. During $f = \bot^n$ or $g = \bot^n$, the contents of π can be modified arbitrarily without being rejected, which is essential in the perfect completeness (Lemma 5.4).
- 3. On the other hand, if neither f nor g contains "many" \perp 's, V expects $f \circ g$ to be close to $L_{\text{ckt}}(S)$; thus, it wants to execute the smooth PCPP verifier $V_{\text{ckt}}(S)$, whose behavior is, however, undefined if $f \circ g \circ \pi$ contains \perp . Instead, we run a *modified verifier* $V'_{\text{ckt}}(S)$, which accepts if and only if $(f \circ g)|_I$ contains \perp or $(\pi|_I \text{ does not contain } \perp \text{ and } D((f \circ g \circ \pi)|_I) = 1)$ for $(I,D) \sim V_{\text{ckt}}(S)$.³ This test is crucial for proving the soundness (Lemma 5.5).

The precise pseudocode of V(S) is presented below.

³In the latter case, we can safely assume that $(f \circ g)|_I$ does not contain \bot .

Verifier $V^{f \circ g \circ \pi}(S)$ using local tester *M* for Enc and smooth PCPP verifier V_{ckt} for L_{ckt} . **Input:** a polynomial-size circuit $S: \{0,1\}^n \rightarrow \{0,1\}^n$. **Oracle access:** strings $f, g \in \{0, 1, \bot\}^{\ell(n)}$ representing an implicit input and a proof $\pi \in$ $\{0,1,\bot\}^{p(n)}.$ 1: run local tester *M* on *f* and *g*. \triangleright *M* declares reject if it reads \perp . 2: **if** both runs of *M* declare reject **then** reject. 3: 4: pick $i \sim [\ell(n)]$ and $j \sim [\ell(n)]$ independently and uniformly. 5: **if** $f_i \neq \bot$ and $g_i \neq \bot$ **then** \triangleright run a modified PCPP verifier $V'_{ckt}(S)$. 6: < execute PCPP verifier $V_{\text{ckt}}(S)$ to generate a query sequence $I = (i_1, \ldots, i_q)$ and a 7: circuit $D: \{0, 1\}^q \to \{0, 1\}.$ **if** $(f \circ g)|_I$ contains \perp **then** 8: accept. 9: else if $\pi|_I$ does not contain \perp and $D((f \circ g \circ \pi)|_I) = 1$ then 10: accept. 11: else 12:reject. 13:14: **else** accept. 15:

For any two strings $\alpha, \beta \in \{0,1\}^n$ such that $\operatorname{Enc}(\alpha) \circ \operatorname{Enc}(\beta) \in L_{\operatorname{ckt}}(S)$, let $\Pi(\alpha, \beta) \in \{0,1\}^{p(n)}$ denote a polynomial-time computable proof that makes $V_{\operatorname{ckt}}(S)$ to accept $\operatorname{Enc}(\alpha) \circ \operatorname{Enc}(\beta) \circ \Pi(\alpha, \beta)$ with probability 1. Note that $\operatorname{Enc}(0^n) \circ \operatorname{Enc}(0^n) \circ \Pi(0^n, 0^n)$ and $\operatorname{Enc}(1^n) \circ \operatorname{Enc}(1^n) \circ \Pi(1^n, 1^n)$ are accepted by V(S) with probability 1.

5.2.2 Completeness and Soundness

We now prove the completeness and soundness. Define $\sigma^{\text{start}} := \text{Enc}(0^n) \circ \text{Enc}(0^n) \circ \Pi(0^n, 0^n) \in \{0, 1\}^{2\ell(n)+p(n)}$ and $\sigma^{\text{goal}} := \text{Enc}(1^n) \circ \text{Enc}(1^n) \circ \Pi(1^n, 1^n) \in \{0, 1\}^{2\ell(n)+p(n)}$. Let $\text{val}_V(S)$ denote the maximum possible value of

 $\min_{\sigma^{(t)} \in \sigma} \Pr\left[V(S) \text{ accepts } \sigma^{(t)}\right]$ (5.6)

over all possible reconfiguration sequences $\sigma = (\sigma^{(1)}, \dots, \sigma^{(T)})$ from σ^{start} to σ^{goal} . The perfect completeness ensures that $\text{val}_V(S) = 1$ if S is a YES instance, while the soundness guarantees that $\text{val}_V(S) < 1 - \delta$ for some $\delta \in (0, 1)$ if S is a NO instance.

We first show the completeness.

Lemma 5.4. Suppose a circuit $S: \{0,1\}^n \to \{0,1\}^n$ is a YES instance of SUCCINCT GRAPH REACHABILITY. Then, there exists a reconfiguration sequence σ from σ^{start} to σ^{goal} over $\{0,1,\bot\}^{2\ell(n)+p(n)}$ such that V(S) accepts any proof in σ with probability 1.

Proof. It suffices to show that for any $\alpha \neq \beta \in \{0,1\}^n$ such that $\alpha = S(\beta)$ or $\beta = S(\alpha)$ (i.e., $Enc(\alpha) \circ Enc(\beta) \in L_{ckt}(S)$), there is a reconfiguration sequence σ from $Enc(\alpha) \circ Enc(\alpha) \circ Inc(\alpha, \alpha)$

to $\text{Enc}(\alpha) \circ \text{Enc}(\beta) \circ \Pi(\alpha, \beta)$ such that V(S) accepts any proof in σ with probability 1. Such a reconfiguration sequence is obtained by the following procedure:

- Reconfiguration σ from $\operatorname{Enc}(\alpha) \circ \operatorname{Enc}(\alpha) \circ \Pi(\alpha, \alpha)$ to $\operatorname{Enc}(\alpha) \circ \operatorname{Enc}(\beta) \circ \Pi(\alpha, \beta)$. 1: \triangleright start from $\operatorname{Enc}(\alpha) \circ \operatorname{Enc}(\alpha) \circ \Pi(\alpha, \alpha)$. 2: change the second string from $\operatorname{Enc}(\alpha)$ to $\bot^{\ell(n)}$ one by one. 3: \triangleright obtain $\operatorname{Enc}(\alpha) \circ \bot^{\ell(n)} \circ \Pi(\alpha, \alpha)$. 4: change the proof from $\Pi(\alpha, \alpha)$ to $\Pi(\alpha, \beta)$ one by one. 5: \triangleright obtain $\operatorname{Enc}(\alpha) \circ \bot^{\ell(n)} \circ \Pi(\alpha, \beta)$. 6: change the second string from $\bot^{\ell(n)}$ to $\operatorname{Enc}(\beta)$. 7: \triangleright end at $\operatorname{Enc}(\alpha) \circ \operatorname{Enc}(\beta) \circ \pi^{(\alpha, \beta)}$.

By the following case analysis, V(S) turns out to accept every intermediate proof $f \circ g \circ \pi$ with probability 1, as desired.

- (Line 2) f ∘ g ∘ π is obtained from Enc(α) ∘ Enc(α) ∘ Π(α, α) by replacing some symbols of the second Enc(α) by ⊥. Observe that the local tester *M* always accepts f = Enc(α). We show that V'_{ckt}(S) always accepts f ∘ g ∘ π. Let (I,D) ~ V_{ckt}(S). If (f ∘ g)|_I contains ⊥, V'_{ckt}(S) accepts. Otherwise, since π = Π(α, α) does not contain ⊥, it holds that (f ∘ g ∘ π)|_I = (Enc(α) ∘ Enc(α) ∘ Π(α, α))|_I, implying D((f ∘ g ∘ π)|_I) = 1.
- (Line 4) $f \circ g \circ \pi$ has a form of $Enc(\alpha) \circ \perp^{\ell(n)} \circ \pi$ for some $\pi \in \{0, 1, \perp\}^{p(n)}$. The local tester M always accepts $Enc(\alpha)$, and V(S) would not have run the modified verifier $V'_{ckt}(S)$; i.e., V(S) always accepts $f \circ g \circ \pi$.
- (Line 6) $f \circ g \circ \pi$ is obtained from $Enc(\alpha) \circ Enc(\beta) \circ \Pi(\alpha, \beta)$ by replacing some symbols of $Enc(\beta)$ by \perp . Similarly to the first case, we can show that V(S) always accepts $f \circ g \circ \pi$.

We then show the soundness.

Lemma 5.5. Suppose a circuit $S: \{0,1\}^n \to \{0,1\}^n$ is a NO instance of SUCCINCT GRAPH REACHABILITY. Then, for any reconfiguration sequence σ from σ^{start} to σ^{goal} over $\{0,1,\bot\}^{2\ell(n)+p(n)}$, σ includes a proof that is rejected by V(S) with probability at least

$$\min\left\{(\kappa\varepsilon)^2, (1-\varepsilon)^2 \cdot \frac{1-s_{\rm ckt}}{2}\right\}, \text{ where } \varepsilon \coloneqq \min\left\{\frac{1-s_{\rm ckt}}{2q}, \frac{\rho}{3}\right\}.$$
(5.7)

By Lemmas 5.4 and 5.5, we can complete the proof of Theorem 5.1.

Proof of Theorem 5.1. We first prove the "only if" direction. Since SUCCINCT GRAPH REACH-ABILITY is PSPACE-complete, it is sufficient to create its verifier V and polynomial-time computable proofs π^{start} and π^{goal} . The verifier V is described in Section 5.2.1. For a polynomial-size circuit $S: \{0,1\}^n \to \{0,1\}$, the number of queries that V makes is bounded by $2 \cdot (\# \text{ queries of } M) + 2 + (\# \text{ queries of } V_{\text{ckt}}) = O(1)$, and the number of random bits that V uses is bounded by $2 \cdot (\text{random bits of } M) + 2 \cdot (\log \ell(n)) + (\text{random bits of } V_{\text{ckt}}) = O(\log n)$. We define $\pi^{\text{start}} := \text{Enc}(0^n) \circ \text{Enc}(0^n) \circ \Pi(0^n, 0^n)$ and $\pi^{\text{goal}} := \text{Enc}(1^n) \circ \text{Enc}(1^n) \circ \Pi(1^n, 1^n)$, which are polynomial-time computable.

We reduce the alphabet size of V from three (i.e., $\{0, 1, \bot\}$) to two. Using Proposition 4.9, we first convert V(S) into a constraint system $\Psi = (\psi_j)_{j \in [m]}$ over alphabet $\{0, 1, \bot\}$ such that $\mathbb{Pr}[V(S) \text{ accepts } \pi]$ is equal to $\operatorname{val}_{\Psi}(\pi)$ for any proof $\pi \in \{0, 1, \bot\}^{\operatorname{poly}(n)}$. By [Ohs23b], we obtain a constraint system $\Psi' = (\psi'_j)_{j \in [m']}$ over alphabet $\{0, 1\}$ and its two satisfying assignments A^{start} and A^{goal} such that $\operatorname{val}_{\Psi}(\pi^{\operatorname{start}} \leftrightarrow \pi^{\operatorname{goal}}) = 1$ implies $\operatorname{val}_{\Psi'}(A^{\operatorname{start}} \leftrightarrow A^{\operatorname{goal}}) = 1$, and $\operatorname{val}_{\Psi}(\pi^{\operatorname{start}} \leftrightarrow \pi^{\operatorname{goal}}) < 1 - \varepsilon$ implies $\operatorname{val}_{\Psi'}(A^{\operatorname{start}} \leftrightarrow A^{\operatorname{goal}}) < 1 - \Omega(\varepsilon)$. Using Proposition 4.9 again, we convert Ψ' into a verifier V' with randomness complexity $O(\log n)$, query complexity O(1), and alphabet $\{0, 1\}$ such that $\mathbb{Pr}[V' \operatorname{accepts} \pi']$ is equal to $\operatorname{val}_{\Psi'}(\pi')$ for any proof $\pi' \in \{0, 1\}^{\operatorname{poly}(n)}$. Consequently, if S is a YES instance, by Lemma 5.4, there exists a reconfiguration sequence \mathscr{A} from A^{start} to A^{goal} over $\{0, 1\}^{\operatorname{poly}(n)}$ such that V' accepts any proof in \mathscr{A} with probability 1, whereas if S is a NO instance, by Lemma 5.5, for any reconfiguration sequence \mathscr{A} from A^{start} to A^{goal} over $\{0, 1\}^{\operatorname{poly}(n)}$, \mathscr{A} includes a proof that is rejected by V'with probability $\Omega(1)$, which can be amplified to $\frac{1}{2}$ by a constant number of repetition, as desired.

We then prove the "if" direction. Suppose a language L admits a verifier V with randomness complexity $r(n) = O(\log n)$ and query complexity q(n) = O(1), associated with polynomialtime computable proofs π^{start} and π^{goal} . Consider then the following nondeterministic algorithm for finding a reconfiguration sequence from π^{start} to π^{goal} .

Nondeterministic polynomial-space algorithm for finding a reconfiguration sequence. —

Input: $x \in \{0, 1\}^*$.

1: compute proofs $\pi^{\text{start}}(x)$ and $\pi^{\text{goal}}(x)$ that are accepted by V with probability 1.

- 2: let $\pi^{(0)} \coloneqq \pi^{\text{start}}(x)$ and $t \leftarrow 0$.
- 3: **repeat**

if $\pi^{(t)} = \pi^{\text{goal}}(x)$ then 4: accept. 5:nondeterministically guess the next proof $\pi^{(t+1)} \in \{0,1\}^{\text{poly}(|x|)}$. 6: check if $\pi^{(t)}$ and $\pi^{(t+1)}$ differ in at most one bit, and V accepts $\pi^{(t+1)}$ with proba-7: bility 1 by enumerating all possible r(|x|) random bits. if the above test passes then 8: forget $\pi^{(t)}$ and let $t \leftarrow t + 1$. 9: else 10: reject. 11: 12: **until** $t > 2^{\text{poly}(|x|)}$ 13: reject.

The above algorithm accepts x if and only if $x \in L$. Moreover, it requires polynomial space and terminates within a finite steps; namely, $L \in NPSPACE$. By Savitch's theorem [Sav70], $L \in PSPACE$.

t	1	•••	t_1	$t_1 + 1$		$t_2 - 1$	t_2	$t_2 + 1$	•••	$t_3 - 1$	t_3	t_3+1	•••	$t_4 - 1$	t_4	$t_4 + 1$	•••	T-1	T
$\alpha^{(t)}$	0 ^{<i>n</i>}		0^n	*		*	$\alpha^{(t_2)}$	$\alpha^{(t_2)}$	•••	$\alpha^{(t_2)}$	$\alpha^{(t_2)}$	*	•••	*	1^n	1^n	•••	1^n	1^n
$\beta^{(t)}$	$ 0^n $	•••	0^n	0^n	•••	0^n	0^n	*	•••	*	$\beta^{(t_3)}$	$\beta^{(t_3)}$	•••	$\beta^{(t_3)}$	$\beta^{(t_3)}$	*	•••	*	1^n

Table 1: Illustration of Claim 5.7, which finds a sequence γ from 0^n to 1^n over $\{0,1\}^n$ using $((\alpha^{(1)},\beta^{(1)}),\ldots,(\alpha^{(T)},\beta^{(T)}))$. Colored strings are included in γ , resulting in $\gamma = (0^n,\ldots,0^n,\alpha^{(t_2)},\ldots,\alpha^{(t_2)},\beta^{(t_3)},\ldots,\beta^{(t_3)},1^n,\ldots,1^n)$. If an input circuit *S* is a NO instance, at least one of $\text{Enc}(\alpha^{(t_2)}) \circ \text{Enc}(0^n)$, $\text{Enc}(\alpha^{(t_2)}) \circ \text{Enc}(\beta^{(t_3)})$, or $\text{Enc}(\beta^{(t_3)}) \circ \text{Enc}(1^n)$ is not in $L_{\text{ckt}}(S)$.

The remainder of this section is devoted to the proof of Lemma 5.5.

Proof of Lemma 5.5. Suppose we are given a reconfiguration sequence $\sigma = (\sigma^{(1)}, \dots, \sigma^{(T)}) = (f^{(1)} \circ g^{(1)} \circ \pi^{(1)}, \dots, f^{(T)} \circ g^{(T)} \circ \pi^{(T)})$ from σ^{start} to σ^{goal} such that $\text{val}_V(S) = \min_{t \in [T]} \mathbb{Pr}[V(S) \text{ accepts } \sigma^{(t)}]$. Define

$$\varepsilon \coloneqq \min\left\{\frac{1 - s_{\text{ckt}}}{2q}, \frac{\rho}{3}\right\},\tag{5.8}$$

where q is the query complexity of V_{ckt} , s_{ckt} is the soundness error of V_{ckt} , and ρ is the relative distance of Enc. Observe that if both $f^{(t)}$ and $g^{(t)}$ for some $t \in [T]$ are ε -far from Enc(·), then M rejects each $f^{(t)}$ and $g^{(t)}$ with probability more than $\kappa \varepsilon$; namely,

$$\mathbb{Pr}\left[V(S) \text{ rejects } \sigma^{(t)}\right] \ge \mathbb{Pr}\left[M \text{ rejects } f\right] \cdot \mathbb{Pr}\left[M \text{ rejects } g\right] > (\kappa \varepsilon)^2.$$
(5.9)

Hereafter, we assume that $f^{(t)}$ or $g^{(t)}$ is ε -close to $Enc(\cdot)$ for every $t \in [T]$.

We then define Dec: $\{0,1\}^{\ell(n)} \rightarrow \{0,1\}^n \cup \{*\}$ as

$$\operatorname{Dec}(f) \coloneqq \begin{cases} \operatorname{argmin} \Delta(f, \operatorname{Enc}(\alpha)) & \text{if } f \text{ is } \varepsilon \text{-close to } \operatorname{Enc}(\cdot), \\ \alpha \in \{0,1\}^n \\ * & \text{otherwise,} \end{cases}$$
(5.10)

where * means "undefined". Using Dec, we obtain a sequence from $(0^n, 0^n)$ to $(1^n, 1^n)$, denoted $((\alpha^{(1)}, \beta^{(1)}), \dots, (\alpha^{(T)}, \beta^{(T)}))$, where $\alpha^{(t)} := \text{Dec}(f^{(t)})$ and $\beta^{(t)} := \text{Dec}(g^{(t)})$ for all $t \in [T]$. By assumption, $\alpha^{(t)}$ or $\beta^{(t)}$ must not be * for all $t \in [T]$. We claim the following (see also Table 1):

Claim 5.6. The following hold:

- (P1) $\alpha^{(t)} = \alpha^{(t+1)} \text{ or } \beta^{(t)} = \beta^{(t+1)} \text{ for each } t.$
- (P2) If $\alpha^{(t)} \neq *$ and $\alpha^{(t+1)} \neq *$, then $\alpha^{(t)} = \alpha^{(t+1)}$.

(P3) If
$$\beta^{(t)} \neq *$$
 and $\beta^{(t+1)} \neq *$, then $\beta^{(t)} = \beta^{(t+1)}$.

Proof. Suppose first $\alpha^{(t)} \neq \alpha^{(t+1)}$ and $\beta^{(t)} \neq \beta^{(t+1)}$ for some *t*. Then, $f^{(t)} \circ g^{(t)}$ and $f^{(t+1)} \circ g^{(t+1)}$ differ in at least two symbols, contradicting that σ is a reconfiguration sequence; thus, (P1) must hold.

Suppose then $\alpha^{(t)} \neq *$, $\alpha^{(t+1)} \neq *$, and $\alpha^{(t)} \neq \alpha^{(t+1)}$. Since $f^{(t)}$ and $f^{(t+1)}$ are assumed to be ε -close to Enc(·), by triangle inequality, we have

$$\Delta(f^{(t)}, f^{(t+1)}) \ge \underbrace{\Delta(\operatorname{Enc}(\alpha^{(t)}), \operatorname{Enc}(\alpha^{(t+1)}))}_{\geqslant \rho} - \underbrace{\Delta(f^{(t)}, \operatorname{Enc}(\alpha^{(t)}))}_{\leqslant \varepsilon} - \underbrace{\Delta(f^{(t+1)}, \operatorname{Enc}(\alpha^{(t+1)}))}_{\leqslant \varepsilon}$$
(5.11)
$$\ge \rho - 2\varepsilon,$$

implying that $f^{(t)}$ and $f^{(t+1)}$ differ in $(\rho - 2\varepsilon) \cdot \ell(n) \ge 2$ bits (for sufficiently large *n*), contradicting that σ is a reconfiguration sequence; thus, (P2) holds. Similarly, (P3) can be shown.

Now, we can find a *valid* sequence from 0^n to 1^n over $\{0,1\}^n$, denoted $\gamma = (\gamma^{(1)}, \dots, \gamma^{(T')})$, along a "path" over a grid $\{\alpha, \beta\} \times [T]$ by the following procedure (see also Table 1):

 \sim Sequence γ from 0^n to 1^n .

let $\alpha^{(T+1)} := 1^n$ and $\beta^{(T+1)} := 1^n$ for convenience. let $t' \leftarrow 0$ and place a token at $(\alpha, 1)$. repeat let $t' \leftarrow t' + 1$. **if** token is at (α, t) **then** let $\gamma^{(t')} \coloneqq \alpha^{(t)}$. if $\alpha^{(t+1)} \neq *$ then token goes to $(\alpha, t+1)$. else token goes to (β, t) . **if** token is at (β, t) **then** let $\gamma^{(t')} \coloneqq \beta^{(t)}$. **if** $\beta^{(t+1)} \neq *$ **then** token goes to $(\beta, t+1)$. token goes to (α, t) . else **until** token is at $(\alpha, T+1)$ or $(\beta, T+1)$ return $\gamma := (\gamma^{(1)}, \ldots, \gamma^{(t')}).$

The correctness of the above procedure is shown in the following claim.

Claim 5.7. The above procedure terminates and returns a sequence $\gamma = (\gamma^{(1)}, \dots, \gamma^{(T')})$ from 0^n to 1^n consisting only of strings of $\{0, 1\}^n$. Moreover, each pair $(\gamma^{(t')}, \gamma^{(t'+1)})$ is equal to either $(\alpha^{(t)}, \alpha^{(t+1)}), (\beta^{(t)}, \beta^{(t+1)}), (\alpha^{(t)}, \beta^{(t)}), \text{ or } (\beta^{(t)}, \alpha^{(t)})$ for some t.

Proof. (P1) of Claim 5.6 ensures that

- if $\alpha^{(t)} \neq *$, then $\alpha^{(t+1)} \neq *$ or $\beta^{(t)} \neq *$;⁴
- if $\beta^{(t)} \neq *$, then $\beta^{(t+1)} \neq *$ or $\alpha^{(t)} \neq *$.

Thus, by construction, γ does not include any *.

⁴Because otherwise, we have $\alpha^{(t)} \neq *$ and $\alpha^{(t+1)} = \beta^{(t)} = *$, which implies $\beta^{(t+1)} = *$ by (P1), a contradiction that $\alpha^{(t+1)}$ or $\beta^{(t+1)}$ is not *.

Suppose the token is currently placed at (α, t) and just before at (β, t) . Then, the token must be placed at $(\alpha, t+1)$ in the next step. Similarly, if the token is placed at (β, t) and just before at (α, t) ; then, it must be at $(\beta, t+1)$ in the next step, which ensures that we eventually reach $(\alpha, T+1)$ or $(\beta, T+1)$ to terminate. The latter statement is obvious from the construction.

Since we have been given a NO instance, γ must include $(\gamma^{(t')}, \gamma^{(t'+1)})$ for some $t' \in [T']$ such that $\gamma^{(t')} \neq \gamma^{(t'+1)}$, $S(\gamma^{(t')}) \neq \gamma^{(t'+1)}$, and $S(\gamma^{(t'+1)}) \neq \gamma^{(t')}$. By (P2) and (P3) and Claim 5.7, either of $(\alpha^{(t)}, \beta^{(t)}) = (\gamma^{(t')}, \gamma^{(t'+1)})$ or $(\beta^{(t)}, \alpha^{(t)}) = (\gamma^{(t')}, \gamma^{(t'+1)})$ must hold for some $t \in [T]$, implying that $\alpha^{(t)} \neq \beta^{(t)}$, $S(\alpha^{(t)}) \neq \beta^{(t)}$, and $S(\beta^{(t)}) \neq \alpha^{(t)}$; namely, $Enc(\alpha^{(t)}) \circ Enc(\beta^{(t)}) \notin L_{ckt}(S)$.

We now estimate the probability that V(S) rejects $\sigma^{(t)}$. Recall that $f^{(t)}$ and $g^{(t)}$ are ε -close to Enc(·), implying that

$$\Pr_{i,j\sim[n]}\left[f_i^{(t)}\neq\perp \text{ and } g_j^{(t)}\neq\perp\right] \ge (1-\varepsilon)^2.$$
(5.12)

So, we would run the modified PCPP verifier V'_{ckt} with probability $\ge (1-\varepsilon)^2$. We use the following claim to bound the rejection probability of $V'_{ckt}(S)$:

Claim 5.8. Suppose $\alpha = \text{Dec}(f) \in \{0,1\}^n$, $\beta = \text{Dec}(g) \in \{0,1\}^n$, and $\text{Enc}(\alpha) \circ \text{Enc}(\beta) \notin L_{\text{ckt}}(S)$ for $f \circ g \in \{0,1,\bot\}^{2\ell(n)}$. Then, for every proof $\pi \in \{0,1,\bot\}^{p(n)}$, the modified PCPP verifier $V'_{\text{ckt}}(S)$ rejects $f \circ g \circ \pi$ with probability more than $1 - s_{\text{ckt}} - \varepsilon q$.

Proof. We first show that $(f \circ g)|_I$ contains \perp for $(I,D) \sim V_{ckt}(S)$ with probability at most εq . Denote by I_{ckt} the indices of $f \circ g \circ \pi$, where $|I_{ckt}| = 2\ell(n) + p(n)$. By smoothness of V_{ckt} , we have $p_{ckt} := \mathbb{Pr}_{(I,D)}[i \in I]$ for all $i \in I_{ckt}$. Since |I| = q for any $(I,D) \sim V_{ckt}(S)$, we obtain

$$|I_{\rm ckt}| \cdot p_{\rm ckt} = \sum_{i \in I_{\rm ckt}} \Pr_{(I,D)} \left[i \in I \right] \le q \implies p_{\rm ckt} \le \frac{q}{2\ell(n) + p(n)}.$$
(5.13)

Using a union bound and the assumption that each f and g contains \perp in at most $\varepsilon \cdot \ell(n)$ positions, we derive

$$\mathbb{Pr}_{(I,D)}\Big[(f \circ g)_I \text{ contains } \bot\Big] = \mathbb{Pr}_{(I,D)}\Big[\exists i \in I \text{ s.t. } (f \circ g)_i = \bot\Big] \\
\leq \sum_{i:(f \circ g)_i = \bot} \mathbb{Pr}_{(I,D)}\Big[i \in I\Big] \\
\leq \varepsilon \cdot 2\ell(n) \cdot p_{\text{ckt}} \leq \varepsilon q.$$
(5.14)

Subsequently, we show that $f \circ g$ is δ_{ckt} -far from $L_{\text{ckt}}(S)$, where $\delta_{\text{ckt}} = \frac{\rho}{4}$. Letting $(\alpha^*, \beta^*) \in \{0, 1\}^n \times \{0, 1\}^n$ such that $\text{Enc}(\alpha^*) \circ \text{Enc}(\beta^*) \in L_{\text{ckt}}(S)$, we have $\alpha \neq \alpha^*$ or $\beta \neq \beta^*$. Suppose $\alpha \neq \alpha^*$; then, we have

$$\Delta(f, \operatorname{Enc}(\alpha^{\star})) \ge \Delta(\operatorname{Enc}(\alpha^{\star}), \operatorname{Enc}(\alpha)) - \Delta(\operatorname{Enc}(\alpha), f) \ge \rho - \varepsilon.$$
(5.15)

Similarly, $\Delta(g, \text{Enc}(\beta^*)) \ge \rho - \varepsilon$ if $\beta \ne \beta^*$. Consequently, we obtain

$$\Delta(f \circ g, \operatorname{Enc}(\alpha^{\star}) \circ \operatorname{Enc}(\beta^{\star})) \ge \frac{\rho - \varepsilon}{2} > \frac{\rho}{4} = \delta_{\operatorname{ckt}},$$
(5.16)

where we used the fact that $\varepsilon \leq \frac{\rho}{3}$.

Taking a union bound, we derive

$$\mathbb{Pr}\left[\text{modified verifier } V'_{ckt}(S) \text{ accepts } f \circ g \circ \pi\right] \\
= \mathbb{Pr}\left[(f \circ g)|_{I} \text{ contains } \perp \text{ or } \left(\pi|_{I} \text{ doesn't contain } \perp \text{ and } D((f \circ g \circ \pi)|_{I}) = 1\right)\right] \\
= \mathbb{Pr}\left[(f \circ g)|_{I} \text{ contains } \perp \text{ or } \left((f \circ g \circ \pi)|_{I} \text{ doesn't contain } \perp \text{ and } D((f \circ g \circ \pi)|_{I}) = 1\right)\right] \\
\leq \mathbb{Pr}\left[(f \circ g)|_{I} \text{ contains } \perp\right] + \mathbb{Pr}\left[(f \circ g \circ \pi)|_{I} \text{ doesn't contain } \perp \text{ and } D((f \circ g \circ \pi)|_{I}) = 1\right]. \\
\leq \underbrace{\mathbb{Pr}_{(I,D)}\left[(f \circ g)|_{I} \text{ contains } \perp\right]}_{\leq \varepsilon q} + \mathbb{Pr}\left[(f \circ g \circ \pi)|_{I} \text{ doesn't contain } \perp \text{ and } D((f \circ g \circ \pi)|_{I}) = 1\right]. \\$$
(5.17)

Let $\tilde{\pi}$ be a proof obtained from π by replacing every occurrence of \perp by 0. If $(f \circ g \circ \pi)|_I$ does not contain \perp and $D((f \circ g \circ \pi)|_I) = 1$, then $D((f \circ g \circ \tilde{\pi})|_I) = 1$. Since $f \circ g$ is δ_{ckt} -far from $L_{\rm ckt}(S)$, we have

$$\mathbb{Pr}_{(I,D)} \Big[(f \circ g \circ \pi) |_{I} \text{ doesn't contain } \perp \text{ and } D((f \circ g \circ \pi) |_{I}) = 1 \Big] \\
\leq \mathbb{Pr}_{(I,D)} \Big[D((f \circ g \circ \tilde{\pi}) |_{I}) = 1 \Big] \\
= \mathbb{Pr} \Big[V_{\text{ckt}}(S) \text{ accepts } f \circ g \circ \tilde{\pi} \Big] \\
\leq s_{\text{ckt}}.$$
(5.18)

Accordingly, we get

$$\mathbb{Pr}\Big[V_{\rm ckt}'(S) \text{ rejects } f \circ g \circ \pi\Big] = 1 - \mathbb{Pr}\Big[V_{\rm ckt}'(S) \text{ accepts } f \circ g \circ \pi\Big] > 1 - s_{\rm ckt} - \varepsilon q, \qquad (5.19)$$
mpleting the proof.

completing the proof.

Using Claim 5.8 and the definition of ε in Eq. (5.8), we derive

$$\mathbb{Pr}\left[V(S) \text{ rejects } \sigma^{(t)}\right] \ge \mathbb{Pr}_{i,j\sim[\ell(n)]}\left[f_i^{(t)} \neq \bot \text{ and } g_j^{(t)} \neq \bot\right] \cdot \mathbb{Pr}\left[V_{\text{ckt}}'(S) \text{ rejects } \sigma^{(t)}\right]$$
$$> (1-\varepsilon)^2 \cdot (1-s_{\text{ckt}} - \varepsilon q)$$
$$\underset{\varepsilon \leqslant \frac{1-s_{\text{ckt}}}{2q}}{\ge} (1-\varepsilon)^2 \cdot \frac{1-s_{\text{ckt}}}{2},$$
(5.20)

Consequently, we get

$$\max_{t \in [T]} \mathbb{P}\mathbb{r}\Big[V(S) \text{ rejects } \sigma^{(t)}\Big] > \min\left\{(\kappa\varepsilon)^2, (1-\varepsilon)^2 \cdot \frac{1-s_{\text{ckt}}}{2}\right\},\tag{5.21}$$

accomplishing the proof of Lemma 5.5.

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5.3 Impossibility of Extension to Average Case

Since the verifier of Theorem 5.1 co-nondeterministically guesses $t \in [T]$ and probabilistically checks $\pi^{(t)}$, one might think of extending it so as to choose $t \in [T]$ randomly. The soundness case then requires that the verifier accepts "most" of but rejects a constant fraction of the proofs in any reconfiguration sequence. The resulting reconfiguration proof $(\pi^{(1)}, \ldots, \pi^{(T)})$ can be thought of as a (kind of) *rectangular PCPs* [BHPT20], whose column is of exponential length and row is of polynomial length, and we pick $t \in [T]$ and run a verifier on $\pi^{(t)}$ to decide whether to accept. However, such relaxation is impossible, as formally stated below.

Observation 5.9. Let V be a verifier with randomness complexity $r(n) = O(\log n)$ and query complexity q(n) = O(1), $x \in \{0, 1\}^n$ be an input of length n, and $\pi^{\text{start}}(x), \pi^{\text{goal}}(x) \in \{0, 1\}^{\text{poly}(n)}$ be a pair of proofs that are accepted by V with probability 1. Then, there always exists a reconfiguration sequence $\pi = (\pi^{(1)}, \dots, \pi^{(T)})$ from $\pi^{\text{start}}(x)$ to $\pi^{\text{goal}}(x)$ over $\{0, 1\}^{\text{poly}(n)}$ such that

$$\mathbb{Pr}_{t\sim[T]}\left[V \ accepts \ \pi^{(t)}\right] = \mathbb{Pr}_{\substack{t\sim[T]\\(I,D)\sim V(x)}}\left[D(\pi^{(t)}|_{I}) = 1\right] > 1 - \frac{1}{2^{\Omega(n)}},\tag{5.22}$$

where the probability is over the r(n) random bits of V and $t \sim [T]$.

Proof. Consider any reconfiguration sequence $(\pi^{(1)}, \dots \pi^{(\text{poly}(n)+1)})$ from $\pi^{\text{start}}(x)$ to $\pi^{\text{goal}}(x)$. By appending $(\underbrace{\pi^{\text{goal}}(x), \dots, \pi^{\text{goal}}(x)}_{2^n - (\text{poly}(n)+1) \text{ times}})$ to it, we obtain an exponential-length reconfiguration

sequence $\pi = (\pi^{(1)}, \dots, \pi^{(2^n)})$. Since π contains (at least) $2^n - (\text{poly}(n) + 1)$ number of $\pi^{\text{goal}}(x)$, we have

$$\Pr_{t\sim[T]}\left[V \text{ accepts } \pi^{(t)}\right] \ge \frac{2^n - (\operatorname{poly}(n) + 1)}{2^n} = 1 - \frac{1}{2^{\Omega(n)}},\tag{5.23}$$

as desired.

6 PSPACE-hardness of Approximation for Reconfiguration Problems

In this section, we show that many popular reconfiguration problems are PSPACE-hard to approximate, answering an open problem of [IDHPSUU11]. Since Ohsaka [Ohs23b] has already shown gap-preserving reductions starting from the Reconfiguration Inapproximability Hypothesis (RIH), which asserts that a gap version of MAXMIN CSP RECONFIGURA-TION is PSPACE-hard, we prove that RIH is true.

6.1 Constant-factor Inapproximability of MAXMIN CSP RECONFIG-URATION

We first define reconfiguration problems on constraint satisfaction. For a *q*-ary constraint system $\Psi = (\psi_j)_{j \in [m]}$ over variable set N and alphabet Σ and its two satisfying assignments A^{start} and A^{goal} for Ψ , a *reconfiguration sequence from* A^{start} *to* A^{goal} *over* Σ^N is

a sequence $(A^{(1)}, \ldots, A^{(T)}) \in (\Sigma^N)^*$ such that $A^{(1)} = A^{\text{start}}$, $A^{(T)} = A^{\text{goal}}$, and $A^{(t)}$ and $A^{(t+1)}$ differ in at most one vertex for every $t \in [T-1]$. In the *q*-CSP RECONFIGURATION problem, we are asked to decide if there is a reconfiguration sequence of satisfying assignments to Ψ from A^{start} to A^{goal} . Subsequently, we formulate an approximate variant of *q*-CSP RECON-FIGURATION [IDHPSUU11, Ohs23b]. For a reconfiguration sequence $\mathscr{A} = (A^{(1)}, \ldots, A^{(T)})$ of assignments, let $val_{\Psi}(\mathscr{A})$ denote the *minimum fraction* of satisfied constraints over all $A^{(i)}$'s in \mathscr{A} ; namely,

$$\operatorname{val}_{\Psi}(\mathscr{A}) \coloneqq \min_{A^{(i)} \in \mathscr{A}} \operatorname{val}_{\Psi}(A^{(i)}).$$
(6.1)

In MAXMIN *q*-CSP RECONFIGURATION, we wish to maximize $val_{\Psi}(\mathcal{A})$ subject to $\mathcal{A} = (A^{start}, \dots, A^{goal})$. For two assignments $A^{start}, A^{goal} : N \to \Sigma$, let $val_{\Psi}(A^{start} \leftrightarrow A^{goal})$ denote the maximum value of $val_{\Psi}(\mathcal{A})$ over all possible reconfiguration sequences \mathcal{A} from A^{start} to A^{goal} ; namely,

$$\operatorname{val}_{\Psi}(A^{\operatorname{start}} \longleftrightarrow A^{\operatorname{goal}}) \coloneqq \max_{\mathscr{A} = (A^{\operatorname{start}}, \dots, A^{\operatorname{goal}})} \operatorname{val}_{\Psi}(\mathscr{A}) = \max_{\mathscr{A} = (A^{\operatorname{start}}, \dots, A^{\operatorname{goal}})} \min_{A^{(i)} \in \mathscr{A}} \operatorname{val}_{\Psi}(A^{(i)}).$$
(6.2)

For every numbers $0 \le s \le c \le 1$, $\text{GAP}_{c,s}$ *q*-CSP RECONFIGURATION requests to determine for a *q*-ary constraint system Ψ and its two assignments A^{start} and A^{goal} , whether $\text{val}_{\Psi}(A^{\text{start}} \leftrightarrow A^{\text{goal}}) \ge c \text{ or val}_{\Psi}(A^{\text{start}} \leftrightarrow A^{\text{goal}}) < s$. As a corollary of Theorem 5.1 and Proposition 4.9, we immediately obtain a proof of RIH, as formally stated below.

Theorem 6.1. The Reconfiguration Inapproximability Hypothesis holds; that is, there exist a universal constant $q \in \mathbb{N}$ such that $\operatorname{GAP}_{1,\frac{1}{2}} q$ -CSP RECONFIGURATION with alphabet size 2 is PSPACE-complete.

By Theorem 6.1 and [Ohs23a, Ohs23b], a host of reconfiguration problems turn out to be PSPACE-hard to approximate within a constant factor, as listed in Corollary 1.6.

6.2 Polynomial-factor Inapproximability of MAXMIN CLIQUE RECON-FIGURATION

We amplify inapproximability of MAXMIN CLIQUE RECONFIGURATION from a constant factor to a polynomial factor. The proof of the following result uses the *derandomized graph product* due to Alon, Feige, Wigderson, and Zuckerman [AFWZ95]; see also [HLW06, Section 3.3.2] and [AB09, Example 22.7].

Theorem 6.2. There exists a constant $\varepsilon \in (0, 1)$ such that MAXMIN CLIQUE RECONFIGURA-TION is PSPACE-hard to approximate within a factor of n^{ε} , where n is the number of vertices.

We here formulate CLIQUE RECONFIGURATION and its approximate variant. Denote by $\omega(G)$ the clique number of a graph G. For a pair of cliques C^{start} and C^{goal} of a graph G, a reconfiguration sequence from C^{start} to C^{goal} is a sequence $(C^{(1)}, \ldots, C^{(T)})$ of cliques of G such that $C^{(1)} = C^{\text{start}}$, $C^{(T)} = C^{\text{goal}}$, and $C^{(t)}$ and $C^{(t+1)}$ differ in at most one vertex; i.e., $|C^{(t)}\Delta C^{(t+1)}| = 1.5$ CLIQUE RECONFIGURATION asks if there is a reconfiguration sequence from C^{start} to C^{goal} made up of cliques only of size at least min{ $|C^{\text{start}}|, |C^{\text{goal}}|$ } - 1. For a reconfiguration sequence of cliques of G, denoted $\mathscr{C} = (C^{(1)}, \ldots, C^{(T)})$, let

$$\operatorname{val}_{G}(\mathscr{C}) \coloneqq \min_{C^{(i)} \in \mathscr{C}} |C^{(i)}|.$$
(6.3)

Then, for a pair of cliques C^{start} and C^{goal} of G, MAXMIN CLIQUE RECONFIGURATION requires to maximize $\text{val}_G(\mathscr{C})$ subject to $\mathscr{C} = (C^{\text{start}}, \dots, C^{\text{goal}})$. Subsequently, let $\text{val}_G(C^{\text{start}} \leftrightarrow C^{\text{goal}})$ denote the maximum value of $\text{val}_G(\mathscr{C})$ over all possible reconfiguration sequences \mathscr{C} from C^{start} to C^{goal} ; namely,

$$\operatorname{val}_{G}(C^{\operatorname{start}} \longleftrightarrow C^{\operatorname{goal}}) := \max_{\mathscr{C} = (C^{\operatorname{start}}, \dots, C^{\operatorname{goal}})} \operatorname{val}_{G}(\mathscr{C}).$$
(6.4)

Reduction. We first describe a gap-amplification reduction from MAXMIN CLIQUE RE-CONFIGURATION to itself using the derandomized graph product [AFWZ95]. Let $(G, C^{\text{start}}, C^{\text{goal}})$ be an instance of MAXMIN CLIQUE RECONFIGURATION, where G = (V, E) is a graph on n vertices. By Theorem 6.1 and [Ohs23b], it is PSPACE-hard to distinguish whether $\operatorname{val}_G(C^{\text{start}} \leftrightarrow C^{\text{goal}}) \ge \omega(G) - 1$ or $\operatorname{val}_G(C^{\text{start}} \leftrightarrow C^{\text{goal}}) \ge (1 - \varepsilon)(\omega(G) - 1)$ for some constant $\varepsilon \in (0, 1)$ even when $|C^{\text{start}}| = |C^{\text{goal}}| = \omega(G)$ and $\frac{\omega(G)}{n} \in [\frac{1}{3}, \frac{1}{2}]$.

Construct then a new instance $(H, D^{\text{start}}, D^{\text{goal}})$ of MAXMIN CLIQUE RECONFIGURATION as follows. Let $\ell = \lceil \log n \rceil$, and X be a (d, λ) -expander graph over the same vertex as G. The precise value of d and λ will be determined later. Graph H = (W, F) is defined as follows:

- Vertex set: *W* is the set consisting of all length- $(\ell 1)$ walks $\mathbf{w} = (w_1, \dots, w_\ell)$ over *X*. Note that the number of vertices is equal to $N \coloneqq |W| = nd^{\ell-1}$, which is polynomial in *n*.
- **Edge set:** *H* contains an edge between $\mathbf{w}_1 \neq \mathbf{w}_2 \in W$ if and only if a subgraph of *G* induced by $\mathbf{w}_1 \cup \mathbf{w}_2$ forms a clique.

For any clique $C \subseteq V$ of G, define $D_C \subseteq W$ as

$$D_C \coloneqq \left\{ \mathbf{w} \in W \mid \mathbf{w} \subseteq C \right\},\tag{6.5}$$

which is a clique of *H* as well. Constructing $D^{\text{start}} := D_{C^{\text{start}}}$ and $D^{\text{goal}} := D_{C^{\text{goal}}}$ completes the reduction. We refer to the following property about random walks over expander graphs.

Lemma 6.3 ([AFWZ95]). Let S be any vertex set of X, and $\mathbf{Z} \coloneqq (Z_1, \ldots, Z_\ell)$ a ℓ -tuple of random variables denoting the vertices of a uniformly chosen $(\ell - 1)$ -length random walk over X. Then, it holds that

$$\frac{\left(\frac{|S|}{|V|} - 2\frac{\lambda}{d}\right)^{\ell} \leq \mathbb{P}_{\mathbb{Z}}\left[\forall i \in [\ell], Z_i \in S\right] \leq \left(\frac{|S|}{|V|} + 2\frac{\lambda}{d}\right)^{\ell}.$$
(6.6)

⁵Such a model of reconfiguration is called *token addition and removal* [IDHPSUU11].

The completeness and soundness are shown below.

Lemma 6.4. If $\operatorname{val}_G(C^{\operatorname{start}} \longleftrightarrow C^{\operatorname{goal}}) \ge \omega(G) - 1$, then

$$\operatorname{val}_{H}(D^{\operatorname{start}} \longleftrightarrow D^{\operatorname{goal}}) \ge |W| \cdot \left(\frac{\omega(G) - 1}{|V|} - 2\frac{\lambda}{d}\right)^{\ell}.$$
(6.7)

Proof. It suffices to consider the case that C^{start} and C^{goal} differ in exactly two vertices (i.e., C^{goal} is obtained from C^{start} by removing and adding a single vertex). There is a reconfiguration sequence $(C^{\text{start}}, C^{\circ}, C^{\text{goal}})$ from C^{start} to C^{goal} , where $C^{\circ} := C^{\text{start}} \cap C^{\text{goal}}$ is a clique of size $\omega(G) - 1$. Since $D_{C^{\text{start}}} \supset D_{C^{\circ}}$ and $D_{C^{\text{goal}}} \supset D_{C^{\circ}}$ by definition, we can reconfigure from $D^{\text{start}} = D_{C^{\text{start}}}$ to $D^{\text{goal}} = D_{C^{\text{goal}}}$ by first removing the vertices of $D_{C^{\text{start}}} \setminus D_{C^{\circ}}$ one by one and then adding the vertices of $D_{C^{\text{goal}}} \setminus D_{C^{\circ}}$ one by one. Thus, we have $\text{val}_{H}(D^{\text{start}} \longleftrightarrow D^{\text{goal}}) \ge |D_{C^{\circ}}|$. Lemma 6.3 derives that

$$\frac{|D_{C^{\circ}}|}{|W|} = \mathbb{P}_{\mathbf{Z}}\left[\forall i \in [\ell], Z_i \in C^{\circ}\right] \ge \left(\frac{|C^{\circ}|}{|V|} - 2\frac{\lambda}{d}\right)^{\ell} = \left(\frac{\omega(G) - 1}{|V|} - 2\frac{\lambda}{d}\right)^{\ell},\tag{6.8}$$

completing the proof.

Lemma 6.5. If $\operatorname{val}_G(C^{\operatorname{start}} \leftrightarrow C^{\operatorname{goal}}) < (1 - \varepsilon)(\omega(G) - 1)$, then

$$\operatorname{val}_{H}(D^{\operatorname{start}} \longleftrightarrow D^{\operatorname{goal}}) < |W| \cdot \left((1-\varepsilon) \frac{\omega(G)-1}{|V|} + 2\frac{\lambda}{d} \right)^{\ell}.$$
(6.9)

Proof. We show the contrapositive. Suppose we are given a reconfiguration sequence $\mathcal{D} = (D^{(1)}, \ldots, D^{(T)})$ from D^{start} to D^{goal} such that $\operatorname{val}_{H}(\mathcal{D}) \ge |W| \cdot \left((1-\varepsilon)\frac{\omega(G)-1}{|V|} + 2\frac{\lambda}{d}\right)^{\ell}$. For any clique D of H, define C_{D} as

$$C_D \coloneqq \bigcup_{\mathbf{w} \in D} \mathbf{w},\tag{6.10}$$

which is a clique of *G* as well. Observe that $\operatorname{val}_G(C_{D^{(t)}} \leftrightarrow C_{D^{(t+1)}}) \ge \min\{|C_{D^{(t)}}|, |C_{D^{(t+1)}}|\}$ for any $t \in [T-1]$ since $C_{D^{(t)}} \subset C_{D^{(t+1)}}$ or $C_{D^{(t)}} \supset C_{D^{(t+1)}}$, implying further that

$$\operatorname{val}_{G}(C^{\operatorname{start}} \longleftrightarrow C^{\operatorname{goal}}) \geq \min_{t \in [T-1]} \operatorname{val}_{G}(C_{D^{(t)}} \longleftrightarrow C_{D^{(t+1)}})$$

$$\geq \min_{t \in [T-1]} \min\left\{ |C_{D^{(t)}}|, |C_{D^{(t+1)}}| \right\}$$

$$\geq \min_{D^{(t)} \in \mathcal{D}} |C_{D^{(t)}}|.$$

(6.11)

Lemma 6.3 derives that

$$\frac{|D^{(t)}|}{|W|} = \mathbb{P}_{\mathbf{Z}} \left[\mathbf{Z} \in D^{(t)} \right] \leq \mathbb{P}_{\mathbf{Z}} \left[\forall i \in [\ell], \, Z_i \in C_{D^{(t)}} \right] \leq \left(\frac{|C_{D^{(t)}}|}{|V|} + 2\frac{\lambda}{d} \right)^{\ell}.$$
(6.12)

On the other hand, by assumption,

$$\frac{|D^{(t)}|}{|W|} \ge \left((1-\varepsilon)\frac{\omega(G)-1}{|V|} + 2\frac{\lambda}{d}\right)^{\ell}.$$
(6.13)

Consequently, we have $|C_{D^{(t)}}| \ge (1-\varepsilon)(\omega(G)-1)$ for all $t \in [T]$; thus, $\operatorname{val}_G(C^{\operatorname{start}} \longleftrightarrow C^{\operatorname{goal}}) \ge (1-\varepsilon)(\omega(G)-1)$, as desired.

We are now ready to accomplish the proof of Theorem 6.2.

Proof of Theorem 6.2. Letting X satisfy $\frac{\lambda}{d} < \frac{\varepsilon}{32}$ so that $\frac{\lambda}{d} < \frac{1}{8} \frac{\omega(G)-1}{n} \varepsilon$ for sufficiently large n, we have

$$\frac{\omega(G)-1}{|V|} - 2\frac{\lambda}{d} \ge \frac{\omega(G)-1}{|V|} \left(1 - \frac{\varepsilon}{4}\right)$$
(6.14)

$$(1-\varepsilon)\frac{\omega(G)-1}{|V|} + 2\frac{\lambda}{d} < \frac{\omega(G)-1}{|V|} \left(1 - \frac{3}{4}\varepsilon\right).$$
(6.15)

Such an expander graph X can be constructed in polynomial time in n, e.g., by using an explicit construction of near-Ramanujan graphs [Alo21, MOP21]. By Lemmas 6.4 and 6.5 and Eqs. (6.14) and (6.15), it is PSPACE-hard to approximate MAXMIN CLIQUE RECONFIG-URATION within a factor of

$$\frac{|W| \cdot \left(\frac{\omega(G)-1}{n} - 2\frac{\lambda}{d}\right)^{\ell}}{|W| \cdot \left((1-\varepsilon)\frac{\omega(G)-1}{n} + 2\frac{\lambda}{d}\right)^{\ell}} > \nu^{\ell}, \text{ where } \nu \coloneqq \frac{1-\frac{\varepsilon}{4}}{1-\frac{3}{4}\varepsilon}.$$
(6.16)

Suppose $v^{\ell} = N^{\delta}$ for some δ ; then, δ should be

$$v^{\lceil \log n \rceil} = (n \cdot d^{\lceil \log n \rceil - 1})^{\delta}$$

$$\implies \lceil \log n \rceil \cdot \log v = \delta \cdot (\log n + (\lceil \log n \rceil - 1) \cdot \log d)$$

$$\implies \delta = \frac{\lceil \log n \rceil \cdot \log v}{\log n + (\lceil \log n \rceil - 1) \cdot \log d} = \Theta\left(\frac{\log v}{1 + \log d}\right).$$
(6.17)

Consequently, MAXMIN CLIQUE RECONFIGURATION is PSPACE-hard to approximate within a factor of N^{δ} for some $\delta \in (0, 1)$.

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