On the Complexity of Avoiding Heavy Elements

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Abstract

We introduce and study the following natural total search problem, which we call the heavy element avoidance (Heavy Avoid) problem: for a distribution on $N$ bits specified by a Boolean circuit sampling it, and for some parameter $\delta(N) \geq 1/\text{poly}(N)$ fixed in advance, output an $N$-bit string that has probability less than $\delta(N)$. We show that the complexity of Heavy Avoid is closely tied to frontier open questions in complexity theory about uniform randomized lower bounds and derandomization. Among other results, we show:

1. For a wide range of circuit classes $C$, including $\text{ACC}^0$, $\text{TC}^0$, $\text{NC}^1$ and general Boolean circuits, $\text{EXP}$ does not have uniform randomized $C$-circuits if and only if Heavy Avoid for uniform implicit $C$-samplers has efficient deterministic algorithms infinitely often. This gives the first algorithmic characterization of lower bounds for $\text{EXP}$ against uniform randomized low-depth circuits. We show similar algorithmic characterizations for lower bounds in $\text{PSPACE}$, $\text{NP}$ and $\text{EXP}^{\text{NP}}$.

2. Unconditionally, there are polynomial-time pseudodeterministic algorithms that work infinitely often for several variants of Heavy Avoid, such as for uniform samplers of small randomness complexity. In contrast, the existence of a similar algorithm that solves Heavy Avoid for arbitrary polynomial-time samplers would solve a long-standing problem about hierarchies for probabilistic time.

3. If there is a time and depth efficient deterministic algorithm for Heavy Avoid, then $\text{BPP} = \text{P}$. Without the depth-efficiency requirement in the assumption, we still obtain a non-trivial form of infinitely-often deterministic simulation of randomized algorithms. These results are shown using non-black-box reductions, and we argue that the use of non-black-box reductions is essential here.

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1 Introduction

Let $C$ be a Boolean circuit sampling a distribution $D$ on $N$-bit strings. Say that an $N$-bit string $y$ is $\delta$-heavy in $D$ if $y$ occurs with probability at least $\delta$ in $D$. Assuming that some $2\delta$-heavy string exists, for $\delta \geq 1/\text{poly}(N)$, how hard is it to find a $\delta$-heavy string given $C$ as input?

We call this natural search problem the heavy element finding (Heavy Find) problem. It is not difficult to see that the complexity of Heavy Find is closely related to the complexity of derandomization. There is a simple randomized polynomial-time algorithm for Heavy Find: we use $C$ to draw $O(N/\delta^2)$ independent samples from $D$ and output the string that occurs with the greatest multiplicity in the multiset of samples. A standard application of Chernoff–Hoeffding bounds shows that assuming that a $2\delta$-heavy string exists, the output of the algorithm will be a string that is $\delta$-heavy in $D$ with high probability.

Moreover, a deterministic polynomial-time algorithm for Heavy Find implies $\text{BPP} = \text{P}$. Indeed, let $M$ be a probabilistic polynomial-time Turing machine with error bounded by $1/4$ and $x$ be an input to $M$. We can define a circuit sampler $C_x$ which interprets its input as randomness $r$ for the computation of $M$ on $x$, outputting $1^N$ if $M$ accepts on $x$ using randomness $r$ and $0^N$ otherwise. Observe that if $M$ accepts $x$, the unique solution to Heavy Find on input $C_x$ with parameter $\delta = 1/3$ is $1^N$, and if $M$ rejects $x$, the unique solution to Heavy Find on input $C_x$ with parameter $1/3$ is $0^N$. Thus, a deterministic polynomial-time algorithm for Heavy Find allows us to decide if $M$ accepts $x$, also in deterministic polynomial time.\footnote{Readers who are familiar with derandomization might already see that the derandomization also holds for the promise version of $\text{BPP}$ ($\text{prBPP}$). In fact, it is not hard to show that Heavy Find can be solved in deterministic polynomial time if and only if $\text{prBPP}$ collapses to $\text{prP}$, the promise version of $\text{P}$.}

We now turn our original question on its head: given $C$ as input, how hard is it to find a string that is not $\delta$-heavy? We call this the heavy element avoidance (Heavy Avoid) problem. Heavy Avoid is the complementary search problem to Heavy Find: a string $y \in \{0,1\}^N$ is a solution to Heavy Avoid if and only if it is not a solution to Heavy Find. The complexity of Heavy Avoid is the primary focus of this paper.

Superficially, Heavy Avoid seems to be a much simpler problem to solve than Heavy Find. First, when $\delta > 2^{-N}$, Heavy Avoid is a total search problem, i.e., the promise that a non-heavy $N$-bit string exists is automatically satisfied. In this paper, we mainly focus on the regime where $\delta \geq 1/\text{poly}(N)$, hence this is always true if $N$ is large enough. Second, there is a trivial algorithm that list-solves Heavy Avoid: Since the number of $\delta$-heavy strings is at most $1/\delta$, at least one of the lexicographically first $\lceil 1/\delta \rceil + 1$ strings of length $N$ is guaranteed to be a solution to Heavy Avoid. Third, there is a very efficient randomized algorithm for Heavy Avoid with overwhelming success probability: output a uniformly random string of length $N$. Note that by the previous observation that the number of $\delta$-heavy strings is at most $1/\delta$, this randomized algorithm fails on at most $1/\delta \leq \text{poly}(N)$ of its random choices.

Our main contribution is to introduce Heavy Avoid as a natural search problem of interest, and show that despite its seeming simplicity, Heavy Avoid has applications to several frontier questions in complexity theory regarding uniform randomized lower bounds and derandomization. Indeed, we show that in many settings the existence of algorithms for Heavy Avoid is equivalent to a complexity lower bound. The study of Heavy Avoid also illuminates recent almost-all-inputs-hardness assumptions in the theory of derandomization [CT21], and leads to novel white-box reductions in settings where black-box reductions are hard to show. Moreover, the connections between Heavy Avoid and complexity lower bounds can be used to derive efficient unconditional pseudodeterministic algorithms (in the sense of [GG11]) for Heavy Avoid in several settings, i.e., efficient randomized algorithms that output some fixed solution to Heavy Avoid with high probability.
1.1 Results

We now discuss our results in greater detail. We present algorithmic characterizations of uniform lower bounds via Heavy Avoid, unconditional pseudodeterministic algorithms for Heavy Avoid, and connections between derandomization with minimum assumptions and Heavy Avoid. Thus, our results can be divided naturally into three sets.

- In Section 1.1.1, we present algorithmic characterizations of lower bounds against uniform probabilistic circuits via Heavy Avoid. That is, deterministic algorithms for Heavy Avoid (in certain settings and with certain parameters) are equivalent to such lower bounds. In fact, we obtain very general characterizations that hold for classes such as EXP, PSPACE, EXP$^\text{NP}$ and NP, against uniform randomized circuit classes such as ACC$^0$, TC$^0$, or SIZE[poly]. This suggests that the analysis of Heavy Avoid could be useful in attacking frontier open questions such as EXP $\not\subseteq$ BP-ACC$^0$ and EXP$^\text{NP} \not\subseteq$ BPP.

- In Section 1.1.2, we use our algorithmic characterizations together with other ideas to give unconditional pseudodeterministic algorithms for several variants of Heavy Avoid.

- In Section 1.1.3, we give applications of Heavy Avoid to derandomization, including novel white-box reductions from promise problems that are hard for prRP or prBPP to Heavy Avoid, as well as connections to “almost-all-inputs-hardness” assumptions that have been explored in recent work on derandomization.

We consider both uniform and non-uniform versions of Heavy Avoid. In the uniform version, the search algorithm is given $N$ in unary, and needs to find a $\delta$-light element in $D_N$, where $D = \{D_N\}_{N \in \mathbb{N}}$ is an ensemble of distributions over $N$-bit strings that are sampled by some uniform sequence of circuits from a circuit class. Since $D$ is sampled by a uniform sequence of circuits, we do not need to give the circuit sampler explicitly to the search algorithm—the search algorithm can compute the circuit sampler by itself. In this uniform variant of the problem, fix a parameter $\delta : \mathbb{N} \rightarrow [0,1]$. $(D, \delta)$-Heavy-Avoid is the problem of finding a $\delta(N)$-light element in $D_N$, given $1^N$ as input. This variant is the one we consider in Sections 1.1.1 and 1.1.2.

In the non-uniform variant of the problem, the search algorithm is given as input a circuit sampler $C$ from some circuit class $\mathcal{C}$, and needs to output a $\delta$-light element in the distribution sampled by $C$. This is the version we mostly consider for the results in Section 1.1.3.

There are also two kinds of samplability we consider: implicit and explicit. In the implicit version, our sampler $C$ is Boolean: given randomness $r$ as input together with an index $i \in [N]$, it outputs the $i$th bit of the string sampled on randomness $r$. In this setting, the circuit size is typically less than $N$. In the explicit version, the circuit $C$ is given randomness $r$ as input and has $N$ output bits: it outputs the string sampled on randomness $r$. In this setting, the circuit size is at least $N$, since there are $N$ output bits. Note that when we show an implication from solving Heavy Avoid to proving lower bounds, the implication is stronger when we consider implicit solvers, since the algorithmic problem is easier to solve for implicit samplers.\(^3\) An implicit solver $C(r,i)$ can easily be converted to an equivalent explicit solver

$$C_{\text{Explicit}}(r) := C(r, 1)C(r, 2) \ldots C(r, N).$$

\(^2\)A $\delta$-light element is one that is not $\delta$-heavy.

\(^3\)We measure the complexity of solving the search problem as a function of $N$, even in the implicit-sampler setting.
1.1.1 Equivalences Between Complexity Separations and Algorithms for Heavy Avoid

It is a long-standing open question to prove lower bounds against non-uniform circuits – we still have not ruled out the possibility that every language computable in exponential time with an NP oracle (EXP^{NP}) has polynomial-size circuits. What is more embarrassing is our inability to separate EXP^{NP} from BPP (see, e.g., [Wil13b, Wil19] for discussions), despite the belief shared by many researchers that BPP = P [NW94, IW97].\(^4\) Moreover, the state of affairs is the same regarding lower bounds against uniform probabilistic circuits from restricted circuit classes: for example, it is open whether EXP can be simulated by DLOGTIME-uniform probabilistic $\text{ACC}^0$ circuits or $\text{EXP}^{\text{NP}}$ can be simulated by DLOGTIME-uniform probabilistic $\text{TC}^0$ circuits.\(^5\)

Our first set of results gives equivalences between such explicit lower bounds against uniform probabilistic circuits and efficient deterministic algorithms for Heavy Avoid. The equivalences work in a wide variety of settings, for a range of circuit classes including $\text{ACC}^0$, $\text{TC}^0$, $\text{NC}^1$ and general Boolean circuits, and for explicit lower bounds in several standard complexity classes of interest such as EXP, EXP^{NP}, PSPACE and NP. Notably, these results give new algorithmic characterizations of uniform lower bound questions by the existence of efficient algorithms for a natural search problem. Thus they could potentially be useful in attacking frontier open questions such as the EXP vs (uniform probabilistic) $\text{ACC}^0$ question, or the $\text{EXP}^{\text{NP}}$ vs BPP question.

We use BP-$\mathcal{C}$ to denote the set of languages computed by DLOGTIME-uniform probabilistic $\mathcal{C}$-circuits.

**Theorem 1.1** (Informal). Let $\mathcal{C}$ be a nice\(^6\) class of Boolean circuits. The following equivalences hold:

(i) $\text{EXP} \not\subseteq \text{BP-}$\(\mathcal{C}\) if and only if \((D, \delta)\)-Heavy-Avoid with $\delta(N) = 1/\text{polylog}(N)$ can be solved in deterministic polynomial time on infinitely many input lengths for any $D$ that admits implicit DLOGTIME-uniform $\mathcal{C}$-samplers of size $\text{polylog}(N)$.

(ii) $\text{EXP}^{\text{NP}} \not\subseteq \text{BP-}$\(\mathcal{C}\) if and only if \((D, \delta)\)-Heavy-Avoid with $\delta(N) = 1/\text{polylog}(N)$ can be solved in deterministic polynomial time with an NP oracle on infinitely many input lengths for any $D$ that admits implicit DLOGTIME-uniform $\mathcal{C}$-samplers of size $\text{polylog}(N)$.

(iii) $\text{PSPACE} \not\subseteq \text{BP-}$\(\mathcal{C}\) if and only if \((D, \delta)\)-Heavy-Avoid with $\delta(N) = 1/\text{polylog}(N)$ can be solved in deterministic logarithmic space on infinitely many input lengths for any $D$ that admits implicit DLOGTIME-uniform $\mathcal{C}$-samplers of size $\text{polylog}(N)$.

(iv) $\text{NP} \not\subseteq \text{BP-}$\(\mathcal{C}\) if and only if \((D, \delta)\)-Heavy-Avoid with $\delta(N) = 1/\text{polylog}(N)$ can be solved by DLOGTIME-uniform unbounded fan-in circuits of quasi-polynomial size and constant depth on infinitely many input lengths for any $D$ that admits implicit DLOGTIME-uniform $\mathcal{C}$-samplers of size $\text{polylog}(N)$.

For the PSPACE lower bounds, analogous algorithmic characterizations hold for almost everywhere uniform lower bounds and for lower bounds against uniform randomized sub-exponential size

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\(^4\)Since BPP is strictly contained in $\text{SIZE}[\text{poly}]$ [Adl78], the open problem of separating $\text{EXP}^{\text{NP}}$ from BPP is strictly more embarrassing than separating $\text{EXP}^{\text{NP}}$ from $\text{SIZE}[\text{poly}]$. See also [Wil19, Table 1] for a related perspective.

\(^5\)It follows from $\text{EXP}^{\text{NP}} \not\subseteq \text{ACC}^0$ [Will14, CLW20], which is a non-uniform circuit lower bound, that $\text{EXP}^{\text{NP}}$ cannot be simulated by DLOGTIME-uniform probabilistic $\text{ACC}^0$ circuits. (Note that we do not know how to prove such lower bounds by exploiting the circuit uniformity condition.)

\(^6\)In brief, a nice circuit class is one that contains $\text{AC}^0[\oplus]$, is closed under composition, and admits universal circuits for the corresponding class.
circuits. Perhaps interestingly, it follows from our arguments that the existence of efficient algorithms for \((\mathcal{D}, \delta)\text{-Heavy-Avoid}\) in the settings considered in Theorem 1.1 is robust with respect to the threshold parameter \(\delta(N)\): the existence of algorithms for any \(\delta(N) = o(1)\) yields the existence of algorithms of similar complexity for \(\delta(N) = 1/\text{polylog}(N)\).

Theorem 1.1 has direct corollaries that characterize frontier open questions in complexity theory.

**Corollary 1.2** (Informal). The following results hold:

(i) \(\text{EXP}^{\text{NP}} \not\subseteq \text{BP}\text{-TC}^0\) if and only if \text{Heavy-Avoid} for implicit \text{DLOGTIME}-uniform \text{TC}^0\text{-samplers} can be solved in deterministic polynomial time with access to and \text{NP} oracle on infinitely many input lengths.

(ii) \(\text{PSPACE} \not\subseteq \text{BP}\text{-ACC}^0\) if and only if \text{Heavy-Avoid} for implicit \text{DLOGTIME}-uniform \text{ACC}^0\text{-samplers} can be solved in logarithmic space on infinitely many input lengths.

Previously, algorithmic characterizations of non-uniform lower bounds were known for classes such as \text{NEXP} [IKW02, Wil16] and \text{EXP}^{\text{NP}} [Kor21, RSW22], and such characterizations for uniform randomized lower bounds against general circuits (that is, against \text{BPP}) were known for \text{EXP} [IW01] and \text{NEXP} [Wil16]. We are not aware of any previous algorithmic characterization of super-polynomial non-uniform or uniform randomized lower bounds for \text{NP}.

As a consequence of our results, we also observe a sharp threshold phenomenon in the setting of quantified derandomization of search problems (see Section 1.3 below for related work on quantified derandomization and some discussions, and Section 3.3 for a proof of this corollary).

**Corollary 1.3.** For every function \(e(N) = \omega(1)\), there is a unary \text{BPP} search problem \(S\) such that:

- \(S(1^N)\) can be solved in randomized \(\text{poly}(N)\) time by an algorithm that uses \(N\) random bits and errs on at most \(e(N)\) random bit sequences;
- If there exists a randomized polynomial-time algorithm for \(S\) with non-zero success probability that errs on at most \(O(1)\) random bit sequences for any input, then \(\text{EXP} \neq \text{BPP}\).

### 1.1.2 Unconditional Pseudodeterministic Algorithms for Heavy Avoid

Recall that a randomized algorithm is pseudodeterministic if it outputs the same answer with high probability. In the next result, we use the results of the previous subsection together with other ideas to obtain unconditional pseudodeterministic algorithms for different variants of Heavy Avoid.

**Theorem 1.4.** Let \(\mathcal{D} = \{D_N\}_{N \geq 1}\) be a distribution ensemble, where each \(D_N\) is supported over \(\{0,1\}^N\). The following results hold:

(i) Let \(\mathcal{C}\) be a nice class of Boolean circuits, and suppose \(\mathcal{D}\) admits implicit \text{DLOGTIME}-uniform \text{C-samplers} of size \(\text{polylog}(N)\). Then, for every function \(\delta(N) = 1/(\log N)^k\), where \(k \in \mathbb{N}\), either \((\mathcal{D}, \delta)\text{-Heavy-Avoid}\) can be solved in logarithmic space on infinitely many input lengths, or \((\mathcal{D}, \delta)\text{-Heavy-Avoid}\) can be solved pseudodeterministically by \text{DLOGTIME}-uniform \text{BP}\text{-C-circuits} of size \(\text{polylog}(N)\) on all input lengths.

In particular, when \(\mathcal{C}\) is the class of general Boolean circuits and \(\delta(N) = 1/(\log N)^k\), there is a polynomial-time pseudodeterministic algorithm for the \((\mathcal{D}, \delta)\text{-Heavy-Avoid}\) problem that succeeds on infinitely many inputs.

\footnote{In other words, the corresponding \text{DLOGTIME}-uniform \text{BP}\text{-C-circuit} \(E(i)\) is given \(i \in [N] = \{0,1\}^\text{log } N\) and outputs the \(i\)-th bit of the solution with high probability.}
(ii) Suppose \( D \) admits a polynomial-time sampler of randomness complexity \((\log N)^{O(1)}\). Then, for every function \( \delta(N) = 1/(\log N)^k \), where \( k \in \mathbb{N} \), there is a polynomial-time pseudodeterministic algorithm for the \((D, \delta)\)-Heavy-Avoid problem that succeeds on infinitely many inputs.\(^8\)

(iii) Suppose \( D \) admits a polynomial-time sampler. Then, for every function \( \delta(N) = 1/N^k \), where \( k \in \mathbb{N} \), and for every constant \( \varepsilon > 0 \), there is a pseudodeterministic algorithm for the \((D, \delta)\)-Heavy-Avoid problem that runs in time \( 2^{N^\varepsilon} \) and succeeds on infinitely many inputs.

Moreover, in all items the corresponding algorithm behaves pseudodeterministically on every input.

While the pseudodeterministic algorithms above are for natural algorithmic problems about samplers, the design and analysis of the algorithms rely heavily on connections to complexity theory.

The first item of Theorem 1.4 provides a pseudodeterministic polynomial-time infinitely-often algorithm for Heavy Avoid for implicit samplers. Moreover, this algorithm computes pseudodeterministically on all input lengths. We observe that the existence of a pseudodeterministic algorithm with these properties for the larger class of explicit samplers (as in the third item of Theorem 1.4) would solve the longstanding open problem of showing a tight hierarchy theorem for probabilistic time [KV87, Bar02, FS04, FST05, vMP06]. This is captured by the following result (see Section 4.4 for a more detailed discussion).

Proposition 1.5. Suppose that for every polynomial-time samplable distribution ensemble \( D = \{D_N\}_{N \geq 1} \), the corresponding \((D, \delta)\)-Heavy-Avoid problem for \( \delta(N) = 1/\log N \) admits a pseudodeterministic polynomial-time algorithm that succeeds on infinitely many input lengths and behaves pseudodeterministically on all input lengths. Then, for every constant \( k \geq 1 \), we have \( \text{BPE} \not\subseteq \text{BPTIME}[2^{k-\varepsilon}] \) (in particular, for every constant \( c \geq 1 \), \( \text{BPP} \not\subseteq \text{BPTIME}[n^c] \)).

1.1.3 Connections to Derandomization

Our final set of results explore relations between the complexity of Heavy Avoid and fundamental questions in derandomization. We consider the non-uniform variant of Heavy Avoid, where a Boolean circuit sampler is given as input to the algorithm solving Heavy Avoid. For \( \delta : \mathbb{N} \to [0,1] \), Implicit-\(\delta\)-Heavy-Avoid is the problem where we are given as input a circuit \( C \) implicitly sampling a distribution on \( N \) bits (as explained at the beginning of Section 1.1), and would like to output a \( \delta \)-light element in the distribution.

Our first result shows that the existence of efficient deterministic algorithms for Heavy Avoid that in addition can be implemented by sub-polynomial depth uniform circuits leads to a complete derandomization of \( \text{prBPP} \). Note that in this result, to obtain the desired conclusion it is sufficient for this algorithm to solve the problem for implicit samplers.

Theorem 1.6. Let \( \delta(N) = o(1) \) be any function. Suppose there is a constant \( \varepsilon > 0 \) and a deterministic algorithm \( A \) that solves the Implicit-\(\delta\)-Heavy-Avoid problem on implicit samplers of size \( N^\varepsilon \). Moreover, assume that \( A \) can be implemented as a logspace-uniform circuit of size \( \text{poly}(N) \) and depth \( N^{o(1)} \). Then \( \text{prBPP} = \text{prP} \).

If we could eliminate the circuit depth constraint from the statement of Theorem 1.6, it would be possible to establish an equivalence between the derandomization of \( \text{prBPP} \) and algorithms for

\(^8\)Note that the result in this item subsumes the “in particular” result from the previous item.
Heavy Avoid (in both the implicit and explicit settings). While obtaining this strong characterization remains elusive, in the next result we get a non-trivial derandomization consequence from the existence of an efficient algorithm for Heavy Avoid without assuming a circuit depth bound.

Let GAP-SAT denote the promise problem where YES instances are Boolean circuits with at least half of assignments being satisfying, and NO instances are unsatisfiable Boolean circuits. It is well known that GAP-SAT is complete for the promise version of RP.

**Theorem 1.7 (Informal).** Let $\delta(N) = o(1)$ be any function. Suppose there is an algorithm for Implicit-$\delta$-Heavy-Avoid on maps $G: \{0,1\}^{\text{poly}(n)} \rightarrow \{0,1\}^N$ (where $N = 2^n$) implicitly computable by an input circuit of size $\text{poly}(n)$, where the Heavy Avoid algorithm runs in $\text{poly}(N)$ time and is infinitely-often correct. Then there is an algorithm for GAP-SAT that runs in subexponential time and is infinitely-often* correct.\(^9\)

Theorem 1.6 and Theorem 1.7 are both established using *non-black-box reductions* that make use of recent hardness-randomness tradeoffs. In more detail, as explained in Section 1.2 below, Theorem 1.6 crucially relies on the instance-wise hardness-randomness tradeoff for low-depth circuits of Chen and Tell [CT21], while Theorem 1.7 combines the framework of [CT21] and the “leakage-resilient” hardness-randomness framework of Liu and Pass [LP23]. In contrast to the non-black-box nature of the proofs given for these two results, we show that it will be quite difficult to obtain them using black-box reductions. In particular, we show that improving Theorem 1.7 to a polynomial-time Levin reduction [Lev73] would derandomize prBPP.\(^10\) Stated more precisely, if there is an efficient black-box Levin reduction from the search version of GAP-SAT to Heavy Avoid (even with respect to non-uniform explicit samplers), then prBPP = prP holds unconditionally. We refer to Section 5.3 for more details.

Finally, we establish a deeper connection between the implicit non-uniform variant of Heavy Avoid considered in this section and the recent paradigm of *instance-wise* hardness-randomness tradeoffs alluded to above [CT21, LP22, LP23, CTW23]. Roughly speaking, in this paradigm, we convert a hard function $f: \{0,1\}^n \rightarrow \{0,1\}^{\text{poly}(n)}$ with multiple output bits into pseudorandomness, where the obtained derandomization is instance-wise: for every $x \in \{0,1\}^n$, if $f$ is hard to compute on $x$, then the derandomization of the corresponding computation over input $x$ succeeds. Naturally, the derandomization assumptions used in these results need *almost-all-inputs hardness*, meaning that $f$ is hard on *all but finitely many* inputs (instead of input lengths).\(^11\) In Section 5.4, we prove that the existence of efficient deterministic algorithms for Heavy Avoid in the implicit non-uniform setting is *equivalent* to the existence of functions $f$ with multiple output bits that are easy to compute deterministically but are hard against fixed polynomial-size randomized algorithms. This result sheds light into the relevance of the techniques that we employ to prove Theorem 1.6 and Theorem 1.7, and suggest that developing further connections between Heavy Avoid and these modern hardness-randomness tradeoffs paradigms could be a fruitful research direction.

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\(^9\)In Theorem 1.7, we only obtain GAP-SAT algorithms satisfying a technical condition called infinitely-often* correctness, which is a nonstandard variant of infinitely-often correctness. The crucial difference is that, for a sequence of inputs $\{x_n\}_{n \in \mathbb{N}}$, given $1^n$, the algorithm is allowed to inspect every input $x_1, x_2, \ldots, x_{\text{poly}(n)}$, and needs to provide a solution for $x_n$. In other words, the algorithm is correct infinitely-often* if it outputs the correct answer on infinitely many input lengths $n$ while having access to all input strings from the sequence that have length polynomial in $n$. We refer the reader to Definition 5.7 and to the proof of Theorem 5.8 for more details.

\(^10\)Recall that in a Levin reduction between search problems we have a pair $(f,g)$ of functions, where $f$ maps to an instance of the other problem while $g$ converts a given solution into a solution to the original problem.

\(^11\)Compared with classical hardness-randomness frameworks such as [NW94, IW97, STV01], the advantage of the new paradigm is that lower bounds against *uniform algorithms* (instead of non-uniform circuits) suffice for *worst-case* derandomization.
1.2 Techniques

We now discuss the proofs of Theorem 1.1, Theorem 1.4, Theorem 1.6, and Theorem 1.7. We make use of a variety of techniques to establish these results:

- The proof of Theorem 1.1 Item (iii) relies on extremely efficient instance checkers for a special PSPACE-complete problem investigated in [Che23]. This allows us to establish equivalences for very weak circuits classes $\mathcal{C}$ at the frontier of existing separations. Extending the equivalence result to NP, EXP, and EXP$^{NP}$ in the context of weak circuit classes poses some additional challenges that we address through different ideas and techniques.

- In the proof of Theorem 1.4 Item (i), we use an instance checker to design a polynomial-time pseudodeterministic algorithm for the $(D, \delta)$-Heavy-Avoid problem. To our knowledge, this is the first application of instance checkers in the design of an algorithm for a natural problem. On the other hand, the algorithms in Items (ii) and (iii) of Theorem 1.4 explore a connection to randomized time-bounded Kolmogorov complexity [Oli19, LO22] and its source coding theorem [LO21, LOZ22].

- The proof of Theorem 1.6 relies on a novel application of the Chen–Tell non-black-box hitting set generator construction from [CT21, CLO+23]. In contrast to previous applications, here the reconstruction procedure of the generator itself, as well as the assumed algorithm for Heavy Avoid, plays a key role in the specification of a “hard” function.

- Finally, the proof of Theorem 1.7 builds on the proof of Theorem 1.6. It combines for the first time the Chen–Tell derandomization framework [CT21] with the leakage resilience derandomization framework of [LP23], using a win-win analysis. We show that either the Heavy Avoid algorithm is leakage resilient, which allows us to use the framework of [LP23], or it can be implemented by a low-depth circuit, which allows us to use the framework of [CT21]. This enables us to derive a non-trivial derandomization consequence without the circuit depth constraint present in the hypothesis of Theorem 1.6.

Next, we describe some of our proofs and techniques in more detail.

Sketch of the Proof of Theorem 1.1. We first explain the proof of Item (iii), i.e., the equivalence between the complexity separation $\text{PSPACE} \nsubseteq \text{BP-}\mathcal{C}$ and the existence of (infinitely often) logarithmic-space algorithms for Heavy-Avoid over implicit DLOGTIME-uniform $\mathcal{C}$-samplers.

First, we show how to obtain the separation using algorithms for the implicit heavy avoid problem. Using standard arguments, it suffices to show that for every choice of $k \geq 1$, there is $L \in \text{DSPACE}[n^2]$ such that $L$ cannot be computed by $\text{DTIME}[k\cdot \log n]$-uniform randomized $\mathcal{C}$-circuits of size $n^k$.

Let $N = 2^n$. We consider a map $G_N : \{0, 1\}^{n^{O(k)}} \rightarrow \{0, 1\}^N$ that views its input string $x$ as a pair $(M, r)$, where $M$ is a short encoding (say, $\log n$ bits) of a clocked machine running in time $10k \cdot \log n$, and $r$ is a random string. Let $D_M$ be the $\mathcal{C}$-circuit of size at most $n^{2k}$ whose direct connection language is encoded by the machine $M$. For $i \in [N]$, we define the $i$-th output bit of $G_N(x)$ as $D_M(r, i)$. Due to its running time, the computation of $M$ can be uniformly converted into an $\text{AC}^0$ circuit of size at most $n^{10k}$. Using that $\mathcal{C}$ is a nice circuit class, $G_N$ can be implicitly computed by a DLOGTIME-uniform probabilistic $\mathcal{C}$-circuit $C_N$ of size at most $n^{O(k)}$.

Let $B(1^N)$ be an algorithm of space complexity $O(\log N)$ that solves $\mathcal{C}$-Implicit-$\delta$-Heavy-Avoid on infinitely many values of $N$ for the sequence $G_N$, and let $L_B$ be the language defined by $B$. Note that $L_B$ is in $\text{DSPACE}[O(n)] \subseteq \text{DSPACE}[n^2]$. To argue that $L_B$ cannot be computed by
DTIME$[k \cdot \log n]$-uniform randomized $C$-circuits of size $n^k$, it is enough to show that for every language $L$ computed by such circuits, each string in the sequence $\{y_N^L\}_N$ of truth-tables obtained from $L$ is $\delta$-heavy in $G_N(\mathcal{U}_{m(N)})$. Since $B$ solves $C$-Implicit-$\delta$-Heavy-Avoid for the sequence $\{G_N\}$, it follows that $L_B \neq L$.

The proof that the sequence $\{y_N^L\}_N$ of truth-tables obtained from $L$ is $\delta$-heavy in $G_N(\mathcal{U}_{m(N)})$ relies on the definition of $G_N$. In more detail, under the assumption that $L$ admits DTIME$[k \cdot \log n]$-uniform randomized $C$-circuits of size $n^k$, it is not hard to show that its truth-table is produced with probability comparable to $2^{-\Omega(n)}$. However, this probability is sufficiently large under the assumption that the encoding length $|M|$ is small in the definition of $G_N$.

The proof of the other direction in Theorem 1.1 is more interesting. We establish the contrapositive. Suppose that for some $G_N : \{0,1\}^{\text{poly}(n)} \rightarrow \{0,1\}^N$ implicitly computed by DLOGTIME-uniform $C$-circuits of size $\text{poly}(n)$, every algorithm $A(1^N)$ running in space $O(\log N)$ fails to solve $C$-Implicit-$\delta$-Heavy-Avoid on every large enough input length $N$. We employ this assumption to show that $\text{PSPACE} \subseteq \text{BP-}C$. For this, we recall the notion of instance checkers. Let $L \subseteq \{0,1\}^*$ be a language, and let $\{C_n^L(x, z)\}_{n \in \mathbb{N}}$ be a family of probabilistic oracle circuits. We say that $C$ is an instance checker for $L$ if for every input $x \in \{0,1\}^*$:

1. $\Pr_x[C_n^L(x, z) = L(x)] = 1$, and

2. for every oracle $O$, $\Pr_x[C_n^O(x, z) \notin \langle L(x), \bot \rangle] \leq 1/2^n$.

We will rely on an appropriate PSPACE-complete language $L^*$ that admits highly efficient instance checkers computable in any nice circuit class. This is a consequence of a result from [Che23], as explained in Appendix B.

We then consider a candidate algorithm $A(1^N)$ that computes as follows. On input $1^N$, define $tt_N$ to be the truth table of $L^*$ on $n$-bit inputs; we simply output $tt_N$. It is possible to show that $A$ computes in space $O(\log N)$ after an appropriate scaling of parameters, which we omit here for simplicity. Therefore, $A$ fails to solve $C$-Implicit-$\delta$-Heavy-Avoid on every large enough input length $N$. This means that for every large enough $N$, the probability of $tt_N$ under the distribution $G_N(\mathcal{U}_{\text{poly}(n)})$ from above is at least $\delta = 1/(\log N)^{O(1)} = 1/\text{poly}(n)$.

To explain how we compute $L^*$ on an input $x \in \{0,1\}^n$, assume for simplicity that the oracle instance checker circuit (call it IC) only queries its oracle on input length $n$. We sample $v := n^{O(1)}$ strings $z_1, \ldots, z_v \in \{0,1\}^{\text{poly}(n)}$ uniformly and independently at random, and for each string $z_i$, we define an oracle $O_i$ whose truth table is the string $G_N(z_i) \in \{0,1\}^N$. We run IC in parallel and obtain $b_i := \text{IC}_{n}^O(x)$ for each $i \in [v]$. We output 1 if at least one bit among $b_1, \ldots, b_v$ is 1, and 0 otherwise.

Next, we argue that $A$ computes $L^*$ with high probability. Let $tt_N$ denote the truth table of $L^*$ on input length $n$. By our choice of $v$, with high probability the string $tt_N$ appears among the strings $G_N(z_1), \ldots, G_N(z_v)$, meaning that one of the oracles $O_i$ computes $L^*$ on inputs of length $n$. Consequently, in this case, if $L^*(x) = 1$ then at least one bit $b_i = 1$, and the procedure outputs 1. On the other hand, if $L^*(x) = 0$, then by a union bound over the internal randomness of IC, with high probability every bit $b_i \in \{0,1\}$. In this case, the procedure outputs 0. This establishes the correctness of $A$. Using the efficiency of the instance checker and that $C$ is a nice circuit class, it is also possible to upper the circuit complexity of $A$ and to analyze the uniformity of the corresponding circuits. This implies that $L^* \in \text{BP-}C$. Since $L^*$ is PSPACE-complete under DLOGTIME-uniform projection reductions, we get that $\text{PSPACE} \subseteq \text{BP-}C$, as desired.

We now briefly comment on the additional ideas needed for the proofs of the other items in Theorem 1.1. The proof of Item (ii) requires a different approach, since instance checkers for EXPNP-complete languages are not known. We provide two different proofs in this case. In more
detail, the result for $\text{EXP}^{\text{NP}}$ can be obtained using a win-win argument and a reduction to Item (iii), or through the use of selectors for $\text{EXP}^{\text{NP}}$-complete languages [Hir15]. These two approaches provide different extensions of the result, which we discuss in detail in Section 3.3. On the other hand, the proof of Item (iv) relies on a randomized depth-efficient version of the search-to-decision reduction for SAT based on the Valiant-Vazirani Isolation Lemma [VV86], as well as the equivalence between the polynomial hierarchy and $\text{DLOGTIME}$-uniform constant-depth circuits of exponential size [BIS90].

**Sketch of the Proof of Theorem 1.4.** First, we discuss the proof of Item (i) in the case where $\mathcal{C} =$ “general Boolean circuits”. Consider a map $G_N: \{0,1\}^{\text{poly}(n)} \rightarrow \{0,1\}^N$ that is implicitly computable in time $\text{poly}(n)$. We consider two cases, based on whether $\text{EXP} = \text{BPP}$. If $\text{EXP} \nsubseteq \text{BPP}$, then by Theorem 1.1 Item (i) (with circuit class $\mathcal{C} =$ “general Boolean circuits”) the heavy avoid problem over $G_N$ can be solved in deterministic polynomial time (i.e., in time $\text{poly}(N)$) on infinitely many input lengths. Note that the correctness of the procedure obtained from Theorem 1.1 Item (i) relies on the existence of instance checkers for EXP-complete languages.

In the remaining case, assume that $\text{EXP} \subseteq \text{BPP}$. Let $B(j)$ be the following deterministic machine with input $j \in \{0,1\}^n$: It first goes over all choices of $x \in \{0,1\}^{\text{poly}(n)}$ and computes $G_N(x)$, then calculates the probability of each string in $\{0,1\}^N$ produced in this way, and finally outputs the $j$-th bit of the lexicographic first string $y$ such that $\Pr[G_N(U_{\text{poly}(n)}) = y] \leq \delta$. Note that $B$ runs in time exponential in $n$, its input length. Therefore, it defines a language $L_B \in \text{EXP}$. By the assumption, $L_B \in \text{BPP}$. Consequently, we can compute $y \in \{0,1\}^N$ from $1^N$ in pseudodeterministic time $\text{poly}(N)$. Note that in this case the algorithm succeeds on every input length.\(^{12}\)

More generally, to obtain the result claimed in Item (i) for a nice circuit class $\mathcal{C}$, we use a similar approach but consider whether $\text{PSPACE} \nsubseteq \text{BPL}$ instead (see Section 4.1).

In contrast, the proof of Theorem 1.4 Item (iii) relies on ideas from randomized time-bounded Kolmogorov complexity. More specifically, we consider the randomized Levin complexity of a string $y \in \{0,1\}^n$ [Oli19], denoted $\text{rKt}(y)$. Roughly speaking, $\text{rKt}(y)$ measures the minimum description length of a time-bounded machine that outputs $y$ with high probability. Our key idea is that, by the coding theorem of [LO21], if a string $y$ can be sampled in polynomial time with probability at least $\delta$, then $\text{rKt}(y) = O(\log 1/\delta)$. Consequently, to avoid the set of heavy strings produced by a polynomial-time sampleable distribution $D$, it is sufficient to construct a string $z$ such that $\text{rKt}(z) \geq C \cdot \log n$, where $C$ is large enough. In order to implement this idea, we employ a related subexponential time pseudodeterministic construction of strings of large $\text{rKt}$ complexity from [LOS21].

Finally, the proof of Theorem 1.4 Item (ii) is obtained via a translation to Item (iii), using a simple “prefix” reduction described in Section 4.3.

**Sketch of the Proof of Theorem 1.6.** Using existing results [BF99], in order to derandomize $\text{prBPP}$ it is sufficient to describe an algorithm that, given an input circuit $D: \{0,1\}^M \rightarrow \{0,1\}$ of size $O(M)$ with the promise that $\Pr_D[D(y) = 1] \geq 1/2$, runs in deterministic time $\text{poly}(M)$ and outputs a positive input of $D$. To achieve this, we will rely on a novel application of the Chen–Tell generator [CT21] (with the improved parameters from [CLO\textsuperscript{+}23]). In more detail, given a function $f: \{0,1\}^n \rightarrow \{0,1\}^{T(n)}$ computed by logspace uniform circuits of size $T(n)$ and depth $d(n)$, and a parameter $M(n)$ such that $c \cdot \log T \leq M \leq T^{1/c}$ (for a constant $c$), [CT21, CLO\textsuperscript{+}23] provides algorithms $\text{HSG}_{f}$ and $\text{Recon}_{f}$ depending on $f$ such that:

\(^{12}\)We remark that using a more involved construction that employs the instance checker as a subroutine, one can simultaneously consider both cases to describe a single explicit algorithm that succeeds infinitely often. We omit the details. For a similar situation where a non-constructive win-win argument can be turned into an explicit algorithm, see [OS17, Section 3.4].
• The algorithm $\text{HSG}_f(x)$ runs in deterministic $T^c$ time and outputs a set of $M$-bit strings.

• Given $x \in \{0,1\}^n$ and $i \in [T]$ as inputs, and oracle access to a candidate distinguisher $D: \{0,1\}^M \rightarrow \{0,1\}$, $\text{Recon}_D^D(x, i)$ runs in randomized $(dnM)^c$ time. If $D$ is dense and avoids $\text{HSG}_f(x)$, then with probability $\geq 1 - 2^{-M}$, $\text{Recon}_f^D(x, i)$ outputs the $i$-th bit of $f(x)$.

We consider an appropriate function $f': \{0,1\}^{\tilde{O}(M)} \rightarrow \{0,1\}^N$, where $N = M^{C_1}$ for a large enough constant $C_1$. We view the input of $f'$ as the description of an arbitrary circuit $D: \{0,1\}^M \rightarrow \{0,1\}$ of size $O(M)$. In this construction, the parameter $T = M^{C_2}$ for a large enough constant $C_2 > C_1$, while $d = M^{o(1)} = N^{o(1)}$. Moreover, $f'$ will be computed by a logspace-uniform family of circuits. We then show that $\text{HSG}_{f'}(D)$ hits $D$ if $D$ is a dense circuit. Note that the generator runs in time $\text{poly}(T) = \text{poly}(M)$ by our choice of parameters.

The function $f'$ makes use of the algorithm $\mathcal{A}$ that solves the Implicit-$\delta$-Heavy-Avoid problem on instances $G: \{0,1\}^{N^c} \rightarrow \{0,1\}^N$ that are implicitly computable in $N^c$ size. In more detail, we let $f'(D) = \mathcal{A}(C_D)$, where $C_D$ is an implicit (non-uniform) sampler of size $N^c$ for a map $G_D: \{0,1\}^{N^c} \rightarrow \{0,1\}^N$ described next.

First, we make a simplifying assumption: The sampler $G_D$ has access to the code of a machine $M_f$ that serves as a logspace-uniform description of a circuit family that computes $f'$. (Observe that this is self-referential, since we have defined $f'(D) = \mathcal{A}(C_D)$ above, while we will also use $f'$ to define $C_D$. We will handle this issue later.)

The sampler $G_D$ stores $D$ as advice. This is possible because $D$ is of size $M$, and if $C_1$ is large enough then $M \ll N^c$. The implicit sampler $C_D(r, i)$ for $G_D$ then uses its random input string $r$ of length $N^c$ and $i \in [\log N]$ to compute $\text{Recon}_D^D(D, i, r)$, where we have made explicit the random string $r$ used by $\text{Recon}_D^D$. Since $d = M^{o(1)}$ and $C_1$ is large enough, we get that $\text{Recon}_D^D(D, i, r)$ can be computed in time $(d \cdot M^{1+o(1)} : M^{c+o(1)}) \leq N^c$. This completes the definition of $f'(D)$ and of $\text{HSG}_{f'}(D)$. We note that to establish the size, depth, and logspace-uniformity of the sequence of circuits computing $f'$ we can rely on the fact that $f'$ only needs to produce the code of $C_D$.\(^{13}\)

Next, we argue that $\text{HSG}_{f'}(D)$ hits any dense circuit $D$. Assume this is not the case. Then, since $D$ avoids the generator, $\text{Recon}_D^D(D, i)$ outputs the $i$-th bit of $f'(D)$ with probability at least $1 - 2^{-M}$. Consequently, by a union bound over $i \in [N]$, it follows that the string $\mathcal{A}(C_D) = f'(D) \in \{0,1\}^N$ is output by $\text{Recon}_D^D(D, \cdot)$ with probability $1 - o(1)$. In other words, the string $f'(D)$ is sampled with high probability by the sampler $G_D$ encoded by $C_D$. On the other hand, since $f'(D) = \mathcal{A}(C_D)$ and $\mathcal{A}$ solves the heavy avoid problem for $G_D$, we get that the string $f'(D)$ has probability $o(1)$ under $G_D$. This contradiction implies that $\text{HSG}_{f'}(D)$ indeed hits $D$.

It remains to explain how to fix the self-referential nature of the definition of $G_D$ via the implicit sampler $C_D$, which depends on $f'$ (and which in turn depends on $C_D$). In more detail, the construction is self-referential due to the use of the routine $\text{Recon}_D^D$, which depends on $f'$. To patch the argument, we combine the following key points:

• There is a deterministic algorithm that, given the Turing machine $M_{f'}$ that prints the circuit for $f'$ in logspace, outputs the description of $\text{Recon}_f$ in $\text{poly}(|\langle M_f' \rangle|)$ time.

• We can combine constantly many samplers into a single sampler that produces the convex combination of the corresponding distributions. A string with weight $o(1)$ under the new distribution must have weight $o(1)$ under each original sampler.

\(^{13}\)We make a brief comment about the novelty of this argument. In order to define the “hard” function $f'$, here we make use of the reconstruction procedure of the generator. This is different from an application of this generator in [CLO+23], where the code of the hitting set procedure plays a key role in the definition of the hard function.
Therefore, we can change the description of $G_D$ so that it interprets a small prefix of its random input string as the description of a Turing machine $M_f$ that prints a circuit of the expected size using logarithmic uniformity, then use the first bullet above to produce the procedure $\text{Recon}_f$ corresponding to $f$. Notice that with this change the sampler $G_D$ no longer depends on $f'$. Moreover, since $f'$ is encoded by some finite machine $M_{f'}$, using the second bullet the argument described above to reach a contradiction and establish the correctness of the hitting set generator still holds:

When $D$ avoids $\text{HSG}_{f'}(D)$ the modified sampler $G_D$ outputs the string $f'(D) = A(C_D)$ with constant probability, while as a solution to the heavy avoid problem for $G_D$ this string has probability $o(1)$. This completes the sketch of the argument.\(^{14}\)

**Sketch of the Proof of Theorem 1.7.** Since this is a more sophisticated construction, we only provide a brief sketch of the idea. As alluded to above, the argument combines the two instance-wise hardness-randomness tradeoffs introduced by Chen and Tell [CT21] and by Liu and Pass [LP23], respectively.

We employ a win-win analysis based on whether the assumed algorithm for $\text{Implicit-}\delta\text{-Heavy-Avoid}$ (call it $\text{Avoid}$) is “leakage resilient” hard. In more detail, let $f: \{0,1\}^n \rightarrow \{0,1\}^T$ be a function, $A$ be a randomized algorithm, and $x \in \{0,1\}^n$ be an input of $f$. We say that $f(x)$ is $\ell$-leakage resilient hard against $A$ if for every “leakage string” $\text{leak} \in \{0,1\}^\ell$, there is some $i \in [T]$ such that $\Pr[A(x,\text{leak},i) = f(x)_i] \leq 2/3$, where the probability is taken over the internal randomness of $A$. Liu and Pass [LP23] showed that leakage resilient hardness can be used for derandomization.

We can now explain the main idea behind the win-win analysis. If $\text{Avoid}$ is leakage resilient hard, we use the hardness-randomness tradeoffs in [LP23]. If this is not the case, we show that $\text{Avoid}$ can actually be implemented by a low-depth circuit. We can then use the hardness-randomness tradeoffs in [CT21], which requires the hard function to be computed by a low-depth circuit family.

Implementing this plan turns out to require a delicate construction and the notion of infinitely-often* correctness appearing in the statement of Theorem 1.7. We refer to Section 5.2 for more details.

### 1.3 Related Work

Our work relates to several recent lines of research in algorithms and complexity theory.

**Algorithmic Characterizations of Uniform Lower Bounds.** The algorithmic method of Williams [Wil13a], which derives $C$-circuit lower bounds for NEXP or EXP$^{NP}$ from non-trivial algorithms for Satisfiability or GAP-SAT for $C$ circuits, has been successful in showing several new circuit lower bounds [Wil14, Wil18b, Wil18a, MW20, CW19, CLW20]. However, in settings where non-uniform lower bounds are unknown, it is unclear how to use such methods to at least give uniform randomized lower bounds. A step towards such methods is to give algorithmic characterizations of uniform lower bounds, which show that certain algorithmic results are both necessary and sufficient for lower bounds. An algorithmic approach to the NEXP vs BPP problem is given in [Wil13b], but this does not seem to give a characterization. An algorithmic characterization of EXP $\neq$ BPP follows from [IW01], but this characterization does not extend to frontier lower bound questions such as EXP$^{NP}$ $\nsubseteq$ BP-TC$^0$ and EXP $\nsubseteq$ BP-ACC$^0$. Theorem 1.1 gives generic characterizations that apply to these and other frontier questions – this showcases the benefits of considering

\(^{14}\)We note that this argument is non-black-box. The code of a machine $M_{f'}$ that describes a uniform circuit family for $f'$ is needed to instantiate the Chen-Tell generator. In the aforementioned construction, this means that black-box access to the algorithm $A$ is not enough.
the Heavy Avoid problem rather than the previously studied Gap-SAT or Circuit Acceptance Probability Problem (CAPP). Theorem 1.1 also gives characterizations of uniform lower bounds for NP, where no algorithmic characterizations at all were known before. An algorithmic approach to uniform lower bounds for NP is given in [San23], but the method there does not seem to extend to randomized lower bounds, and moreover does not give unconditional algorithmic characterizations.

**Range Avoidance.** There have been several recent works on the Range Avoidance problem [KKMP21, Kor21, RSW22, GLW22, CHLR23, ILW23, GGNS23, CL24, Kra24], which is a total search problem where we are given a circuit \( C \) from \( n \) bits to \( m \) bits, \( m > n \), and need to output an \( m \)-bit string that is not in the range of \( C \).\(^{15} \) The Range Avoidance problem is tightly connected to proving non-uniform lower bounds (see, e.g., [Jeˇ r04, Kor21, RSW22, CHR24, Li24, Kra24]). Heavy Avoid can be thought of as an easier version of Range Avoidance, where we are asked to output some \( m \)-bit string that does not have many pre-images, rather than a string that has no pre-images at all. Indeed, for \( \delta \) that is inverse polynomial, Heavy Avoid with parameter \( \delta \) is a BPP search problem, which is unknown for Range Avoidance and would have new circuit lower bound consequences if it were the case. We refer to Appendix A for reductions from both Heavy Avoid and Heavy Find to Range Avoidance. One of our motivations for defining and studying Heavy Avoid is that algorithms for this easier problem might give a way to exploit uniformity in the lower bound.

**Quantified Derandomization.** In the quantified derandomization setting [GW14, Tel22], we are interested in the possibility of derandomizing algorithms with overwhelming success probability, e.g., derandomization of randomized algorithms for a decision problem that err on at most \( S(n) \) random bit sequences, for some \( S(n) \) that is sub-exponential or even just slightly super-polynomial. Note that this derandomization setting is quite specialized, since bounded-error randomized algorithms are in general allowed to err on an inverse polynomial fraction of all random bit sequences. A naive way to derandomize such algorithms for decision problems is to run the randomized algorithm on the lexicographically first \( 2S(n) + 1 \) random bit sequences, and take the majority answer. The question of when it is possible to do better has been studied extensively [GW14, Tel17, Tel18, CT19, Tel22]. Our Corollary 1.3 identifies an interesting phenomenon for quantified derandomization of BPP search problems, which has not been studied before. In this setting, the previously mentioned “naive” method to derandomize doesn’t work, as verifying a candidate solution itself involves the use of randomness. We show that for any \( \delta = o(1) \) there is a natural search problem, i.e., Heavy Avoid with parameter \( \delta \), such that the problem can be solved by a randomized algorithm which errs on at most \( 1/\delta = \omega(1) \) random bit sequences, but any efficient randomized algorithm which errs on at most \( O(1) \) random bit sequences would imply \( \text{EXP} \neq \text{BPP} \)! Thus even slightly beating the performance of a known algorithm in a quantified derandomization sense would imply a breakthrough lower bound.

**Pseudodeterministic Algorithms.** The notion of pseudodeterminism was introduced in the influential work of [GG11]. A pseudodeterministic algorithm for a search problem is a randomized algorithm that outputs some fixed solution to the search problem with high probability. There has been a lot of recent work on pseudodeterminism in various settings, as well as connections to complexity theory (see, e.g., [GGR13, OS17, DPV18, OS18, GL19, DPV21, GIPS21, LOS21, DPWV22, CDM23]). In general, one might hope to show that every BPP search problem can

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\(^{15}\)The problem is closely related to the dual weak pigeonhole principle, which has been widely investigated in logic and bounded arithmetic (see [Kra95, Jeˇ r04, Jeˇ r05, Kra24] and references therein).
also be solved pseudodeterministically, but this is not known, though there are important special cases such as finding an \( N \)-bit prime for which efficient pseudodeterministic algorithms are known infinitely often [CLO+23]. Theorem 1.4 gives unconditional pseudodeterministic algorithms for Heavy Avoid that can be implemented in low depth when the circuit sampler for Heavy Avoid is itself low depth. Moreover, improving our strongest pseudodeterministic algorithms would have immediate consequences for the long-standing open problem of showing a hierarchy for randomized time (Proposition 1.5). Note that unlike the problem of finding an \( N \)-bit prime, Heavy Avoid is a fairly powerful \( \mathsf{BPP} \) search problem, as evidenced by Theorems 1.6 and 1.7.

Minimal Assumptions for Derandomization. The standard “hardness vs. randomness” approach towards \( \mathsf{prBPP} = \mathsf{prP} \) requires lower bounds against non-uniform circuits [NW94, IW97, Uma03]; another line of work employs uniform lower bounds such as \( \mathsf{EXP} \neq \mathsf{BPP} \) to obtain heuristic (i.e., average-case) derandomization of \( \mathsf{BPP} \) [IW01, TV07, CRTY23, CRT22]. A long-standing open problem is whether circuit lower bounds such as \( \mathsf{EXP} \not\subseteq \mathsf{SIZE}[\text{poly}] \) are indeed necessary for derandomization (see, e.g., [Gol11] and [CRTY23]). Recently, Chen and Tell [CT21] proposed an instance-wise hardness-randomness tradeoff and showed that almost-all-inputs hardness suffices for worst-case derandomization. The Chen-Tell result has already sparked a new line of research on instance-wise hardness-randomness tradeoffs and minimal assumptions for derandomization [LP22, LP23, CRT22, CTW23, vMS23] (see also the survey [CT23]). As demonstrated in Theorems 1.6 and 1.7, these instance-wise hardness-randomness tradeoffs can be used as proof techniques to connect Heavy Avoid to the problem of derandomizing \( \mathsf{prRP} \) or \( \mathsf{prBPP} \). Moreover, as we show in Section 5.4, the almost-all-inputs hardness assumptions used in [CT21] are closely connected to Heavy Avoid. Given the rich interplay between Heavy Avoid and derandomization, we believe that investigating the complexity of Heavy Avoid is likely to shed further light on the minimal assumptions required for derandomization.

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2 Preliminaries

2.1 Notation

We use \( U_n \) to denote the uniform distribution over \( \{0, 1\}^n \). For a distribution \( D \) and an element \( x \), we use \( D(x) \) to denote the probability of \( x \) under \( D \).

We say that a probability distribution \( D \) contains a \( \delta \)-heavy element if there is \( x \) in the support of \( D \) such that \( D(x) \geq \delta \). Any such element \( x \) is said to be \( \delta \)-heavy. In this case, we also say that the distribution \( D \) is \( \delta \)-heavy. If an element \( x \) is not \( \delta \)-heavy, then we say it is \( \delta \)-light.

We will often consider a distribution ensemble \( \mathcal{D} = \{D_n\}_{n \geq 1} \), where each \( D_n \) is a distribution supported over \( \{0, 1\}^n \). For convenience, we might simply refer to \( \mathcal{D} \) as a distribution. We let \( \mathsf{PSAMP} \) denote the set of polynomial-time samplable distributions.

We say a probabilistic algorithm \( A \) for a search problem \( \mathcal{P} \) is pseudodeterministic [GG11], if for every input \( x \), there is a canonical \( \mathcal{P} \)-solution \( y \) of \( x \) such that \( A(x) \) outputs \( y \) with probability
\[ \geq 2/3. \] It is easy to see that the success probability can be amplified to \( 1 - \exp(-n) \) by parallel repetition.

### 2.2 The Heavy Avoid Problem

In general, given a distribution \( D \) over \( \{0,1\}^n \) and a parameter \( \delta \in (0,1) \), the Heavy Avoid problem asks to find a string \( x \in \{0,1\}^n \) such that \( D(x) < \delta \). It is easy to see that such a string always exists as long as \( 2^n > 1/\delta \). Consequently, the Heavy Avoid problem is a total search problem. This paper mainly focuses on the regime where \( 1/\delta \) is significantly smaller than \( 2^n \), such as \( \delta = 1/\text{poly}(n) \) or even just \( \delta \approx 1/\log n \).

In this paper, we will consider the Heavy Avoid problem in different settings, depending on whether the sampler for \( D \) implicitly samples the distribution and whether it is computed uniformly.\footnote{Do not confuse the uniformity of the sampler with the distribution \( D_n \), which most often in this work will not be the uniform distribution.}

- We say a distribution \( D \) over \( \{0,1\}^n \) is implicit (or, locally-samplable) if there is an efficient procedure that given an integer \( i \) and the randomness \( r \) used by the sampler, outputs the \( i \)-th bit of the sample according to \( r \). In the typical parameter regime, \( D \) runs in time \( t \approx \text{poly}(\log n, |r|) \) which is much smaller than \( n \). Depending on the context, “efficient procedure” could either mean Turing machines or circuits, as will be addressed in the next bullet. Note that the random string \( r \) is also short and the sampler has sequential access (instead of random access) to \( r \).

  We use the word explicit to describe samplers that take \( \text{poly}(n) \) time, as opposed to implicit samplers.

- We say a family of distribution \( D = \{D_n\}_{n \in \mathbb{N}} \) is uniformly samplable if there is a Turing machine \( M \) that given \( 1^n \) (and access to uniformly random bits), samples from \( D_n \). (Similarly, we often consider a Turing machine \( M(1^n) \) that prints a circuit that samples \( D_n \).) On the other hand, if we only have a (non-uniform) family of circuits \( \{C_n\} \), where each \( C_n \) samples from \( D_n \), then we say the distribution is non-uniformly samplable.

  We will also say that uniformly samplable distributions are sampled in time \( t \), while non-uniformly samplable distributions are sampled in size \( t \).

#### Explicit Maps

**Definition 2.1** (Uniform Heavy Avoid). Let \( D = \{D_n\} \in \text{PSAMP} \), where each \( D_n \) is supported over \( \{0,1\}^n \), and let \( \delta(n) \in [0,1] \). In the \((D, \delta)\)-Heavy-Avoid problem, given \( 1^n \) the goal is to output an element \( x \in \{0,1\}^n \) such that \( D_n(x) < \delta(n) \).

We say that the \((D, \delta)\)-Heavy-Avoid problem can be solved in polynomial time if there is a deterministic algorithm \( A(1^n) \) that runs in polynomial time and solves \((D, \delta)\)-Heavy-Avoid. Similarly, the \((D, \delta)\)-Heavy-Avoid problem can be solved in pseudodeterministic polynomial time if there is a pseudodeterministic algorithm \( A(1^n) \) that runs in polynomial time and solves \((D, \delta)\)-Heavy-Avoid.

**Definition 2.2** (Non-Uniform Heavy Avoid). Let \( C : \{0,1\}^m \rightarrow \{0,1\}^n \) be a Boolean circuit, and let \( D_C \) be the distribution induced by \( C(U_m) \). Let \( \delta \in [0,1] \). In the Non-Uniform-Heavy-Avoid problem, given \( C \) and \( \delta \), the goal is to output an element \( x \in \{0,1\}^n \) such that \( D_C(x) < \delta \).
We may also represent $1/\delta$ in unary when we want to emphasize that we consider the regime where $\delta \geq 1/\text{poly}(n)$. In this case, the input consists of $(C, 1^t)$, let $\delta := 1/t$, and the goal is to output a $\delta$-light element of $D_C$.

We say that Non-Uniform-Heavy-Avoid can be solved in polynomial time if for every constant $c \geq 1$, Non-Uniform-Heavy-Avoid over inputs where the circuit $C$ is of size at most $n^c$ and $\delta \geq 1/n^c$ can be solved in deterministic polynomial time.

Note that it is not hard to solve Non-Uniform-Heavy-Avoid with randomness.

**Proposition 2.3.** Let $c \geq 1$. There is a probabilistic polynomial-time algorithm $A$ such that, given a circuit $C: \{0,1\}^m \rightarrow \{0,1\}^n$ of size at most $n^c$ and a parameter $\delta \geq 1/n^c$, $A$ runs in time polynomial in $n^c$ and outputs with high probability a set $T_{C,\delta}$ of size at most $2 \cdot (1/\delta)$ that contains all $\delta$-heavy elements of $D_C$. Hence, if we output the lexicographically smallest string not in $T_{C,\delta}$, then we obtain a probabilistic polynomial-time algorithm solving Non-Uniform-Heavy-Avoid.

Let $S_{C,\delta} = \{x \in \{0,1\}^c \mid D_C(x) \geq \delta\}$, where $D_C = C(U_m)$. Note that the algorithm $A$ outputs with high probability a set $T_{C,\delta}$ of bounded size such that $S_{C,\delta} \subseteq T_{C,\delta}$. However, different executions of $A$ might produce different sets $T_{C,\delta}$. Consequently, this does not give rise to a pseudodeterministic algorithm for Non-Uniform-Heavy-Avoid.

**Implicit Maps (Locally Samplable Distributions)**

For locally samplable distributions (which will also be called “implicit maps” in this paper), it will be important to fix the following notation. A map, or generator, is a function $G: \{0,1\}^m \rightarrow \{0,1\}^N$ (typically $N \gg m$) such that our input distribution is $G(U_m)$. We say the map is *implicitly computed* by a circuit $C$ if $C: \{0,1\}^m \times [N] \rightarrow \{0,1\}$ satisfies that for every $r \in \{0,1\}^m$, $C(r,i)$ outputs the $i$-th bit of $G(r)$. The input of Implicit-Heavy-Avoid will be a circuit $C$ even though we are actually solving Heavy Avoid on the corresponding instance $G$. (In the uniform case, the circuit $C$ is generated by a uniform procedure, in which case the input to the problem is simply $1^N$.)

Although the input length, $\text{poly}(|C|)$, is usually much smaller than $N$, the output length is still $N$, hence we still measure the time complexity of algorithms solving Implicit-Heavy-Avoid by $N$. For example, we say Implicit-Heavy-Avoid can be solved in deterministic polynomial time if it can be solved by a deterministic machine that runs in time polynomial in $N$.

**Definition 2.4 (C-Implicit-δ-Heavy-Avoid for non-uniform samplers).** Let $C$ be a circuit class, $\delta: \mathbb{N} \rightarrow [0,1]$, $m,N,s: \mathbb{N} \rightarrow \mathbb{N}$ be parameters. We define the $C$-implicit $\delta$-heavy avoid problem for maps that stretch $m(N)$ bits to $N$ bits and are implicitly computed by $C$-circuits of size at most $s(N)$. (A typical parameter regime is that $N = 2^n$ for some integer $n$, $\delta(N) = 1/\text{poly}(n)$, and $m(N),s(N) \leq \text{poly}(n)$.) The parameters will be clear in each statement.

The input of this problem are a $s$-C-circuit $C: \{0,1\}^m \times [N] \rightarrow \{0,1\}$. Recall that this circuit $C$ implicitly defines a map $G: \{0,1\}^m \rightarrow \{0,1\}^N$ such that, for every $r \in \{0,1\}^m$, and $i \in [N]$, $C(r,i)$ outputs $G(r)_i$ (the $i$-th bit of the $N$-bit string $G(r)$). Given $C$, the goal is to output an element $y \in \{0,1\}^N$ such that $\Pr[G(U_m) = y] < \delta$.

Similarly, we can also define C-Implicit-δ-Heavy-Avoid for uniformly-samplable maps. Here, we consider families of maps $\{G_N\}$ that are implicitly computed by DLOGTIME-uniform $C$-circuits $\{C_N\}$ of size at most $s(N)$. In other words, there is a DLOGTIME-uniform sequence $\{C_N\}_{N \geq 1}$ of size-$s(N)$ $C$-circuits such that, for every $N \geq 1$, $r \in \{0,1\}^{m(N)}$, and $i \in [N]$, $C_N(r,i)$ outputs $G_N(r)_i$, i.e., the $i$-th bit of the $N$-bit string $G_N(r)$. (Here, we say $\{C_N\}$ is DLOGTIME-uniform if the direct connection language of $C_N$ can be decided in $O(\log s(N))$ time.)
Definition 2.5 (C-Implicit-δ-Heavy-Avoid for uniform samplers). Let \( m(N), s(N), \delta(N) \) be parameters as above, and \( \{ C_N \} \) be a DLOGTIME-uniform sequence of \( C \)-circuits that defines a family of maps \( \{ G_N \} \). That is, given \( r \in \{0,1\}^m \) and \( i \in [N] \), \( C_N(r,i) \) outputs the \( i \)-th bit of \( G_N(r) \). The C-Implicit-δ-Heavy-Avoid problem corresponding to \( \{ G_N \} \) is the following problem: Given \( 1^N \) the goal is to output a string \( x \in \{0,1\}^N \) such that \( \Pr_r[G_N(r) = x] < \delta(N) \).

Note that the input to the Heavy Avoid problem is given by a circuit when we consider the non-uniform formulations (in both the implicit and explicit settings), while the input to the problem is simply the input length when we consider uniform formulations (since the sampler can be efficiently obtained from the input length).

2.3 Time-Bounded Kolmogorov Complexity

This section reviews some notions from time-bounded Kolmogorov complexity (see, e.g., [LO22] for more details). Let \( U \) be a Turing machine. Given a positive integer \( t \) and a string \( x \in \{0,1\}^* \), we let
\[
K_U^t(x) = \min_{p \in \{0,1\}^*} \left\{ |p| \mid U(p) \text{ outputs } x \text{ in at most } t \text{ steps} \right\}.
\]
We say that \( K_U^t(x) \) is the \( t \)-time-bounded Kolmogorov complexity of \( x \) (with respect to \( U \)). As usual, we fix \( U \) to be a time-optimal machine [LV19], i.e., a universal machine that is almost as fast and length efficient as any other universal machine, and drop the index \( U \) when referring to time-bounded Kolmogorov complexity measures.

For \( x \in \{0,1\}^* \), the probabilistic \( t \)-time-bounded Kolmogorov complexity of \( x \) is defined as
\[
pK_U^t(x) = \min \left\{ k \in \mathbb{N} \mid \Pr_{w \sim \{0,1\}^k} \left[ \exists p \in \{0,1\}^k, U(p,w) \text{ outputs } x \text{ within } t \text{ steps} \right] \geq \frac{2}{3} \right\}.
\]
In other words, if \( k = pK_U^t(x) \), then with probability at least 2/3 over the choice of the random string \( w \), given \( w \) the string \( x \) admits a \( t \)-time-bounded encoding of length \( k \).

We can also consider the randomized \( Kt \) complexity of a string \( x \in \{0,1\}^* \), defined as
\[
rK_t(x) = \min_{t \in \mathbb{N}, p \in \{0,1\}^*} \left\{ |p| + \lfloor \log t \rfloor \mid \Pr_{r \sim \{0,1\}^*} \left[ U(p,r) \text{ outputs } x \text{ in } t \text{ steps} \right] \geq 2/3 \right\}.
\]
All these notions of time-bounded Kolmogorov complexity can be generalized to capture the conditional complexity of \( x \) given \( y \) in the natural way, i.e., by providing \( y \) as an extra input string to the universal machine \( U \).

2.4 Pseudorandomness and Derandomization

Fix an input length \( n \). A generator is simply a multiset \( G \subseteq \{0,1\}^n \). We will consider families of generators \( \{ G_n \}_{n \in \mathbb{N}} \) where each \( G_n \subseteq \{0,1\}^n \) is a generator outputting \( n \)-bit strings. In the literature, it is also common to consider these generators as functions: let \( \ell(n) < n \) denote the seed length of the generator, then the function \( G_n : \{0,1\}^{\ell(n)} \to \{0,1\}^n \) is equivalent to the multiset
\[
\{ G_n(s) : s \in \{0,1\}^{\ell(n)} \}.
\]
In this paper, we will use the subset- and functional definitions of generators interchangeably.

Let \( A : \{0,1\}^n \to \{0,1\} \) be a function, \( H \subseteq \{0,1\}^n \) be a generator, and \( \epsilon > 0 \) be a parameter. We say that \( A \) is \( \epsilon \)-dense if \( \Pr_{x \sim \{0,1\}^n} [A(x) = 1] \geq \epsilon \). We say that \( A \) \( \epsilon \)-avoids \( H \) if \( A \) is \( \epsilon \)-dense,
and for every string $x$ ∈ $H$, we have $\mathcal{A}(x) = 0$. If $\mathcal{A}$ does not $\epsilon$-avoid $H$, then we say that $H$ $\epsilon$-hits $\mathcal{A}$.

Let $\mathcal{A} : \{0, 1\}^n \rightarrow \{0, 1\}$ be a function, $G : \{0, 1\}^\ell \rightarrow \{0, 1\}^n$ be a generator, and $\epsilon > 0$ be a parameter. We say that $\mathcal{A}$ $\epsilon$-distinguishes $G$, if

$$\left| \Pr_{x \sim \{0, 1\}^n} [\mathcal{A}(x) = 1] - \Pr_{s \sim \{0, 1\}^\ell} [\mathcal{A}(G(s)) = 1] \right| > \epsilon;$$

otherwise (if the above inequality does not hold), we say that $G$ $\epsilon$-fools $\mathcal{A}$.

Like many papers in derandomization [Gol11, CT21, LP22, LP23], we will consider promise versions of randomized complexity classes, such as $\text{prRP}$ and $\text{prBPP}$. A promise problem [ESY84] ($\Pi_{\text{YES}}, \Pi_{\text{NO}}$) is a pair of disjoint sets ($\Pi_{\text{YES}} \cap \Pi_{\text{NO}} = \emptyset$). A machine solves the corresponding promise problem if given an input $x \in \{0, 1\}^n$, it outputs 1 when $x \in \Pi_{\text{YES}}$ and outputs 0 when $x \in \Pi_{\text{NO}}$; note that there is no requirement on the behaviour of the machine when $x \notin (\Pi_{\text{YES}} \cup \Pi_{\text{NO}})$.

We also recall the definitions of the canonical $\text{prRP}$-complete problem $\text{GAP-SAT}$ and the canonical $\text{prBPP}$-complete problem $\text{CAPP}$.\footnote{CAPP stands for “Circuit Acceptance Probability Problem.”}

**Definition 2.6 (GAP-SAT).** The problem $\text{GAP-SAT}$ is the following promise problem ($\Pi_{\text{YES}}, \Pi_{\text{NO}}$): $\Pi_{\text{YES}}$ consists of all circuits $C : \{0, 1\}^n \rightarrow \{0, 1\}$ that are $1/10$-dense, and $\Pi_{\text{NO}}$ consists of all circuits $C : \{0, 1\}^n \rightarrow \{0, 1\}$ such that $C(x) = 0$ for every $x \in \{0, 1\}^n$.

**Definition 2.7 (CAPP).** The problem $\text{CAPP}$ is the following promise problem ($\Pi_{\text{YES}}, \Pi_{\text{NO}}$): On input $(C, \ell)$, where $C : \{0, 1\}^n \rightarrow \{0, 1\}$ is a circuit and $\ell \in (0, 1)$ is a number, $\Pi_{\text{YES}}$ consists of $(C, \ell)$ where $\delta \geq \Pr_{x \sim \{0, 1\}^n} [C(x)] + 1/10$, and $\Pi_{\text{NO}}$ consists of $(C, \ell)$ where $\delta \leq \Pr_{x \sim \{0, 1\}^n} [C(x)] - 1/10$.

The constant $1/10$ in the above two definitions is arbitrary and can be amplified to $1/poly(n)$ by parallel repetition.

3 Heavy Avoid and Lower Bounds Against Uniform Probabilistic Circuits

In this section, we study the connection between the Heavy-Avoid problem and the problem of proving lower bounds against uniform probabilistic circuits. Our main result is that in many settings, lower bounds against uniform probabilistic circuits are characterized by the existence of algorithms for Implicit-Heavy-Avoid.

Let $\mathcal{C}$ be a circuit class. A probabilistic $\mathcal{C}$-circuit $E(x; z)$ is a circuit from $\mathcal{C}$ that computes over an input $x$ and an input $z$, where the latter corresponds to the random choice of $E$. We denote $\text{BP-}\mathcal{C}$ the set of languages $L$ that can be computed by a $\text{DLOGTIME}$-uniform sequence of probabilistic $\mathcal{C}$-circuits of polynomial size. We stress that the uniform machine generating the $\mathcal{C}$ circuit is deterministic, while the circuit itself is allowed to make random choices ($z$).

Our results hold for any (uniform probabilistic) circuit class $\mathcal{C}$ that is nice, i.e., satisfies a few technical conditions. More precisely, we say a circuit class $\mathcal{C}$ is nice if the following holds:

- $(\mathcal{C}$ contains $\text{AC}^0[\oplus].) \text{AC}^0[\oplus] \subseteq \mathcal{C}$.

- $(\mathcal{C}$ is closed under composition.) For every language $L \in \mathcal{C}$ and every $\text{DLOGTIME}$-uniform family of oracle circuits $\{C_n^{L_n}\}_{n \in \mathbb{N}}$ making non-adaptive projection queries where the top (i.e., post-processing) circuit is in $\mathcal{C}$, the language computed by the circuit family $\{C_n^{L_n}\}_{n \in \mathbb{N}}$ is still in $\text{DLOGTIME}$-uniform $\mathcal{C}$.\footnote{CAPP stands for “Circuit Acceptance Probability Problem.”}
• (C admits universal circuits.) There is a DLOGTIME-uniform family of C-circuits Eval such that given the description of a C-circuit C (i.e., the truth table of the direct connection language of C) and an input x, Eval((C), x) outputs C(x).

In the case that C is the union of depth-d circuits for every constant d, such as AC^0 or TC^0, we allow Eval to have higher depth than C: for every fixed depth d, the circuit evaluation problem can be solved by a family of DLOGTIME-uniform C-circuit of constant depth.

It is not hard to check that many standard circuit classes considered in the literature are nice, e.g., AC^0[⊕], ACC^0, TC^0, NC^1, P/poly. For instance, universal circuits for NC^1 are constructed in [Bus87], while universal circuits for TC^0 can be built using the universal threshold function (see, e.g., [BW05]) and standard techniques.

A note on notation: throughout this section, when we use parameters n and N together, we implicitly assume N = 2^n. We switch back and forth between the two parameters based on which one is more natural in a given context.

### 3.1 Equivalences for PSPACE via Instance Checkers

Our equivalences for PSPACE follow from the existence of instance checkers [BK95, TV07] for PSPACE-complete languages. To establish our equivalences with respect to restricted circuit classes, we use a recent construction of AC^0[⊕]-computable instance checkers by Chen [Che23].

Below, we say that an oracle circuit \( E(\cdot)(x, z) \) from BP-\( C \) makes projection queries if every query it makes to the oracle can be computed by a projection over the inputs \((x, z)\). After gathering the answers of the oracles, the final output is computed by a C circuit over \((x, z)\) and these oracle answers. We stress that any oracle circuit that makes projection queries is non-adaptive. When we say such a circuit is DLOGTIME-uniform, we mean that both the projection (computing the queries to the oracles) and the top C circuit are DLOGTIME-uniform.

**Theorem 3.1** (A PSPACE-Complete Language with Useful Properties). There is a language \( L^* \subseteq \{0,1\}^* \) with the following properties:

1. (Complexity Upper Bound) \( L^* \in \text{PSPACE} \).
2. (Completeness) \( L^* \) is \( \text{PSPACE} \)-hard under DLOGTIME-uniform projection reductions.
3. (Instance Checkability) There is a DLOGTIME-uniform family of BP-AC^0[⊕] oracle circuits \( \{IC_n\}_{n \geq 1} \) making projection queries such that, on every input string \( x \in \{0,1\}^n \) and for every oracle \( \mathcal{O} \subseteq \{0,1\}^* \), the following holds:
   - \( IC_n(x) \) only makes queries of length \( n \) to \( \mathcal{O} \).
   - If \( \mathcal{O} \) agrees with \( L^* \) on inputs of length \( n \), then \( \Pr_r[IC_n(x;r) = L^*(x)] = 1 \).
   - For every oracle \( \mathcal{O} \), \( \Pr_r[IC_n(x;r) \in \{ \bot, L^*(x) \}] \geq 1 - \exp(-n) \).

Theorem 3.1 follows from [Che23, Section 7]; we refer the reader to Appendix B for more details.

We say that the \( C \)-Implicit-\( \delta \)-Heavy-Avoid problem corresponding to a given family \( \{G_N\} \) of implicitly computed maps can be solved in space \( s(N) \) if there is an algorithm \( A \) of space complexity \( s(N) \) such that, for every input length \( N \), there is some \( x \in \{0,1\}^N \) for which \( A(1^N, i) \) outputs the \( i \)-th bit of \( x \) for all \( i \in [N] \), and \( x \) is a solution to the \( C \)-Implicit-\( \delta \)-Heavy-Avoid problem for \( G_N \).

**Theorem 3.2** (Equivalence for PSPACE). Let \( C \) be a nice class of Boolean circuits. The following statements are equivalent:
(i) \( \text{PSPACE} \not\subseteq \text{BP-C} \).

(ii) For every choice of \( c,d,\ell \in \mathbb{N} \), with \( m(N) = n^d \) and \( \delta(N) = 1/n^c \), and for every sequence \( \{G_N\} \) of maps \( G_N : \{0,1\}^{m(N)} \rightarrow \{0,1\}^{N} \) implicitly computed by \( \text{DLOGTIME} \)-uniform \( C \)-circuits of size at most \( n^k \), the corresponding \( C \)-\text{Implicit-}\( \delta \)-\text{Heavy-Avoid} problem can be solved in space \( O(\log N) \) on infinitely many input lengths \( N \).

\textbf{Proof.} We consider each implication below.

\((ii) \Rightarrow (i)\). Using the assumption, we show below that for every choice of \( k \geq 1 \), there is \( L \in \text{DSPACE}[n^2] \) such that \( L \) cannot be computed by \( \text{DTIME}[k \cdot \log n] \)-uniform randomized \( C \)-circuits of size \( n^k \).\(^{18}\) Since there exist \( \text{PSPACE} \)-complete problems, a standard argument shows that this implies \( \text{PSPACE} \not\subseteq \text{BP-C} \).

Fix a large enough \( k \geq 1 \), and consider the map \( G_N : \{0,1\}^{m(N)} \rightarrow \{0,1\}^{N} \) defined as follows, where \( m(N) := n^{2k} \). The map \( G_N \) views its input string \( x \) as a pair \( (M,r) \), where \( M \) is the description of a clocked deterministic machine running in time \( 10k \cdot \log n \), and \( r \) is the rest part of \( x \), treated as a random string. We assume that this encoding satisfies that for a random \( x \), every machine \( M \) of description length \( \ell \) occurs with probability \( \Theta(2^{-\ell}/\ell^2) \) (this is possible since \( \sum_{\ell \geq 1} \frac{1}{\ell^2} \) is bounded). The important part is that if the description length of \( M \) is a constant, then it occurs with constant probability. Let \( D_M : \{0,1\}^{2k} \times \{0,1\}^{n} \rightarrow \{0,1\} \) be the \( C \)-circuit of size at most \( n^{2k} \) encoded by the machine \( M(1^n,\cdot) \) (i.e., we assume that \( M \) computes the direct connection language of \( D_M \)). For \( i \in \{0,1\}^{n} \), we define the \( i \)-th output bit of \( G_N(x) \) as \( D_M(r,i) \). Note that a uniform computation over inputs of length at most \( 10k \cdot \log n \) and running in time \( 10k \cdot \log n \) can be uniformly converted into an \( AC^0 \) circuit of size at most \( n^{10k} \). Since \( C \) contains \( AC^0 \), admits universal circuits, and is closed under composition, \( G_N \) can be implicitly computed by a \( \text{DLOGTIME} \)-uniform probabilistic \( C \)-circuit \( C_N \) defined over \( m(N) + n \) input bits and of size at most \( n^{C \cdot k} \), where \( C \) is a large enough universal constant that depends only on the circuit class \( C \).

Let \( B(1^N) \) be an algorithm of space complexity \( O(\log N) \) that solves \( C \)-\text{Implicit-}\( \delta \)-\text{Heavy-Avoid} on infinitely many values of \( N \) for the sequence \( G_N \), with parameters \( c,d \leq C \cdot k \) and function \( \delta(N) \leq o(1) \). Let \( L_B \) be the language defined by \( B \), i.e., a string \( z \in \{0,1\}^{n} \) is in \( L_B \) if and only if the \( z \)-th bit of \( B(1^N) \) (with \( N = 2^n \)) is 1. Note that \( L_B \) is in \( \text{DSPACE}[O(n)] \).

We now argue that \( L_B \) cannot be computed by \( \text{DTIME}[k \cdot \log n] \)-uniform randomized \( C \)-circuits of size \( n^k \). To prove this, it is enough to show that for every language \( L \) computed by such circuits, each string in the sequence \( \{y^L_N\}_N \) of truth-tables obtained from \( L \) is \( \delta \)-heavy in \( G_N(U_{m(N)}) \) for every large enough \( N \). Under this claim, since \( B \) solves \( C \)-\text{Implicit-}\( \delta \)-\text{Heavy-Avoid} for the sequence \( \{G_N\} \), it follows that \( L_B \neq L \).

To see that the claim holds, recall that \( L \) is computed by \( \text{DTIME}[k \cdot \log n] \)-uniform randomized \( C \)-circuits of size \( n^k \). Consequently, there is a deterministic machine \( M_L \) that runs in time \( k \cdot \log n \) and decides the direct connection language of a corresponding randomized \( C \)-circuit \( D_L \) of size at most \( n^{2k} \) and using at most \( n^k \) random bits that computes \( L \) on \( n \)-bit inputs. We now boost the success probability of the circuit \( D_L \) via repetition and (approximate) majority vote. More precisely, since the approximate majority function can be computed by \( \text{DLOGTIME} \)-uniform \( AC^0 \) circuits \([\text{Ajt90, Vio09}]\), \( C \) contains \( AC^0 \), and \( C \) is closed under composition, there is a deterministic machine \( M_L \) that runs in time \( 10k \cdot \log n \) and decides the direct connection language of a corresponding randomized \( C \)-circuit \( D_L \) of size at most \( n^{C \cdot k} \) and using at most \( n^{2k} \) random bits that computes

\(^{18}\) If \( C \) is a constant-depth circuit class defined as a union of classes for each fixed depth \( k \), the argument can be adapted accordingly.
L on each n-bit input string with probability at least $1 - 2^{-2n}$. Moreover, we can assume that the description length of $M_L$ is a constant $\ell_{\text{desc}}$, hence it occurs with constant probability. Let $y^L_N$ be the truth-table of $L$ on input length $n$, i.e., $|y^L_N| = N = 2^n$. By construction, using an union bound over all $n$-bit input strings, the probability that $G_N(U_{m(N)}) = y^L_N$ is at least $\Omega(2^{-\ell_{\text{desc}}}/\ell_{\text{desc}}^2) \cdot (1 - 2^{-n}) \geq \Omega(1) > \delta$, for large enough $N$. This shows that $y^L_N$ is $\delta$-heavy, concluding the proof of this item.

(i) $\Rightarrow$ (ii). We argue in the contrapositive. In other words, suppose that there is a choice of constants $c$, $d$, and $\ell$, with $m(N) = n^d$ and $\delta(N) = 1/n^\ell$, and a sequence $G_N: \{0, 1\}^{m(N)} \to \{0, 1\}^N$ implicitly computed by DLOGTIME-uniform $C$-circuits of size $n^c$ such that every algorithm $A(1^N)$ running in space $O(\log N)$ time fails to solve $C$-Implicit-$\delta$-Heavy-Avoid on every large enough input length $N$. Next, we use this assumption to establish that $\text{PSPACE} \subseteq \text{BP}-C$, which concludes the proof.

We consider a candidate algorithm $A(1^N)$ that computes as follows. Consider the language $L^*$ from Theorem 3.1, and assume that $L^* \in \text{DSPACE}[n^a]$, where $a \in \mathbb{N}$. For a given $N' = 2^n$, we let $tt^*_N \in \{0, 1\}^{N'}$ denote the truth table of $L^*$ over inputs of length $n'$. On input $1^{N}$, algorithm $A$ outputs the string $y_N = tt^*_N \circ 0^N \in \{0, 1\}^N$, where $N' = 2^n$ for $n' = n^{1/a}$, and $N = N - N'$.

Note that $A$ computes in space $O(\log N)$, due to our choice of parameters. Therefore, $A$ fails to solve $C$-Implicit-$\delta$-Heavy-Avoid on every large enough input length $N$. This means that for every large enough $N$ the probability of $y_N$ under $G_N(U_{m(N)})$ is at least $\delta = 1/n^\ell$.

We first describe a randomized algorithm that computes $L^*$, deferring for now a discussion of its correctness, circuit complexity, and uniformity. Let $n = (n')^a$, as above. To compute $L^*$ on a given input $x$ of length $n' \in \mathbb{N}$, we sample $v = n^{3\ell}$ strings $z_1, \ldots, z_v \in \{0, 1\}^{m(N)}$ uniformly and independently at random, and use the $2n'$-bit prefixes $O_1', \ldots, O_v'$ of the corresponding oracles $O_1, \ldots, O_v$ as candidate oracles for $L^*$ on input length $n'$, where each $O_i$ is the oracle associated with the string $G_N(z_i) \in \{0, 1\}^N$. In more detail, let $b_i = \text{IC}_{n'}^{O_i'}(x)$, where $\text{IC}_{n'}$ is the algorithm from Theorem 3.1. We output 1 if at least one bit among $b_1, \ldots, b_v$ is 1, and 0 otherwise.

Next, we argue that $A$ computes $L^*$ with high probability. Consider an arbitrary input length $n'$ and a given input string $x \in \{0, 1\}^{n'}$. By our choice of $v$, with high probability the string $y_N$ appears among the strings $G_N(z_1), \ldots, G_N(z_v)$. In particular, with high probability the truth table $tt^*_N$ appears as an $N'$-bit prefix of one of these strings, meaning that one of the oracles $O_i'$ computes $L^*$ on inputs of length $n'$. Consequently, in this case, if $L^*(x) = 1$ then at least one bit $b_i = 1$, and the procedure outputs 1. On the other hand, if $L^*(x) = 0$, then by a union bound over the internal randomness of $\text{IC}_{n'}$, with high probability every bit $b_i \in \{0, \bot\}$. In this case, the procedure outputs 0. This establishes the correctness of $A$.

It remains to establish an upper bound on the circuit complexity of $A$ and to analyze the uniformity of the corresponding circuits. Note that each bit $b_i \in \{0, 1, \bot\}$ can be computed by a randomized $C$-circuit of polynomial size, since $G_N$ is implicitly computed by $C$-circuits of polynomial size, $\text{IC}_{n'}$ is computable by randomized $C$-circuits of polynomial size, and $C$ is closed under composition. Moreover, the disjunction of the bits $b_i$ can also be computed in $C$, since this class contains $\text{AC}^{O}(\oplus)$. Therefore, $A$ can be implemented by randomized $C$-circuits of polynomial size. Finally, it is not hard to check that the corresponding sequence of randomized $C$-circuits is DLOGTIME uniform, since $\text{IC}$ is computed by DLOGTIME-uniform randomized circuits, and $G_N$ is implicitly computed by DLOGTIME-uniform circuits.

The above discussion implies that $L^* \in \text{BP}-C$. Since $L^*$ is complete under DLOGTIME-uniform projection reductions, we get that $\text{PSPACE} \subseteq \text{BP}-C$, as desired.

Our characterizations also extend to almost-everywhere lower bounds and sub-exponential lower
Theorem 3.3. Let $\mathcal{C}$ be a nice class of Boolean circuits. The following statements are equivalent:

(i) $\text{PSPACE} \not\subset \text{BP-}\mathcal{C}$.

(ii) For every choice of $c, d, \ell \in \mathbb{N}$, with $m(N) = n^d$ and $\delta(N) = 1/n^\ell$, and for every sequence $\{G_N\}$ of maps $G_N : \{0,1\}^{m(N)} \rightarrow \{0,1\}^N$ implicitly computed by $\text{DLOGTIME}$-uniform $\mathcal{C}$-circuits of size at most $n^c$, the corresponding $\mathcal{C}$-$\text{Implicit-}\delta$-$\text{Heavy-Avoid}$ problem can be solved in $O(\log(N))$ space for all large enough $N$.

Proof Sketch. The result follows from the same argument given for Theorem 3.2:

- (ii) $\Rightarrow$ (i): If our algorithm $B$ is correct on input $1^N$, then our language $L$ is hard on input length $n$.
- (i) $\Rightarrow$ (ii): If the language $L^\star$ is hard on input length $n'$, then our algorithm $A$ is correct on input $1^N$ where $N = 2^{(n')^a}$.

We use $\text{BP-}\mathcal{C}-\text{SIZE}[f(n)]$ to denote the class of languages computable by $\text{DLOGTIME}$-uniform $\text{BP-}\mathcal{C}$ circuits of size $f(n)$.

Theorem 3.4. Let $\mathcal{C}$ be a nice class of Boolean circuits. The following statements are equivalent:

(i) There is a constant $\epsilon > 0$ such that $\text{PSPACE} \not\subset \text{BP-}\mathcal{C}$-$\text{SIZE}[2^{n^a}]$.

(ii) There is a constant $\epsilon > 0$ such that for $\delta(N) := 2^{-n^a}$ and for every sequence $\{G_N\}$ of maps $G_N : \{0,1\}^{2n^a} \rightarrow \{0,1\}^N$ implicitly computed by $\text{DLOGTIME}$-uniform $\mathcal{C}$-circuits of size $2^{n^a}$, the corresponding $\mathcal{C}$-$\text{Implicit-}\delta$-$\text{Heavy-Avoid}$ problem can be solved in $\text{poly}(N)$ time on infinitely many input lengths $N$.

Proof Sketch. The argument is an adaptation of the proof of Theorem 3.2 by adjusting a few parameters, so we refer the reader to that proof for more details.

To see that (ii) $\Rightarrow$ (i) holds, let $\epsilon' := \epsilon/4$, and consider the map $G_N : \{0,1\}^{m(N)} \rightarrow \{0,1\}^N$ where $m(N) := 2^{2n^a}$ and the input bits are parsed into the description of a Turing machine $M$ that encodes a size-$2^{2n^a}$ $\mathcal{C}$-circuit $D_M$ and the rest random inputs (fed to $D_M$). Like in Theorem 3.2, we assume that every constant-size Turing machine occurs with constant probability. This map $G_N$ can be implicitly computed by a $\text{DLOGTIME}$-uniform probabilistic $\mathcal{C}$-circuit $C_N$ of size $2^{n^a}$, in the sense that for every $x \in \{0,1\}^{m(N)}$ and $i \in [N]$, the $i$-th bit of $G_N(x)$ is equal to $C_N(x,i)$. We can see that for every language $L \in \text{BP-}\mathcal{C}$-$\text{SIZE}[2^{n^a}]$, let $y_N^L$ denote the truth table of $L_n$, then the probability that $G_N(U_m(N)) = y_N^L$ is at least a constant. Hence, given an algorithm $B(1^N)$ that solves the $\mathcal{C}$-$\text{Implicit-}\delta$-$\text{Heavy-Avoid}$ problem for $\delta = o(1)$ (on infinitely many $N$), the language whose truth table is the output of $B(1^N)$ is not in $\text{BP-}\mathcal{C}$-$\text{SIZE}[2^{n^a}]$ (on infinitely many $N$). Since we further assumed that $B$ runs in space $O(\log(N))$, we obtain a hard language in $\text{SPACE}[O(\log(N))] = \text{SPACE}[O(n)]$ that is not in $\text{BP-}\mathcal{C}$-$\text{SIZE}[2^{n^a}]$.

To see that (i) $\Rightarrow$ (ii) holds, let $L^\star$ denote the $\text{PSPACE}$-complete language in Theorem 3.1 and let $\epsilon' := \epsilon/(5a)$, where $a \geq 1$ is a constant such that $L^\star \in \text{SPACE}[n^a]$. Consider the following algorithm $A(1^N)$ for solving the $\mathcal{C}$-$\text{Implicit-}\delta$-$\text{Heavy-Avoid}$ problem with parameter $\epsilon'$. On input $1^N$, let $n' := n'^{1/a}$, $N' := 2^{n'}$, $y_{n'} \in \{0,1\}^{N'}$ be the truth table of $L^\star$ on input length $n'$, then $A$ outputs $y_{n'}0^{N-N'} \in \{0,1\}^N$. If $A$ fails to solve $\mathcal{C}$-$\text{Implicit-}\delta$-$\text{Heavy-Avoid}$, then we can compute $L^\star$ in $\text{BP-}\mathcal{C}$-$\text{SIZE}[2^{n^a}]$ as follows. Let $x \in \{0,1\}^{n'}$ be an instance of $L^\star$, we set $n := (n')^a$ and $N' := 2^{n'}$. We
sample \( v := (1/\delta(N))^3 \leq 2^{3n^c} \) strings \( z_1, \ldots, z_v \in \{0,1\}^{2n^c} \) uniformly and independently at random, and for each string \( z_i \) we define an oracle \( O_i: \{0,1\}^{n^c} \to \{0,1\} \) whose truth table is the first \( N' \) bits of \( G_N(z_i) \). We then run \( IC \) against each \( O_i \) and obtain \( b_i := IC_{N'}^O(x) \) for each \( i \in [v] \), and finally we output 1 if some \( b_i \) is equal to 1. This algorithm computes \( L^* \) because with high probability, the truth table \( y_{n^c} \) appears in these oracles, and also the instance checker will never output 1 \(-\) \( L(x) \) by mistake. Our algorithm can be implemented in \( \text{BP-C-SIZE}[2^{n^c}] \subset \text{BP-C-SIZE}[2^{(n^c)^r}] \). \( \square \)

Remark 3.5. In the proof of Theorem 3.2, we only need to solve the \( C \)-Implicit-\( \delta \)-Heavy-Avoid problem for \( \delta = o(1) \) to obtain the lower bound (i.e., Item \((i)\)), while the latter implies algorithms for the \( C \)-Implicit-\( \delta \)-Heavy-Avoid problem even when \( \delta = 1/\text{poly}(n) = 1/\text{polylog}(N) \). This illustrates the robustness of the parameter \( \delta \) in \( C \)-Implicit-\( \delta \)-Heavy-Avoid with respect to \( O(\log N) \)-space algorithms: if the problem is solvable for \( \delta = o(1) \), then it is also solvable for \( \delta = 1/\text{polylog}(N) \). Similarly, Theorem 3.4 shows that if we consider implicit maps computable in the \( 2^{n^c} \) time regime, then this problem is solvable for \( \delta = o(1) \) if and only if it is solvable for \( \delta = 2^{-n^c} \). In fact, it is evident from the proofs that the robustness of the parameter \( \delta \) holds in every characterization result in Section 3.

3.2 Equivalences for \( \text{NP} \) via Search-to-Decision Reductions

In this section, we show equivalences between uniform randomized lower bounds for \( \text{NP} \) and Heavy Avoid algorithms implementable by constant-depth circuits. \( \text{NP} \) is not known to be instance-checkable and so we cannot use the technique from the previous section. However, it turns out that search-to-decision reductions can also be used to argue the desired equivalences. The standard search-to-decision reduction is highly sequential, so in order to show equivalences that work for any nice circuit class, we use a depth-efficient version based on the Valiant-Vazirani Isolation Lemma \([VV86]\) which exploits our access to randomness.

We first need a generalization of the standard result that \( \text{NP} \not\subset \text{BPP} \iff \text{PH} \not\subset \text{BPP} \).

Lemma 3.6. Let \( C \) be a nice circuit class. \( \text{NP} \not\subset \text{BP-C} \) if and only if \( \text{PH} \not\subset \text{BP-C} \).

Proof Sketch. The proof is essentially the same inductive argument as for the standard equivalence between \( \text{NP} \not\subset \text{BPP} \) and \( \text{PH} \not\subset \text{BPP} \). We must show that if \( \text{NP} \subset \text{BPP} \) then \( \text{PH} \subset \text{BPP} \). In order to implement that argument, we need to be able to do error reduction to exponentially small error by \( \text{DLOGTIME} \)-uniform randomized \( C \) circuits, which holds since \( C \) contains \( \text{AC}^0 \), and Approximate Majority can be computed in \( \text{DLOGTIME} \)-uniform \( \text{AC}^0 \) \([Ajt90, Vio09]\). We also need the closure of \( C \) under composition to be able to do induction, but that also holds by niceness of \( C \). \( \square \)

Now we proceed to our equivalences for \( \text{NP} \).

Theorem 3.7 (Equivalences for \( \text{NP} \)). Let \( C \) be a nice circuit class. The following statements are equivalent:

(i) \( \text{NP} \not\subset \text{BP-C} \).

(ii) There are positive integers \( k \) and \( r \) such that for every \( c, d, \ell \in \mathbb{N} \), with \( m(N) = n^d \) and \( \delta(N) = 1/n^\ell \), and for every sequence \( \{G_N\} \) of maps \( G_N: \{0,1\}^{m(N)} \to \{0,1\}^N \) implicitly computed by \( \text{DLOGTIME} \)-uniform \( C \)-circuits of size at most \( n^c \), the corresponding \( C \)-Implicit-\( \delta \)-Heavy-Avoid problem can be solved by \( \text{DLOGTIME} \)-uniform unbounded fan-in circuits of size \( 2^{\log(N)^r} \) and depth \( k \) on infinitely many input lengths \( N \).
Proof. We consider each implication below.

(ii) ⇒ (i). We will use the assumption to show that PH \not\subseteq BP-C, and the desired implication then follows from Lemma 3.6 and the assumption that \mathcal{C} is nice.

As in the proof of the analogous equivalence for PSPACE, fix a large enough \(a \geq 1\), and consider the map \(G_N: \{0,1\}^{m(N)} \rightarrow \{0,1\}^N\) defined as follows, where \(m(N) := n^{2a}\). The map \(G_N\) parses its input string \(x\) into \((M,r)\), where \(M\) is the description of a clocked deterministic machine running in time \(10a \cdot \log n\), and \(r\) consists of the remaining bits of \(x\), treated as randomness. Let \(D_M: \{0,1\}^{2n} \times \{0,1\}^n \rightarrow \{0,1\}\) be the randomized \(\mathcal{C}\) circuit of size at most \(n^{2a}\) encoded by the machine \(M(1^n, \cdot)\) (i.e., we assume that \(M\) computes the direct connection language of \(D_M\)). For \(i \in \{0,1\}^n\), we define the \(i\)-th output bit of \(G_N(x)\) as \(D_M(r, i)\). Note that \(G_N\) can be implicitly computed by a \(\text{DLOGTIME}\)-uniform randomized \(\mathcal{C}\) circuit \(C_N\) defined over \(m(N) + n\) input bits and of size at most \(n^{C-a}\), where \(C\) is a large enough universal constant.

As per assumption, let \(\{C_N\}\) be a \(\text{DLOGTIME}\)-uniform family of unbounded fan-in circuits of size \(2^{\log(N)^r}\) and depth \(k\) such that for infinitely many \(N\), \(C_N\) solves \(\text{C-Implicit-\(\delta\)-Heavy-Avoid}\) on \(G_N\), with parameters \(c = C \cdot a\), \(d = 2a + 1\), and \(\delta(N) = o(1)\). (Recall that each output bit of \(G_N\) is computed in time \(n^c\), \(m(N) \leq n^d\), and we want to find a \(\delta(N)\)-light element.) Let \(L\) be the language defined by \(\{C_N\}\), i.e., a string \(z \in \{0,1\}^n\) is in \(L\) if and only if the \(z\)-th bit of \(C_N(1^N)\) (with \(N = 2^n\)) is 1. Note that there are integers \(s\) and \(k'\) (depending only on \(r\) and \(k\)) such that \(L\) is in \(\Sigma_{k'}\text{-TIME}[n^s]\) by the known equivalence [BIS90] between \(\text{PH}\) and \(\text{DLOGTIME}\)-uniform circuits of exponential size in \(n\) (which is quasi-polynomial size in \(N\)).

We now argue that \(L\) cannot be computed by \(\text{DTIME}[a \cdot \log n]\)-uniform randomized \(\mathcal{C}\) circuits of size \(n^a\). To prove this, it is enough to show that for every language \(L'\) computed by such circuits, each string in the sequence \(\{y^{L'}_N\}_N\) of truth-tables obtained from \(L'\) is \(\delta\)-heavy in \(G_N(U_{m(N)})\) for every large enough \(N\). Under this claim, since \(B\) solves \(\text{C-Implicit-\(\delta\)-Heavy-Avoid}\) for the sequence \(\{G_N\}\), it follows that \(L \neq L'\).

To see that the claim holds, suppose that \(L'\) is computed by \(\text{DTIME}[a \cdot \log n]\)-uniform randomized \(\mathcal{C}\) circuits \(D_{L'}\) of size \(n^a\). Consequently, there is a deterministic machine \(M_{L'}\) that runs in time \(a \cdot \log n\) and decides the direct connection language of a corresponding randomized \(\mathcal{C}\) circuit \(D_{L'}\), and the circuit \(D_{L'}\) computes \(L'\) on \(n\)-bit inputs, has size at most \(n^{2a}\) and uses at most \(n^a\) random bits. We can then reduce the error of the circuit \(D_{L'}\) to be exponentially small by using the facts that Approximate Majority is in \(\text{DLOGTIME}\)-uniform \(\text{AC}^0\) [Ajt90, Vio09] and that \(\mathcal{C}\) is nice. Thus we obtain a family of randomized Boolean circuits \(\tilde{D}_L\) that has size at most \(n^{C-a}\), uses at most \(n^{2a}\) random bits, and computes \(L\) on each \(n\)-bit input string with probability at least 1 \(- 2^{-2n}\). Moreover, there is a deterministic machine \(\tilde{N}_{L'}\) that runs in time \(10a \cdot \log n\) and decides the direct connection language of \(\tilde{D}_L\). Since the description length of \(\tilde{N}_{L'}\) is constant, it occurs with constant probability in the distribution sampled by \(G_N\). Let \(y^{L'}_N\) be the truth-table of \(L'\) on input length \(n\), i.e., \(|y^{L'}_N| = N = 2^n\). By construction, using an union bound over all \(n\)-bit input strings, the probability that \(G_N(U_{m(N)}) = y^{L'}_N\) is at least \(\Omega(1) \cdot (1 - 2^{-n}) \geq \delta(N)\). This shows that \(y^{L'}_N\) is \(\delta\)-heavy, concluding the proof of the claim.

Note that the language \(L\) defined above depends on \(a\), however by the standard fact that there is a language \(L_{\text{comp}}\) complete for \(\Sigma_{k'}\text{-TIME}[n^s]\) under \(\text{DLOGTIME}\)-uniform projections of linear size, we get that for each \(a\), \(L_{\text{comp}}\) is not computed by \(\text{DTIME}[a \cdot \log n]\)-uniform randomized \(\mathcal{C}\) circuits of size \(n^a\). This implies that \(L_{\text{comp}} \not\subseteq \text{BP-C}\), and hence that \(\text{PH} \not\subseteq \text{BP-C}\). Therefore \(\text{NP} \not\subseteq \text{BP-C}\) by Lemma 3.6, concluding the proof of this item.

(i) ⇒ (ii). We argue in the contrapositive. Suppose that there is a choice of constants \(c,d\), and \(\ell\), with \(m(N) = n^d\) and \(\delta(N) = 1/n^\ell\), and a sequence \(G_N: \{0,1\}^{m(N)} \rightarrow \{0,1\}^N\) implicitly
computed by DLOGTIME-uniform $C$ circuits of size $n^c$ such that every DLOGTIME-uniform sequence of unbounded fan-in circuits of size $2^{\log(N)^2}$ and depth 3 fails to solve $C$-Implicit-$\delta$-Heavy-Avoid on every large enough input length $N$. We use this assumption to establish that $\text{NP} \subseteq \text{BP}-C$, which concludes the proof.

We consider a candidate algorithm $A(1^N)$ that simply outputs $tt_N$, where $tt_N$ is the truth table of SAT on $n$-bit inputs. We consider a standard encoding of SAT in which SAT is depth-efficiently paddable, i.e., there is an algorithm Pad implemented by DLOGTIME-uniform $\text{AC}^0$ circuits which, given as inputs $1^t$ for positive integer $t$ and a formula $\phi$ of length at most $t$, outputs an equisatisfiable formula $\phi'$ of length $t$. Note that $A$ can be implemented by DLOGTIME-uniform unbounded fan-in circuits of size $2^{\log(N)^2}$ and depth 3, using the known simulation of non-deterministic quasi-linear time by uniform unbounded fan-in circuits [BIS90]. By assumption, $A$ fails to solve $C$-Implicit-$\delta$-Heavy-Avoid on every large enough input length $N$. We show how to use this failure together with a depth-efficient randomized search-to-decision reduction based on the Valiant-Vazirani Isolation Lemma [VV86] and depth-efficient paddability of SAT to solve SAT in BP-C, which implies $\text{NP} \subseteq \text{BP}-C$ by the NP-completeness of SAT with respect to DLOGTIME-uniform projections.

By the failure of $A$, we have that for every large enough $N$, the probability of $tt_N$ under $G_N(U_m(N))$ is at least $\delta = 1/n^\ell$. First, we describe a polynomial-time randomized algorithm $B$ to solve SAT. Let $\phi$ be a length-$n$ input to SAT. For some $T = \text{quasipoly}(N)$ and $t = \log T$ to be determined later, we sample $v := t^{5d}$ strings $z_1, \ldots, z_v \in \{0, 1\}^{m(T)}$ uniformly and independently at random, and for each string $z_i$, we define an oracle $O_i$ whose truth table is the string $G_T(z_i) \in \{0, 1\}^T$. For each $i \leq v$, we try to use $O_i$ and a depth-efficient search-to-decision reduction to find a satisfying assignment to $\phi$, as follows. Assume without loss of generality that $\phi$ has $n$ variables. We do the following for each $i$ in parallel. We check if $O_i(\text{pad}(1^t, \phi)) = 1$. If this is not the case for any $i$, we reject. If $O_i$ does evaluate to 1 on the padded version of $\phi$, we use this oracle to find a candidate satisfying assignment $w$ to $\phi$ as follows. The idea is to use the Valiant-Vazirani technique of intersecting the solution space of $\phi$ with $k$ randomly chosen hyperplanes for $k = 1 \ldots n$ to obtain formulas $\phi_1, \ldots, \phi_n$. The Valiant-Vazirani Isolation Lemma [VV86] states that if $\phi$ is satisfiable, then with probability at least $1/4n$ over random choices of these formulas, some $\phi_j$ has a unique solution. Note that each $\phi_j$ can be constructed from $\phi$ by randomized constant-depth circuits. We would like to use $O_i$ to find and check the unique satisfying assignment so that we can verify that $\phi$ is satisfiable. An issue is that the $\phi_j$ are in general of size larger than $n$, but they are still of size $\text{poly}(n)$ and we choose $t$ a large enough polynomial in $n$ so that they can all be padded to length $t$. For each $\phi_j$ and each of the $n$ original variables $x_k$ in $\phi$, we use an oracle call to $O_i$ (using padding if necessary) to determine if there is a satisfying assignment to $\phi$ with the variable $x_k$ set to 0. If yes, we set the wire $b_{j,k}$ to 1, else to 0. We check if there is a $j$ such that the assignment $x_k = b_{j,k}$ for each $k$ satisfies $\phi$. If this is the case for some $i$, we accept, otherwise we reject. Note that all of the above can be implemented in constant-depth, apart from the oracle calls to $O_i$, which we simulate by evaluations of the implicit sampler for $O_i$.

By the niceness of $C$, specifically the assumptions that $\text{AC}^0[\oplus]$ is contained in $C$ and $C$ is closed under composition, as well as the fact that our sampler is implicitly computed by uniform $C$ circuits, there are DLOGTIME-uniform randomized $C$ circuits of polynomial size implementing the procedure above. We need to argue that these circuits correctly solve SAT with high probability. Note that a circuit only accepts a formula $\phi$ if it verifies that some assignment satisfies $\phi$. Hence it is enough to check that satisfiable $\phi$ is accepted with high probability. By our choice of $v$, with high probability the string $tt_T$ appears with multiplicity $\omega(n)$ among the strings $G_T(z_1), \ldots, G_T(z_v)$, meaning that $\omega(n)$ of the oracles $O_i$ compute SAT on inputs of length $t$. By the correctness of the paddability procedure, for all of these correct oracles, satisfiability questions about the randomized formulas
\( \phi_j \) are all answered correctly. This together with the lower bound on probability that one of the \( \phi_j \) is uniquely satisfiable implies that with probability at least \( 1 - \alpha(1) \), oracle calls to some oracle \( O_i \) yield a satisfying assignment for \( \phi \), which is then correctly verified and results in acceptance of the circuit.

\[ \square \]

3.3 Equivalences for EXP and EXP\(^{NP} \) via a Win-Win Argument and Selectors

In this section, we generalize our equivalence results to the classes EXP and EXP\(^{NP} \).

We provide two proofs. The first proof uses a win-win argument and relies on the existing equivalence result for PSPACE (Theorem 3.2). The second proof uses selectors for EXP\(^{NP} \)-complete languages [Hir15] or instance checkers for EXP-complete languages [BFL91]. Each proof has its advantages and disadvantages, as will be discussed in Remark 3.16.

We start with the first proof. We first state the equivalence result for EXP

**Theorem 3.8** (Equivalence for EXP\(^{NP} \)). Let \( C \) be a nice class of Boolean circuits. The following statements are equivalent:

(i) EXP\(^{NP} \) \( \not\subseteq \) BP\(\cdot\)C.

(ii) For every choice of \( c, d, \ell \in \mathbb{N} \), with \( m(N) = n^d \) and \( \delta(N) = 1/n^\ell \), and for every sequence \( \{G_N\} \) of maps \( G_N : \{0,1\}^{m(N)} \rightarrow \{0,1\}^N \) implicitly computed by DLOGTIME-uniform \( C \)-circuits of size at most \( n^c \), the corresponding \( C \)-Implicit-\( \delta \)-Heavy-Avoid problem can be solved in deterministic time \( \text{poly}(N) \) with access to an NP oracle on infinitely many input lengths \( N \).

**Proof.** We consider each implication below.

\( (ii) \Rightarrow (i) \). The proof is completely analogous to the implication from \( (ii) \) to \( (i) \) in Theorem 3.2. The only difference is that due to the access to an NP oracle provided to each deterministic polynomial-time algorithm for Implicit-\( \delta \)-Heavy-Avoid, the resulting hard language is in EXP\(^{NP} \) as opposed to PSPACE.

\( (i) \Rightarrow (ii) \). If EXP\(^{NP} \) \( \not\subseteq \) BP\(\cdot\)C then either EXP\(^{NP} \) \( \not\subseteq \) PSPACE or PSPACE \( \not\subseteq \) BP\(\cdot\)C. We show that the desired conclusion holds in each one of these cases.

First, assume that EXP\(^{NP} \) \( \not\subseteq \) PSPACE. Recall that if EXP\(^{NP} \) \( \subseteq \) SIZE[poly] then EXP\(^{NP} \) = PSPACE [BH92]. Therefore, it follows that EXP\(^{NP} \) \( \not\subseteq \) SIZE[poly]. In particular, there is a language \( L \in \text{DTIME}[2^{O(n)}]\)^{NP} \) such that \( L \notin \text{SIZE}[n^k] \) for every choice of \( k \). Now fix a choice of \( c, d, \ell \in \mathbb{N} \), with \( m(N) = n^d \) and \( \delta(N) = 1/n^\ell \), and consider a sequence \( \{G_N\} \) of maps \( G_N : \{0,1\}^{m(N)} \rightarrow \{0,1\}^N \) implicitly computed by DLOGTIME-uniform \( C \)-circuits of size at most \( n^c \). Consider the following algorithm \( A \): On input \( 1^N \), where \( N = 2^n \), \( A \) outputs the \( N \)-bit string \( w_N \) corresponding to the truth-table of \( L \) on \( n \)-bit strings. Since \( L \in \text{DTIME}[2^{O(n)}]\)^{NP}, \( A \) computes in time \( \text{poly}(N) \) with access to an NP oracle. Moreover, since \( L \) is not computed by (general) Boolean circuits of size \( n^k \) on infinitely many input lengths, where \( k \) is an arbitrary constant, it follows that \( w_N \) is not in the range of \( G_N \) for infinitely many values of \( N \). Otherwise, there would be a choice \( a \) for the seed of \( G_N \) such that \( w_N = G_N(a) \), which yields a bounded size circuit for the function encoded by \( w_N \), given that \( G_N \) is implicitly computed by \( C \)-circuits of bounded size. In particular, it follows that on infinitely many values of \( N \), \( w_N \) is not \( \delta \)-heavy for \( G_N \), as desired.

Now consider the remaining case, i.e., assume that PSPACE \( \not\subseteq \) BP\(\cdot\)C. Then, by Theorem 3.2, we can solve the required \( C \)-Implicit-\( \delta \)-Heavy-Avoid problem in space \( O(\log N) \) on infinitely many input lengths \( N \). Since \( O(\log N) \) space algorithms can be simulated by \( \text{poly}(N) \) time algorithm
(not to mention that we have access to an \(NP\) oracle), the desired conclusion also holds in this case.

It is easy to see that a similar equivalence result for \(\text{EXP}\) also holds. Actually, the proof is essentially the same as Theorem 3.8, with the only difference being that we use the Karp–Lipton theorem for \(\text{EXP}\) \([KL80]\) instead of the one for \(\text{EXP}^{\text{NP}}\) \([BH92]\). Hence we only state the result and omit the proof here.

**Theorem 3.9** (Equivalence for \(\text{EXP}\)). Let \(C\) be a nice class of Boolean circuits. The following statements are equivalent:

(i) \(\text{EXP} \notin \text{BP-}C\).

(ii) For every choice of \(c, d, \ell \in \mathbb{N}\), with \(m(N) = n^d\) and \(\delta(N) = 1/n^\ell\), and for every sequence \(\{G_N\}\) of maps \(G_N: \{0,1\}^{m(N)} \to \{0,1\}^N\) implicitly computed by \(\text{DLOGTIME}\)-uniform \(C\)-circuits of size at most \(n^c\), the corresponding \(C\)-Implicit-\(\delta\)-Heavy-Avoid problem can be solved in deterministic time \(\text{poly}(N)\) on infinitely many input lengths \(N\).

Using Theorem 3.9, it is not hard to prove Corollary 1.3 stated in Section 1.1.1. We restate the result below for convenience.

**Corollary 3.10.** For every function \(e(N) = \omega(1)\), there is a unary \(\text{BPP}\) search problem \(S\) such that:\(^{19}\)

- \(S(1^N)\) can be solved in randomized \(\text{poly}(N)\) time by an algorithm that uses \(N\) random bits and errs on at most \(e(N)\) random bit sequences;

- If there exists a randomized polynomial-time algorithm for \(S\) with non-zero success probability that errs on at most \(O(1)\) random bit sequences for any input, then \(\text{EXP} \neq \text{BPP}\).

**Proof Sketch.** To argue this, we can assume that \(\text{EXP} \subseteq \text{BPP}\), since the claim is trivial otherwise. Then, by Theorem 3.9 with \(C = \text{“general Boolean circuits”}\) and \(\text{BP-}C = \text{BPP}\), and using Remark 3.5 on the robustness of \(\delta\), there is a uniform polynomial-time sampler and a corresponding distribution \(D\) for which \((D,1/e(N))\)-Heavy-Avoid does not admit infinitely-often correct deterministic polynomial-time algorithms. Now note that the corresponding Heavy Avoid problem for \(D\) is a unary \(\text{BPP}\) search problem \(S(1^N)\) that can be solved in randomized \(\text{poly}(N)\) time by an algorithm that uses \(N\) random bits and errs on at most \(e(N)\) random bit sequences (indeed, a random string of length \(N\) satisfies this guarantee). On the other hand, if there is a randomized polynomial-time algorithm for \(S\) with non-zero success probability that only errs on constantly many random bit sequences for any input, then we argue that there is a deterministic polynomial-time algorithm for \(S\) that succeeds on infinitely many input lengths, which contradicts our assumption. Suppose \(k\) is a constant such that there is some randomized polynomial-time algorithm \(A\) for \(S\) which errs on at most \(k\) random bit sequences for any input \(1^N\). We define \(k + 1\) deterministic polynomial-time algorithms \(A_1, \ldots, A_{k+1}\) such that for some \(i \in [k+1]\), \(A_i\) solves \(S\) on infinitely many input lengths. On input \(1^N\), \(A_i\) runs \(A\) with random bit sequence \(r_i\), where \(r_i\) is the \(i\)-th random bit sequence used by \(A\) on input length \(N\) in lexicographical order. Note that since \(A\) only makes errors on at most \(k\) random bit sequences for any \(N\) and has non-zero success probability, for each \(N\) there is an \(i\) such that \(A_i\) outputs a correct solution to \(S\) on \(1^N\). Since this is the case for each \(N\), there

\(^{19}\)For the definition of \(\text{BPP}\) search problems and related discussions, see, e.g., \([Gol11]\).
is some $i$ such that $A_i$ outputs a correct solution to $S$ on $1^N$ for infinitely many $N$, and our result follows.\footnote{While the argument presented here is somewhat non-constructive, we remark that it is possible to modify the proof so that for each $k \geq 1$, there is an explicit unary BPP search problem satisfying the same conditions except that one obtains the weaker conclusion that $\text{DTIME}[2^n] \nsubseteq \text{BPTIME}[n^k]$ in the second bullet. We omit the details.}

Next, we present another proof of Theorem 3.8 for the special case where $\mathcal{C}$ is the class of general Boolean circuits. The proof is direct and does not go through win-win arguments. In one sentence, we use selectors for $\text{EXP}^{\text{NP}}$-complete problems \cite{Hir15} and note that the proof strategy from Theorem 3.2 extends to selectors.

**Definition 3.11.** We say that a probabilistic polynomial-time oracle machine $S$ is a selector for a language $L \subseteq \{0,1\}^*$ if the following holds. Let $O_1, O_2 \subseteq \{0,1\}^*$ be arbitrary oracles. Then, for any input $x \in \{0,1\}^*$, if $L \in \{O_1, O_2\}$ then

$$\Pr_{S}[S^{O_1,O_2}(x) = L(x)] \geq 2/3.$$  

It is possible to boost the success probability of the selector using standard techniques. In addition, \cite{Hir15} proved that if a language $L$ admits a selector then it also admits a selector that succeeds when given access to polynomially many oracles provided that at least one of them correctly computes $L$. These are summarized in the following result.

**Theorem 3.12 (\cite{Hir15}).** Every $\text{EXP}^{\text{NP}}$-complete language admits a selector. Moreover, there is a paddable $\text{EXP}^{\text{NP}}$-complete language $L' \in \text{DTIME}[2^{O(n)}]^{\text{NP}}$, a polynomial $q$, and a probabilistic polynomial-time oracle algorithm $S$ such that the following conditions hold:

1. For every $n \geq 1$, $x \in \{0,1\}^n$, and $t \geq 1$, if $O_1, \ldots, O_t \subseteq \{0,1\}^*$ and $L' \in \{O_1, \ldots, O_t\}$, then

$$\Pr_{S}[S^{O_1,\ldots,O_t}(x,1^t) = L'(x)] \geq 1 - 2^{-n}.$$  

2. Every oracle query of $S(x)$ has length exactly $q(n)$. Consequently, it is enough to assume that the oracles $O_1, \ldots, O_t$: $\{0,1\}^{q(n)} \to \{0,1\}$ and that $L'_{q(n)} \in \{O_1, \ldots, O_t\}$, where $L'_{q(n)} = L \cap \{0,1\}^{q(n)}$.

The “moreover” part of the result follows from existence of a selector for every $\text{EXP}^{\text{NP}}$-complete problem \cite{Hir15} combined with the paddability of $L'$ and the discussion above.

**Proof of Theorem 3.8, for the case that $\mathcal{C}$ is the class of general Boolean circuits.**

(iii) $\Rightarrow$ (i). Again, the proof is completely analogous to the same implication in Theorem 3.2, and we omit the details.

(i) $\Rightarrow$ (iii). We argue in the contrapositive. Suppose there is a choice of constants $c$, $d$, and $\ell$, with $m(N) = n^d$ and $\delta(N) = 1/n^\ell$, and a sequence $G_N: \{0,1\}^{m(N)} \to \{0,1\}^N$ implicitly computed by general Boolean circuits of size at most $n^c$ such that every deterministic algorithm $A(1^N)$ with access to an $\text{NP}$ oracle that runs in $\text{poly}(N)$ time fails to solve $\text{Implicit-}\delta-\text{Heavy-Avoid}$ on every large enough input length $N$. Next, we use this assumption to establish that $\text{EXP}^{\text{NP}} \subseteq \text{BPP}$.

We consider a candidate algorithm $A'(1^N)$ with access to an $\text{NP}$ oracle that computes as follows. Consider the $\text{EXP}^{\text{NP}}$-complete language language $L'$ from Theorem 3.12. On input $1^N$, $A'$ outputs the truth-table $tt_N \in \{0,1\}^N$ of $L'$ over strings of length $n$, where $N = 2^n$. Note that $A'$ uses an
NP oracle and computes in time $\text{poly}(N)$, since $L' \in \text{DTIME}[2^{O(n)}]^{\text{NP}}$. Since $A'$ fails to solve $\text{Implicit-}\delta\text{-Heavy-Avoid}$, for every large enough $N$, the probability of $tt_N$ under $G_N(U_{m(N)})$ is at least $\delta = 1/n^\ell$.

The rest of the argument is similar to that of the proof of Theorem 3.2. To compute $L'$ on some input $x \in \{0,1\}^n$, we let $n := q(n')$, where $q$ is the polynomial from Theorem 3.12. We sample $t := n^{3\ell}$ strings $z_1, \ldots, z_t \in \{0,1\}^{m(N)}$ uniformly and independently at random, and use the corresponding oracles $O_1, \ldots, O_t$ as candidate oracles for $L'_n$, where each $O_i$ computes according to the string $G_N(z_i) \in \{0,1\}^N$. By our choice of $t$, with high probability the string $tt_N$ is among the oracles obtained from $z_1, \ldots, z_t$. In this case, there is at least one correct oracle $O_i$ among the oracles $O_1, \ldots, O_t$. Consequently, if we run $S(x, 1^t)$ with access to $O_1, \ldots, O_t$, we compute $L'(x)$ with high probability. Since $n = \text{poly}(n')$, $t = \text{poly}(n)$, the selector runs in time $\text{poly}(n', t)$, and the simulation of each oracle query to $O_i$ can be done using a computation of the corresponding bit of $G_N(z_i)$ in time $\text{poly}(n)$, it follows that given $x$ of length $n'$ we can compute $L'(x)$ with high probability in time $\text{poly}(n')$. Finally, since $L'$ is complete for $\text{EXP}^{\text{NP}}$, it follows that $\text{EXP}^{\text{NP}} \subseteq \text{BPP}$, as desired.

Inspecting the proof, it is not hard to see that it also extends to almost-everywhere and sub-exponential lower bounds. Since the proofs are straightforward modifications of the argument given above, we only state the results and omit the proof details.

**Theorem 3.13.** The following statements are equivalent:

(i) $\text{EXP}^{\text{NP}} \not\subseteq \text{i.o. BPP}$.

(ii) For every choice of the parameters $c, d, \ell \in \mathbb{N}$, with $m(N) = n^d$ and $\delta(N) = 1/n^\ell$, and for every sequence $\{G_N\}$ of maps $G_N : \{0,1\}^{m(N)} \to \{0,1\}^N$ implicitly computed in time $n^c$, the $\text{Implicit-}\delta\text{-Heavy-Avoid}$ problem can be solved with access to an $\text{NP}$ oracle in deterministic time $\text{poly}(N)$ for every large enough $N$.

**Theorem 3.14.** The following statements are equivalent:

(i) There is a constant $\epsilon > 0$ such that $\text{E}^{\text{NP}} \not\subseteq \text{BPTIME}[2^{n^\epsilon}]$.

(ii) There is a constant $\epsilon > 0$ such that for $\delta(N) := 2^{-n^\epsilon}$ and for every sequence $\{G_N\}$ of maps $G_N : \{0,1\}^{2n^\epsilon} \to \{0,1\}^N$ implicitly computed in time $2^{n^\epsilon}$, the $\text{Implicit-}\delta\text{-Heavy-Avoid}$ problem on $\{G_N\}$ can be solved with access to an $\text{NP}$ oracle in deterministic time $\text{poly}(N)$ on infinitely many values of $N$.

One can also use the instance checkers for $\text{EXP}$-complete languages [BFL91] (which imply selectors for such languages [Hir15]) to prove similar characterizations for $\text{EXP}$. For example, the following theorem holds (we omit the details as it is the same as our second proof for $\text{EXP}^{\text{NP}}$):

**Theorem 3.15.** The following statements are equivalent:

(i) $\text{EXP} \not\subseteq \text{i.o. BPP}$.

(ii) For every choice of the parameters $c, d, \ell \in \mathbb{N}$, with $m(N) = n^d$ and $\delta(N) = 1/n^\ell$, and for every sequence $\{G_N\}$ of maps $G_N : \{0,1\}^{m(N)} \to \{0,1\}^N$ implicitly computed in time $n^c$, the $\text{Implicit-}\delta\text{-Heavy-Avoid}$ problem can be solved in deterministic $\text{poly}(N)$ time for every large enough $N$.

**Remark 3.16** (Comparison between the two proof methods). We presented two proofs for Theorem 3.8. Both proofs have their advantages and disadvantages, as we summarize below:
• The first proof only uses (non-adaptive) instance checkers [Che23] that have small circuit complexity overheads, hence it works for restricted circuit classes such as $\text{AC}^0[\oplus]$ and $\text{ACC}^0$. On the other hand, the second proof needs to use the selectors for $\text{EXP}^{\text{NP}}$ [Hir15] which is highly adaptive, hence does not extend to smaller circuit classes such as $\mathcal{C} = \text{NC}^1$ or $\mathcal{C} = \text{TC}^0$.\footnote{One can also construct non-adaptive instance checkers for $\text{EXP}$-complete languages from highly-efficient PCPs such as [BGH+06, BS08, BCGT13, BV14]. In fact, the results in [BV14] imply an instance checker whose circuit complexity is only a 3-CNF over its randomness $r$. However, it is unclear if that instance checker also has low circuit complexity over the input $x$. (Looking into the proof, it seems that one needs to at least compute a Reed-Solomon encoding of $x$). Therefore we chose to use the off-the-shelf $\text{AC}^0[\oplus]$ instance checker in [Che23] for $\text{PSPACE}$ and resort to a win-win argument for $\text{EXP}$. On the other hand, it is unclear to the authors whether the selectors in [Hir15] can be made non-adaptive.}

• The second proof proceeds by a direct argument and hence generalizes to almost-everywhere and sub-exponential time lower bounds (Theorems 3.13 and 3.14), while the first proof uses a win-win analysis and does not seem to generalize to these cases.

4 Unconditional Pseudodeterministic Algorithms for Heavy Avoid

4.1 A Pseudodeterministic Algorithm for Implicit Maps

We present an unconditional pseudodeterministic algorithm for $\text{Implicit-}\delta\text{-Heavy-Avoid}$ for general Boolean circuits on infinitely many input lengths. Our algorithm runs in $\text{poly}(N)$ time and works for maps $G: \{0, 1\}^{\text{poly}(n)} \rightarrow \{0, 1\}^N$ (where $N = 2^n$ following our convention) implicitly computable in $\text{poly}(n)$ time.

**Theorem 4.1.** For every choice of positive constants $c$, $d$, and $\ell$, with $m(N) = n^d$ and $\delta(N) = 1/n^\ell$, and for every sequence $\{G_N\}$ of maps $G_N: \{0, 1\}^{m(N)} \rightarrow \{0, 1\}^N$ implicitly computed in time $n^\ell$, the $\text{Implicit-}\delta\text{-Heavy-Avoid}$ problem can be solved pseudodeterministically in polynomial time on infinitely many input lengths. Moreover, the algorithm computes pseudodeterministically on every input length.

**Proof.** Consider a map $G_N: \{0, 1\}^n \rightarrow \{0, 1\}^N$ that is implicitly computable in time $n^c$. We consider two cases, based on whether $\text{EXP} = \text{BPP}$.

If $\text{EXP} \not\subseteq \text{BPP}$, then by Theorem 3.9, the $\text{Implicit-}\delta\text{-Heavy-Avoid}$ problem can be solved in deterministic polynomial time (i.e., in time $\text{poly}(N)$) on infinitely many input lengths. Since the algorithm is deterministic, it behaves pseudodeterministically on every input length.

In the remaining case, assume that $\text{EXP} \subseteq \text{BPP}$. Let $B(j)$ be the following Turing machine with input $j \in \{0, 1\}^N$: It first goes over all choices of $x \in \{0, 1\}^n$ and computes $G_N(x)$, then calculates the probability of each string in $\{0, 1\}^N$ produced in this way, and finally outputs the $j$-th bit of the lexicographic first string $y$ such that $\Pr[G_N(U_{M(N)}) = y] < \delta$. Note that $B$ runs in time exponential in $n$, its input length. Therefore, it defines a language $L_B \in \text{EXP}$. By the assumption, $L_B \in \text{BPP}$. Consequently, we can compute $y \in \{0, 1\}^N$ from $1^N$ in pseudodeterministic time $\text{poly}(N)$. Note that this algorithm succeeds on every input length. \(\square\)

The win-win argument presented in Theorem 4.1 is non-constructive, but it is possible to combine the two cases and the argument from Theorem 3.9 via instance checkers to give an explicit description of a procedure that solves $\text{C-Implicit-}\delta\text{-Heavy-Avoid}$ on infinitely many input lengths.
While instance checkers are well known in the context of program verification and in complexity-theoretic results, to our knowledge this provides the first algorithm for a natural problem in a completely different context whose design makes use of instance checkers.

We can extend the proof of Theorem 4.1 to any nice circuit class $C$. In this case, it is also possible to obtain a stronger conclusion using a more careful argument.

**Theorem 4.2.** Let $C$ be a nice circuit class. Let $D = \{D_N\}_{N \geq 1}$ be a distribution ensemble, where each $D_N$ is supported over $\{0,1\}^N$. Suppose that $D$ admits implicit DLOGTIME-uniform $C$-samplers of size $\operatorname{poly}(n)$ (recall that $N = 2^n$). Then, for every function $\delta(N) = 1/n^k$, where $k \in \mathbb{N}$, either $(D, \delta)$-Heavy-Avoid can be solved in space $O(\log N)$ on infinitely many input lengths, or $(D, \delta)$-Heavy-Avoid can be solved pseudodeterministically by DLOGTIME-uniform BP-$C$-circuits of size $\operatorname{polylog}(N)$ on every input length.\(^{22}\)

**Proof.** Given an ensemble $D = \{D_N\}_{N \geq 1}$ as in the statement, consider the corresponding map $G_N: \{0,1\}^{nd} \rightarrow \{0,1\}^N$ implicitly computable by DLOGTIME-uniform $C$-samplers of size $n^c$. Let $k \geq 1$ be arbitrary, and set $\delta(n) = 1/n^k$. We consider two cases, based on whether $\operatorname{PSPACE} \not\subseteq \operatorname{BP}-C$.

If $\operatorname{PSPACE} \not\subseteq \operatorname{BP}-C$, then by Theorem 3.2, the $(D, \delta)$-Heavy-Avoid problem can be solved in space $O(\log N)$ on infinitely many input lengths.

In the remaining case, assume that $\operatorname{PSPACE} \subseteq \operatorname{BP}-C$. We show how to solve the Heavy Avoid problem for $G_N$ even for the exponentially smaller threshold $\delta(N) = 1/N^k$. Let $B(j)$ be the following Turing machine with input $j \in \{0,1\}^n$, which we also view as a natural number in $[N]$: if $j > 2k \cdot n$, it outputs 0; otherwise, it goes in lexicographic order over all strings of length $2k \cdot n$ and outputs the $i$-th bit of $G_N(y^*)$, where $y^*$ is the first string such that the $(2k \cdot n)$-prefix of $G_N(y^*)$ is produced by $G_N(U_{nd})$ with probability less than $\delta$. Note that $B$ runs in space $\operatorname{poly}(n)$, since $G_N$ is implicitly computable. Moreover, since $\delta = 1/N^k$, there exists some prefix of length $2k \cdot n$ that is produced by $G_N(U_{nd})$ with probability less than $\delta$. In particular, this prefix concatenated with a sequence of 0’s is a valid solution to the Heavy Avoid problem of $D_N$, the distribution sampled by $G_N$.

Since $B$ runs in space polynomial in $n$, it defines a language $L_B \in \operatorname{PSPACE}$. By the assumption, $L_B \in \operatorname{BP}-C$. Consequently, we can compute each bit of $G_N(y^*) \in \{0,1\}^N$ given $i \in [N] = \{0,1\}^n$ using DLOGTIME-uniform BP-$C$-circuits of size $\operatorname{poly}(n) = \operatorname{polylog}(N)$.

### 4.2 Universality and Connection to Time-Bounded Kolmogorov Complexity

In this section, we observe the existence of a “hardest” instance of uniform Heavy Avoid (Definition 2.1). We also point out a connection to time-bounded Kolmogorov complexity. These connections will be used, in Section 4.3, to obtain an improved pseudodeterministic algorithm as compared to Theorem 4.1.

First, we introduce a collection of sequences of probability distributions, where each sequence in the collection is associated with a time bound $t(n) = n^c$, $c \in \mathbb{N}$. We let $D^t = \{D^t_n\}_{n \geq 1}$, where each sampler $D^t_n: \{0,1\}^{\log n + n + t + n} \rightarrow \{0,1\}^n$ is defined as follows. The input to $D^t_n$ is a random string $x$ that we view as a tuple $(i, M, r, z)$, where $i \in [n]$, $M \in \{0,1\}^n$, $r \in \{0,1\}^t$, and $z \in \{0,1\}^n$. We let $M_{\leq i}$ denote the first $i$ bits of $M$, which we view as an input to a universal Turing machine $U$. Let $y = U(1^n, M_{\leq i}, r)$ be the output of $U$ on machine $M_{\leq i}$ with random string $r$ after it computes for at most $t$ steps. If $y$ is an $n$-bit string, then $D^t_n$ outputs $y$. Otherwise, $D^t_n$ outputs $z$.

Observe that $D^t \in \mathsf{PSAMP}$ for any polynomial $t$. Below we abuse notation and use $D^t_n(w)$ to denote the probability of a string $w$ under the distribution induced by the sampler $D^t_n$.

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\(^{22}\)In other words, the corresponding DLOGTIME-uniform BP-$C$-circuit is given $i \in [N] = \{0,1\}^{\log N}$ and outputs the $i$-th bit of the solution with high probability.
Proof. We need to prove that if $D_n(w) > \delta$ then $D'_n(w) > \delta/(Kn^2)$. Let $k \in \mathbb{N}$ denote the length of the time-$n^c$ program $M'$ that samples the ensemble $\mathcal{D}$. We let $M$ be the program that samples from $D_n$. Note that $M$ can be encoded with $k + \log n$ bits. Set $K = 2^k$.

Now with probability $1/n$ we obtain $i = k + \log n$ as input to sampler $D'_n$, assuming that $n$ is large enough. Moreover, with probability at least $1/(2^{k + \log n}) = 1/(Kn)$ the sampler $D'_n$ selects the description of $M$ as input to the universal machine. Conditioned on these events, the output of $D'_n$ is distributed according to $D_n$. Consequently, if $D_n(w) > \delta$ we have $D'_n(w) > \delta/(Kn^2)$.

Proof. The first item is an immediate consequence of the coding theorem for $pK^n$ complexity from [LOZ22] and of the coding theorem for $rKt$ complexity from [LO21]. The second item follows from the definitions of $D'_n$ and $pK^n$ complexity.

The next result shows that any solution $y \in \{0, 1\}^n$ to the $(D'_n, \delta)$-Heavy-Avoid problem must have non-trivial $pK^n$ complexity. In particular, if $\delta = n^{-c}$ then $pK^n(y) \geq (c - 1) \cdot \log n - 1$.

**Proposition 4.5** (Uniform Heavy Avoid and Time-Bounded Kolmogorov Complexity). Consider the distribution $\mathcal{D}' = \{D'_n\}_{n \geq 1}$ and a probability threshold $\delta$. If $D'_n(y) \leq \delta$ then $pK^n(y) \geq \log(1/\delta) - \log(n) - 1$.

**Proof.** This follows immediately from Lemma 4.4 Item (ii).

Conversely, by Lemma 4.4 Item (i), in order to solve the $(D'_n, \delta)$-Heavy-Avoid problem for $t(n) = n^c$ it is enough to output a string $y \in \{0, 1\}^n$ such that $pK^n(y) > \log(1/\delta) + d \cdot \log n$. Similarly, it is enough that $rKt(y) > d \cdot \log(1/\delta) + d \cdot \log n$.

We obtain the following consequence from the discussion in this section.

**Proposition 4.6.** Let $\mathcal{D} = \{D_n\}_{n \geq 1}$ be a polynomial-time samplable ensemble of distributions, with $D_n$ supported over $\{0, 1\}^n$, and let $\delta(n) = 1 / \text{poly}(n)$. Then there is a constant $k \geq 1$ such that, for every large enough $n$, if $y \in \{0, 1\}^n$ is a string such that $rKt(y) \geq k \cdot \log n$, then $y$ is a solution to the $(D_n, \delta(n))$-Heavy-Avoid problem.

**Proof.** This follows from Proposition 4.3 and Lemma 4.4 Item (i).
4.3 Subexponential-Time Pseudodeterministic Algorithms for Explicit Maps

In this section, we prove the following result.

**Theorem 4.7.** Let $\mathcal{D} = \{D_n\}_{n \geq 1}$ be a polynomial-time samplable ensemble of distributions, with $D_n$ supported over $\{0, 1\}^n$, and let $\delta(n) = 1/\text{poly}(n)$. Then, for every $\varepsilon > 0$, there is a pseudodeterministic algorithm running in time $O(2^{n^\varepsilon})$ that solves $(D_n, \delta(n))$-Heavy-Avoid problem on infinitely many values of $n$. Moreover, this algorithm behaves pseudodeterministically on every input.

**Proof.** Let $\mathcal{D} = \{D_n\}_{n \geq 1}$ be a polynomial-time samplable ensemble of distributions, with $D_n$ supported over $\{0, 1\}^n$, and let $\delta(n) = 1/\text{poly}(n)$. In order to solve the Heavy Avoid problem for this distribution ensemble, it is enough to pseudodeterministically construct an $n$-bit string $y$ of large enough $\text{rkT}$ complexity (Proposition 4.6).

For this, we rely on a unconditional result from [LOS21]. For every integer $d \geq 1$ and $\varepsilon > 0$, [LOS21, Theorem 39] pseudodeterministically constructs a string $w$ of length $d \cdot \log n$ with $\text{rkT}(w) \geq (d/2) \cdot \log n$ in time $2^{n^\varepsilon}$, for infinitely many values of $n$. Additionally, the corresponding procedure behaves pseudodeterministically on all input strings.

Since we can efficiently recover $w$ from a padded version of $w$, it is not hard to see that if we set $y = w0^{n\varepsilon-\|w\|}$, then $\text{rkT}(y) \geq (d/8) \cdot \log n$, assuming that $d \geq 8$. Consequently, we can infinitely-often pseudodeterministically construct in time $2^{n^\gamma}$ an $n$-bit string of $\text{rkT}$ complexity at least $k \cdot \log n$, where $\gamma > 0$ and $k \geq 1$ are arbitrary constants.

**Comparison between Theorem 4.7 and Theorem 4.1.** Next, we explain that Theorem 4.7 is stronger than Theorem 4.1. This is not immediately obvious, since the algorithms from Theorem 4.7 work in a more general setting but have a sub-exponential running time as a function of the output length, while the algorithms for the more restrictive implicit maps provided by Theorem 4.1 run in polynomial time as a function of the output length.

Consider a map $G_N: \{0, 1\}^{n^d} \rightarrow \{0, 1\}^N$ implicitly computed by a uniform circuit $C_N: \{0, 1\}^{n^d+n} \rightarrow \{0, 1\}$ of size at most $n^c$, where $N = 2^n$. Suppose that $C_N$ is the output of $A(1^n)$, a uniform generating procedure that runs in time $O(n^k)$. Consider the associated $C$-Implicit-$\delta$-Heavy-Avoid problem with threshold $\delta(N) = 1/n^\ell$, and let $D_N$ be the probability distribution supported over $\{0, 1\}^N$ that is induced by the map $G_N$ implicitly computed by $C_N$.

For a given $n$, let $n' = n^a$, for a large enough constant $a \geq 1$, $m' = m = n^d$, and $\delta'(n') = \delta(N) = 1/n^\ell$. Consider the distribution $D_{n'}$ supported over $\{0, 1\}^{n'}$ and defined as follows: sample a random string $w \sim \{0, 1\}^{n'}$, let $x^w = G_N(w) \in \{0, 1\}^N$, and output the length-$n'$ prefix of $x^w$.

We make the following observations:

- The ensemble $\mathcal{D}' = \{D_{n'}\}_{n \geq 1}$ is polynomial-time samplable, since the circuit $C_N$ that implicitly samples $D_N$ is generated by $A(1^n)$ in time polynomial in $n$.
- If the constant $a$ is large enough, by our choice of $n'$, there is at least one element in the support of $D_{n'}$ that is not $\delta'$-heavy.
- If an element $y' \in \{0, 1\}^{n'}$ is not $\delta'$-heavy in $D_{n'}$, then $y = y'0^{N-n'}$ is not $\delta$-heavy in $D_N$.

Now suppose that a pseudodeterministic algorithm $E$ solves $(D_{n'}, \delta'(n'))$-Heavy-Avoid and runs in time $O(2^{n^c'})$, for a fixed but arbitrary $c' > 0$, as in the case of Theorem 4.7. Let $y' = \ldots$
\( E(1^n) \). Then, given \( 1^N \), if we use \( E \) to compute \( y' \) then output \( y = y'0^{N-n'} \), we can solve \( \mathcal{C}\text{-Implicit-}\delta\text{-Heavy-Avoid} \) in pseudodeterministic time

\[
O(N + 2^{n'\varepsilon'}) = O(N),
\]

assuming that \( \varepsilon' < 1/a \).

More generally, using the “prefix” reduction described above, one can easily show that distributions \( D_n \) supported over \( \{0,1\}^n \) that are sampled with \( \text{polylog}(n) \) random bits admit polynomial-time infinitely-often pseudodeterministic algorithms for the \((D_n, \delta(n))\)-Heavy-Avoid problem, as long as \( \delta(n) \geq 1/\text{poly}(\log(n)) \).\(^{24}\) We summarize this discussion as follows.

**Theorem 4.8.** Let \( \mathcal{D} = \{D_n\}_{n \geq 1} \) be a distribution ensemble, where each \( D_n \) is supported over \( \{0,1\}^n \), and suppose that \( \mathcal{D} \) admits a polynomial-time sampler of randomness complexity \( (\log n)^r \). Then, for every function \( \delta(n) = 1/(\log n)^k \), there is a polynomial time pseudodeterministic algorithm for the \((\mathcal{D}, \delta)\)-Heavy-Avoid problem that succeeds on infinitely many values of \( n \). Moreover, this algorithm behaves pseudodeterministically on every input.

### 4.4 On Pseudodeterministic Algorithms and Hierarchies for Probabilistic Time

Theorem 4.1 provides a pseudodeterministic polynomial-time infinitely-often algorithm for heavy avoid for implicit maps. Moreover, this algorithm computes pseudodeterministically on all input lengths. In this section, we observe that the existence of a pseudodeterministic algorithm with these properties for the larger class of explicit maps (or even just for implicit maps with polynomial stretch instead of subexponential stretch) would solve a longstanding open problem related to hierarchies for probabilistic time.

To give more context, we know that \( \text{BPP} \) is strictly contained within \( \text{BPSUBEXP} \) [KV87], but the question of whether \( \text{BPTIME}[n] \) is strictly contained within \( \text{BPTIME}[T(n)] \), for any function \( T \) that remains sub-exponential even when composed with itself a constant number of times, remains open. Progress has been made in establishing hierarchies for variants of \( \text{BPP} \). The papers [Bar02, FS04, vMP06] demonstrated hierarchies for \( \text{BPP}/1 \) (problems solvable in probabilistic polynomial time with 1 bit of advice) and for \( \text{Heur-BPP} \) (problems solvable on average in probabilistic polynomial time). However, despite extensive efforts, establishing a hierarchy for \( \text{BPP} \) itself remains an open problem.

**Proposition 4.9.** Suppose that for every polynomial-time samplable distribution ensemble \( \mathcal{D} = \{D_n\}_{n \geq 1} \), there is a function \( \delta(N) = o(1) \) such that the corresponding \((\mathcal{D}, \delta)\)-Heavy-Avoid problem admits a pseudodeterministic polynomial-time algorithm that succeeds on infinitely many input lengths and behaves pseudodeterministically on all input lengths. Then, for every constant \( k \geq 1 \), we have \( \text{BPE} \not\subseteq \text{BPTIME}[2^{k-n}] \) (in particular, for every constant \( c \geq 1 \), \( \text{BPP} \not\subseteq \text{BPTIME}[n^c] \)).

**Proof.** Let \( k \geq 1 \). Using the assumption, we show below that there is \( L \in \text{BPTIME}[2^{O(n)}] \) such that \( L \not\subseteq \text{BPTIME}[2^{k-n}] \). Note that the additional conclusion in the theorem follows from a standard padding argument.

Let \( N(n) = 2^n \), and consider the uniform sampler \( S_N : \{0,1\}^{m(N)} \to \{0,1\}^N \) defined as follows, where \( m(N) = N^{k+2} = 2^{(k+2)n} \). The sampler parses its input string \( x \) as a pair \((M, r)\), where \( M \) describes a clocked probabilistic machine running in time \( 2^{(k+1)n} \), and the rest bits \( r \) are treated

\(^{24}\)In order to maintain the infinitely-often guarantee in the prefix reduction, one needs to make sure that the map from the large output parameter \( n \) to the smaller output parameter \( n' \) is onto, which is easy to achieve.
as randomness. For \( i \in [N] \), which we also view as an \( n \)-bit string, the \( i \)-th output bit of \( S_N(x) \) is the output of the computation of \( M \) over the string \( i \) when running with the random string \( r \). (For concreteness, if \( M \) on \((i, r)\) does not produce an output bit, we assume its output is 0.) Note that the resulting distribution ensemble \( D = \{D_N\} \) obtained from the sampler \( S_N \) is indeed in \( \mathsf{PSAMP} \).

Under the assumption of the theorem, \((\mathcal{D}, \delta)\)-\textbf{Heavy-Avoid} with \( \delta(N) = o(1) \) admits a pseudodeterministic algorithm running in time \( \text{poly}(N) = 2^{O(n)} \) that succeeds on infinitely many input lengths \( N \) and behaves pseudodeterministically on all input lengths. Let \( B(1^N) \) be such a pseudodeterministic algorithm. In addition, let \( L_B \) be the language defined by \( B \), i.e., a string \( z \in \{0, 1\}^n \) is in \( L_B \) if and only if the \( z \)-th bit of \( B(1^N) \) (with \( N = 2^n \)) is 1. Note that \( L_B \) is in \( \mathsf{BPTIME}[2^{O(n)}] \), since by assumption \( B \) computes pseudodeterministically on every input \( 1^N \).

It remains to argue that \( L_B \notin \mathsf{BPTIME}[2^{k^n}] \). To prove this, it is enough to show that for every language \( L \in \mathsf{BPTIME}[2^{k^n}] \), each string in the sequence \( \{y_N\} \) of truth-tables obtained from \( L \) is \( \delta(N) \)-heavy in \( D_N = S_N(\mathcal{U}_{n/(N)}) \), for every large enough \( N \). Since \( B \) correctly solves \((\mathcal{D}, \delta)\)-\textbf{Heavy-Avoid} on infinitely many values of \( N \), we claim that \( L_B \) and \( L \) differ on each such value of \( N \); provided that \( N \) is large enough. To see that the claim holds, note that if \( L \in \mathsf{BPTIME}[2^{k^n}] \) then by amplification there is a probabilistic machine \( M_L \) that runs in time at most \( 2^{kn} \cdot \text{poly}(n) \leq 2^{(k+1)n} \) and computes \( M_L \) on every input string of length \( n \) with probability at least \( 1 - 2^{-2n} \). Moreover, the description length of \( M_L \) is a constant. Let \( y_N^L \) be the truth-table of \( L \) on input length \( n \), then \( |y_N^L| = N = 2^n \). By construction, using an union bound, the probability that \( S_N(\mathcal{U}_{m(N)}) = y_N^L \) is at least \( \Omega(1) \cdot (1 - 2^{-2n}) > \delta(N) \), as long as \( N \) is large enough. This shows that \( y_N^L \) is \( \delta(N) \)-heavy in \( D_N \). Since \( B(1^n) \) avoids \( \delta(N) \)-heavy elements of \( D_N \) on infinitely many values of \( N \), this concludes the proof.

We note that, using a similar argument, the existence of polynomial-time algorithms for the heavy avoid problem for implicit maps with certain parameters would also lead to new hierarchy theorems. For instance, efficient algorithms for maps \( G: \{0, 1\}^{2n^3} \to \{0, 1\}^{2n} \) that are implicitly computed by uniform circuits of size \( 2^{n^3} \) would imply that \( \mathsf{BPE} \subset \mathsf{BPTIME}[2^{n^{o(1)}}] \).

## 5 Heavy Avoid and Derandomization

In this section, we study the relation between algorithms for Heavy Avoid and derandomization, with connections to recent developments in instance-wise hardness-randomness tradeoffs [CT21, LP22, Kor22, LP23, CTW23]. This section mainly considers Heavy Avoid for non-uniformly and implicitly sampled distributions.

### 5.1 A Non-Black-Box Reduction

We show that in some scenarios, solving the \textbf{Implicit-Heavy-Avoid} problem on non-uniform samplers implies general derandomization of \texttt{prBPP}. Intriguingly, our reduction from \texttt{prBPP} to Heavy Avoid is \textit{non-black-box} and relies on the \textit{code} of an algorithm for \textbf{Implicit-Heavy-Avoid}.

We first introduce appropriate notation. We say that a Boolean circuit family \( \{C_n\} \) has \textit{dimension} \( d(n) \times T(n) \) if for each \( n \in \mathbb{N} \), the gates of \( C_n \) are partitioned into \( d(n) \) layers, each layer contains at most \( T(n) \) gates, and each gate on layer \( i \) only receives inputs from layer \( i - 1 \). The \textit{depth} of the circuit is \( d(n) \) and the \textit{width} of the circuit is \( T(n) \). We will always assume \( d(n) \leq T(n) \).

A circuit family of dimension \( d(n) \times T(n) \) is \textit{logspace-uniform} if there is a Turing machine that on input \( 1^n \), uses at most \( O(\log T(n)) \) space, and prints the description of \( C_n \).

Recall that for \( \epsilon(n) > 0 \), a Boolean function \( f: \{0, 1\}^M \to \{0, 1\} \) is \( \epsilon \)-dense if \( \text{Pr}_{x \sim \{0, 1\}^M}[f(x) = 1] \geq \epsilon(M) \). We say \( f \) \( \epsilon \)-avoids a hitting set \( H \subseteq \{0, 1\}^M \) if \( f \) is \( \epsilon \)-dense and for every \( y \in H \), \( f(y) = 0 \).
Now we are ready to state our main technical tool, the instance-wise hardness-randomness tradeoff in [CT21].

**Theorem 5.1 ([CT21] with the improved parameters from [CLO+23]).** There is an absolute constant $c \geq 1$ such that the following holds. Let $f: \{0,1\}^n \rightarrow \{0,1\}^T(n)$ be a multi-output function computable by a logspace-uniform circuit of dimension $d(n) \times T(n)$. Let $M(n)$ be a parameter such that $c \log T \leq M \leq T^{1/c}$. Then there are algorithms $\text{CT21.HSG}_f$ and $\text{CT21.Recon}_f$ depending on $f$, such that:

- The algorithm $\text{CT21.HSG}_f(x)$ runs in deterministic $T^c$ time and outputs a set of $M$-bit strings.
- Given $x \in \{0,1\}^n$ and $i \in [T]$ as inputs, and oracle access to a candidate distinguisher $D: \{0,1\}^M \rightarrow \{0,1\}$, $\text{CT21.Recon}_f^D(x,i)$ runs in randomized $(dnM)^c$ time. If $D \left( \frac{1}{M} \right)$-avoids $\text{CT21.HSG}_f(x)$, then with probability $\geq 1 - 2^{-M}$, $\text{CT21.Recon}_f^D(x,i)$ outputs the $i$-th bit of $f(x)$.

Moreover, there is a deterministic algorithm that, given the Turing machine $M_f$ that prints the circuit for $f$ in logspace, outputs the descriptions of $\text{CT21.HSG}_f$ and $\text{CT21.Recon}_f$ in $\text{poly}(\langle |M_f| \rangle)$ time.

**Remark 5.2.** The hardness-randomness tradeoffs in [CT21, CLO+23] were stated for hard functions of the form $f: \{0,1\}^n \rightarrow \{0,1\}^n$, where the reconstruction algorithm prints the entire string $f(x)$ in $\ll T$ time. Inspecting their proofs, it is easy to see that the same holds when we have a function $f: \{0,1\}^n \rightarrow \{0,1\}^T$ and the reconstruction algorithm prints the $i$-th bit of $f(x)$ in $\ll T$ time, given $i \in [T]$ as an input.

**Theorem 5.3 (Non-black-box reduction under logspace-uniform sub-polynomial depth algorithms).** Let $\delta(n) = o(1)$ be any function. Suppose there is a constant $\epsilon > 0$ and an algorithm $A(\langle C \rangle)$ that solves the Implicit-$\delta$-Heavy-Avoid problem on instances $G: \{0,1\}^{N'} \rightarrow \{0,1\}^N$ that are implicitly computable by a circuit $C$ of size $N^\epsilon$, where a description of $C$ is given as input. Moreover, assume that $A$ can be implemented as a logspace-uniform circuit of size $\text{poly}(N)$ and depth $N^{o(1)}$. Then $\text{prBPP} = \text{prP}$.

**Proof.** Since $\text{prBPP} \subseteq \text{prRP}^{\text{prRP}}$ [BF99], it suffices to prove that $\text{prRP} = \text{prP}$. That is, we want a deterministic algorithm that given as input a size-$2M$ circuit $D: \{0,1\}^M \rightarrow \{0,1\}$, distinguishes between the case that $D$ rejects every input and the case that $D$ is $1/2$-dense.

Let $c$ be the constant in Theorem 5.1, and set $N := M^{3c/\epsilon}$. We recall our assumption: given an implicit map with parameters as above, for some $T \leq \text{poly}(N)$, $d \leq N^{o(1)}$ and $\delta \leq o(1)$, there is a logspace-uniform circuit $A$ of dimension $d \times T$ that solves the corresponding Implicit-$\delta$-Heavy-Avoid problem deterministically.

Consider the implicit map $G_D: \{0,1\}^{N'} \rightarrow \{0,1\}^N$, where the underlying circuit $C_D$ for computing each output bit of $G_D$ is as follows. Given $x \in \{0,1\}^{N'}$ and $i \in [N]$, we parse $x$ as $(M_f, r)$, where $M_f$ is a program, and $r$ consists of the remaining bits of $x$ (treated as randomness). We can encode $x$ in such a way that every program of length $\ell$ appears with probability mass $\Theta(2^{-\ell}/\ell^2)$, hence every program with constant description length appears with probability mass $\Omega(1) > \delta$. Suppose that $M_f$ is a logspace-uniform Turing machine that defines a circuit of size $T \cdot \text{poly}(M)$ and depth $d + \text{polylog}(M)$ that computes a function $f: \{0,1\}^{\tilde{O}(M)} \rightarrow \{0,1\}^N$ (this can be syntactically ensured by imposing a space constraint on $M_f$). We let

$$C_D(x, i) := \text{CT21.Recon}_f^D(\langle D \rangle, i; r).$$  (1)
Here, $\text{CT21.Recon}(D, i; r)$ denotes the output of $\text{CT21.Recon}(D, i)$ on randomness $r$. Note that when we compute $C_D(x, i)$, we treat $D$ both as the distinguisher (for the reconstruction algorithm $\text{CT21.Recon}$) and as the input of $f$. We then run $\text{CT21.Recon}$ on randomness $r$ and (attempt to) reconstruct the $i$-th bit of $f(\langle D \rangle)$. If the length of $\langle M_f \rangle$ is a constant, then we have $|r| \geq N^x - O(1) \geq M^{2.9c} \geq (d^2 \cdot \tilde{O}(M) \cdot M)^c$, hence there is always enough randomness to feed into $\text{Recon}$. Also, by Theorem 5.1, the description $\langle C_D \rangle$ can be computed from the description $\langle D \rangle$ in $\text{poly}(M)$ time and $\text{polylog}(M)$ depth.\footnote{The depth upper bound is dominated by computing the description of $\text{CT21.Recon}$ from $M_f$, which takes time $\text{polylog}(M)$. Although $\text{CT21.Recon}$ is an adaptive oracle algorithm, to compute the code of $\text{CT21.Recon}_D$ from $\langle D \rangle$ we only need to concatenate the codes of $\text{CT21.Recon}_f$ and $D$ together, hence this step is depth-efficient.} Note that $G_D$ can be implicitly computed in time $N^x$ due to our choice of parameters.

Define a function $f' : \{0, 1\}^{\tilde{O}(M)} \rightarrow \{0, 1\}^N$ as follows: Given a size-2M circuit $D : \{0, 1\}^M \rightarrow \{0, 1\}$ as input, $f'$ computes the circuit $C_D$ as in (1) and outputs $A(\langle C_D \rangle)$. Recall that $A$ is computed by a logspace-uniform circuit of dimension $d \times T$, hence $f'$ is computed by a logspace-uniform circuit of size $T \cdot \text{poly}(M)$ and depth $d + \text{polylog}(M)$. Assuming $D$ is 1/2-dense, we argue that $\text{CT21.HSG}_{f'}(\langle D \rangle)$ hits $D$. Otherwise, $D$ (1/2)-avoids $\text{CT21.HSG}_{f'}(\langle D \rangle)$. By Theorem 5.1, for every $i \in [N]$, with probability at least $1 - 2^{-M}$ over the randomness $r$, $\text{CT21.Recon}^D_i(\langle D \rangle, i; r)$ is equal to the $i$-th output bit of $f'(\langle D \rangle)$. By a union bound, w.p. at least $1 - N \cdot 2^{-M}$ over the randomness $r$, we have that for every $i \in [N]$, $\text{CT21.Recon}^D_i(\langle D \rangle, i; r) = f'(\langle D \rangle)_i$. Since $f'(\langle D \rangle) = A(\langle C_D \rangle)$ by definition, we have:

$$\Pr_x[G_D(x) = A(\langle C_D \rangle) \mid \langle M_f \rangle = \langle M_{f'} \rangle \text{ where } (M_f, r) = x] \geq 1 - N \cdot 2^{-M} \geq 1/2.$$ 

Since the description length of $M_{f'}$ is a constant, it follows that $\langle M_f \rangle = \langle M_{f'} \rangle$ with constant probability. Hence,

$$\Pr_x[G_D(x) = A(\langle C_D \rangle)] \geq \Omega(1),$$

contradicting our assumption that $A$ solves the Implicit-$\delta$-Heavy-Avoid problem.

We have shown that if $D$ is 1/2-dense, then $\text{CT21.HSG}_{f'}(\langle D \rangle)$ hits $D$. We can compute $\text{CT21.HSG}_{f'}(\langle D \rangle)$ in $\text{poly}(T, N) \leq \text{poly}(M)$ time, hence we can solve the GAP-UNSAT problem in deterministic polynomial time. This implies that $\text{prRP} = \text{prP}$, as desired. \hfill $\square$

**Remark 5.4.** The above reduction is non-black-box for two reasons. First, the statement $\text{prBPP} \subseteq \text{prRP}^{\text{prRP}}$ can be seen as a non-black-box reduction from $\text{prBPP}$ to $\text{prRP}$ [BF99]. Second, and perhaps more interestingly, the reduction from $\text{prRP}$ to Implicit-Heavy-Avoid is also non-black-box, as it requires the algorithm $A$ for Implicit-Heavy-Avoid to be a logspace-uniform circuit of low depth and relies on an application of Theorem 5.1 over this low-depth circuit. Indeed, the proof of Theorem 5.1 performs arithmetization on this low-depth circuit.

**Remark 5.5.** It is also interesting to compare Theorem 5.3 with [Kor22, Theorem 8]. The latter result is a black-box reduction from $\text{prBPP}$ to a problem called R-Lossy CODE. In contrast, our Theorem 5.3 needs additional constraints on the algorithm solving Implicit-Heavy-Avoid and makes non-black-box use of that algorithm.

Our Implicit-Heavy-Avoid is a special case of R-Lossy CODE in the following sense. Recall that R-Lossy CODE is the problem where, given circuits $\text{Comp} : \{0, 1\}^n \times \{0, 1\}^m \rightarrow \{0, 1\}^{n-m}$ and $\text{Decomp} : \{0, 1\}^{n-1} \rightarrow \{0, 1\}^n$ and a parameter $\delta > 0$, one needs to find some $x \in \{0, 1\}^n$ such that $\Pr_{r \sim \{0, 1\}^n}[\text{Decomp}(\text{Comp}(x, r)) = x] < \delta$. Given an implicit map $C : \{0, 1\}^r \times \{0, 1\}^m \rightarrow \{0, 1\}$ as the input of Implicit-$\delta$-Heavy-Avoid (recall that $r < N$ in the typical parameter setting), we can reduce it to the R-Lossy CODE instance $(\text{Comp}, \text{Decomp})$ where $\text{Comp}(x, r)$ simply outputs its
randomness $r$, and $\text{Decomp}(r) = C(r, 1)C(r, 2)\ldots C(r, N)$. In this sense, Implicit-Heavy-Avoid is no more than a special case of R-LOSSY CODE where the compressor circuit is trivial.

5.2 Getting Rid of the Depth Assumption

In this section, we show how to get rid of the low-depth assumption in Theorem 5.3 in a weaker but non-trivial setting. An ideal statement would be: a deterministic polynomial-time algorithm for Implicit-Heavy-Avoid implies a deterministic polynomial-time algorithm for GAP-SAT or CAPP. Compared to the ideal statement, the actual result that we prove only holds for subexponential-time infinitely-often algorithms, as we shall explain later.

We note that it should not be too surprising that a subexponential-time infinitely-often algorithm for Implicit-Heavy-Avoid implies a subexponential-time infinitely-often algorithm for GAP-SAT. In fact, combining Theorem 3.9 and [IW01], it is easy to show that this holds for heuristic algorithms.

Theorem 5.6. The following items are equivalent.

- (Infinitely-often subexponential-time heuristic derandomization.)
  For every language $L \in \text{BPP}$, every ensemble of polynomial-time samplable distributions $D = \{D_n\} \in \text{PSAMP}$, every polynomial $p(\cdot)$, and every constant $\delta > 0$, there exists a deterministic Turing machine $M$ running in $2^{n^{\delta}}$ time, such that for infinitely many input lengths $n$,
  \[
  \Pr_{x \sim D_n} [L(x) \neq M(x)] \leq 1/p(n).
  \]

- (Infinitely-often polynomial-time algorithms for uniform Implicit-Heavy-Avoid.)
  For every $\delta(N) = 1/\text{polylog}(N)$, and for every sequence $\{G_N\}$ of maps $G_N : \{0, 1\}^{\text{polylog}(N)} \to \{0, 1\}^N$ where each bit of $G_N$ is implicitly computed in $\text{polylog}(N)$ time, there is a deterministic $\text{poly}(N)$-time algorithm that solves the Implicit-$\delta$-Heavy-Avoid problem for $\{G_N\}$ on infinitely many input lengths $N$.

Proof Sketch. In fact, both items are equivalent to $\text{EXP} \neq \text{BPP}$. The equivalence between the first item and $\text{EXP} \neq \text{BPP}$ is shown in [IW01], and the equivalence between the second item and $\text{EXP} \neq \text{BPP}$ follows from Theorem 3.9.

We now attempt to prove a version of Theorem 5.6 with respect to worst-case algorithms, instead of heuristics. Our worst-case version of Theorem 5.6 also has many caveats such as being infinitely-often and requiring subexponential time, but the biggest caveat might be that we could only obtain infinitely-often algorithms in the following, somewhat artificial, setting: For a sequence of inputs $\{x_n\}_{n \in \mathbb{N}}$, the algorithms read many inputs $x_1, x_2, \ldots, x_{\text{poly}(n)}$ but are only required to solve $x_n$. We call this “infinitely-often*” algorithms. Formally, we have:

Definition 5.7. Let $\mathcal{P}$ be a computational problem and $\{x_n\}_{n \in \mathbb{N}}$ be a sequence of inputs. We say an algorithm $A$ infinitely-often* solves $\mathcal{P}$ on $\{x_n\}$ if there is a polynomial $p(\cdot)$ such that for infinitely many integers $n$, $A(1^n, x_1, x_2, \ldots, x_{p(n)})$ outputs a valid $\mathcal{P}$-solution for $x_n$.

Note that Theorem 5.6 refers to Implicit-Heavy-Avoid for uniformly samplable distributions, which is different from most results in this section. We believe that more connections between Implicit-Heavy-Avoid for uniformly samplable distributions and average-case derandomization can be obtained (e.g., using the recent “unstructured hardness to average-case randomness” [CRT22]), but we do not pursue this direction here.
In other words, an infinitely-often* algorithm has access to the (non-uniform) sequence of inputs around $x_n$, as opposed to the usual setting where the algorithm only has access to the given input string. We remark that our algorithm in Theorem 5.8 works in a weaker model where the machine only reads $x_n$ and $x_{p(n)}$ and outputs an answer for $x_n$. However, we believe that Definition 5.7 is a more accurate model to capture win-win analyses in complexity theory, hence choose to define it in this way.

For ease of notation, in what follows, we will denote the sequence $x_1, x_2, \ldots, x_\ell$ simply by $x_{1, \ell}$. We are now able to state our main result in this section.

**Theorem 5.8 (A non-black-box reduction for Implicit-Heavy-Avoid).**

Assume there is an infinitely-often polynomial-time algorithm for Implicit-Heavy-Avoid with subexponential stretch. That is, for every constants $a \geq 1$, $\epsilon > 0$, and function $\delta \leq o(1)$, there exists a deterministic algorithm Avoid running in $\text{poly}(N)$ time such that for infinitely many $n \in \mathbb{N}$ and $N := 2^{n^a}$, and for every generator $G : \{0,1\}^{n^a} \to \{0,1\}^N$ implicitly described by a size-$(n^a)$ circuit $C : \{0,1\}^{n^a} \times [N] \to \{0,1\}$, Avoid$(\{C\})$ solves Implicit-Heavy-Avoid on $G$.

Then there is an infinitely-often* subexponential-time algorithm for GAP-SAT. That is, for every $\epsilon > 0$ and $c \geq 1$, there exists a deterministic algorithm Derand running in $2^{n^c}$ time such that the following holds: For every sequence of circuits $\{D_n\}_{n \in \mathbb{N}}$, where each $D_n : \{0,1\}^{n^c} \to \{0,1\}$ is a circuit of size $2n^c$, Derand$(1^n, (D_1, \ldots, D_{n^c}))$ infinitely-often* solves GAP-SAT on $\{D_n\}$.

Our proof combines the two instance-wise hardness-randomness tradeoffs introduced by Chen and Tell [CT21] and Liu and Pass [LP23] recently. Since the hardness-randomness tradeoff in [CT21] is already summarized in Theorem 5.1, in what follows we summarize the hardness-randomness tradeoff in [LP23].

**Definition 5.9.** Let $f : \{0,1\}^n \to \{0,1\}^{T(n)}$ be a function, $A$ be a randomized algorithm, and $x \in \{0,1\}^n$ be an input of $f$. We say that $f(x)$ is $\ell$-leakage resilient hard against $A$ if for every “leakage string” leak $\in \{0,1\}^\ell$, there is some $i \in [T]$ such that $\Pr[A(x, \text{leak}, i) = f(x), i] \leq 2/3$, where the probability is over the internal randomness of $A$.

We need the following result by Liu and Pass [LP23] showing that leakage resilient hardness can be used for derandomization.

**Theorem 5.10 ([LP23]).** There are algorithms LP23.PRG and LP23.Recon and an absolute constant $c \geq 1$ such that the following holds. Let $f : \{0,1\}^n \to \{0,1\}^{T(n)}$ be a function, $D : \{0,1\}^M \to \{0,1\}$ be a distinguisher, and $x \in \{0,1\}^n$ be an input of $f$. Let $\ell := (M \log T)^c$ and $r := O(\log^2 T / \log M)$.

Then:

- LP23.PRG: $\{0,1\}^T \times \{0,1\}^r \to \{0,1\}^M$ runs in deterministic $\text{poly}(T, M)$ time.
- LP23.Recon$^{(-)}: \{0,1\}^{\ell} \times [T] \to \{0,1\}$ runs in randomized $\text{poly}(\ell, \log T)$.
- If $f(x)$ is $\ell$-leakage resilient hard against LP23.Recon$^D$, then LP23.PRG$(f(x), -)$ is a (targeted) PRG that $(1/10)$-fools $D$.

**Proof Sketch.** This follows by observing that the leakage resilient hardness-randomness tradeoff in [LP23] holds instance-wise. In particular, if we let $g$ be the “$k$-reconstructive PRG” described in [LP23, Theorem 3.11] (which follows from [STV01]), then LP23.PRG$(f(x), z) = g^{f(x)}(1^n, 1^M, z)$; the algorithm LP23.Recon is simply the corresponding “reconstruction algorithm” $R$ as defined in [LP23, Definition 3.10].
Suppose that \( f : \{0, 1\}^n \rightarrow \{0, 1\}^{m(n)} \) is computable by a deterministic Turing machine \( M \) running in time \( T(n) \). We can define \( f_{\text{hist}} : \{0, 1\}^n \rightarrow \{0, 1\}^{T(n)} \) for some function \( T'(n) \leq \text{poly}(T(n), m(n)) \) such that \( f_{\text{hist}}(x) \) outputs the computational history of \( f(x) \), i.e., the sequence of configurations of \( M \) when computing over the input string \( x \). It will be useful to consider the leakage-resilience hardness of \( f_{\text{hist}} \), since if \( f_{\text{hist}} \) is not leakage resilient hard, then \( f \) can be computed by a low-depth circuit.

**Claim 5.11.** Let \( f : \{0, 1\}^n \rightarrow \{0, 1\}^{T(n)} \) be a function computable in time \( T(n) \), \( \ell = \ell(n) \) be a parameter, and \( A \) be a randomized algorithm running in time \( T_A(n) \). Then, for \( d(n) := O(T_A(n) + \ell(n) + \log T(n)) \), there is a logspace-uniform circuit \( C : \{0, 1\}^n \rightarrow \{0, 1\}^{T(n)} \) of dimension \( d(n) \times 2^{d(n)} \) such that the following holds. For every input \( x \in \{0, 1\}^n \), if \( f_{\text{hist}}(x) \) is not \( \ell(n) \)-leakage resilient hard against \( A \), then \( f(x) = C(x) \).

**Proof.** The circuit \( C(x) \) enumerates all strings \( \text{leak} \in \{0, 1\}^\ell \) and computes \( A(x, \text{leak}, i) \) for each \( i \). Although \( A \) is a randomized algorithm, we can compute \( A(x, \text{leak}, i) \) by brute force using a logspace-uniform circuit of width \( 2^{O(T_A(n))} \) and depth \( O(T_A(n)) \). Then, for each \( \text{leak} \), \( C \) verifies whether the computational history \( H(x, \text{leak}) \) defined by \( H(x, \text{leak})_i = A(x, \text{leak}, i) \) is indeed the correct history for \( f(x) \); this can be done by a logspace-uniform circuit of width \( \text{poly}(T(n)) \) and depth \( O(\log T(n)) \), by checking that every local step in \( H(x, \text{leak}) \) is correct. Whenever there is some \( \text{leak} \) that corresponds to the correct history for \( f(x) \), the circuit \( C \) outputs the value of \( f(x) \) according to this history. The depth of \( C \) is \( d(n) \leq O(T_A(n) + \ell(n) + \log T(n)) \) and the size of \( C \) is exponential in \( d(n) \). \( \square \)

Now we are ready to prove Theorem 5.8.

**Proof of Theorem 5.8.** Let \( c \geq 1 \) and \( \epsilon > 0 \) be constants. Let \( \{D_n\}_{n \in \mathbb{N}} \) be a sequence of circuits where each \( D_n : \{0, 1\}^{nc} \rightarrow \{0, 1\} \) is of size \( 2^{nc} \). Recall that we want a deterministic \( 2^{nc} \)-time algorithm that infinitely-often solves GAP-SAT on \( \{D_n\} \). Let \( c_1 \) be the constant in Theorem 5.1 (the tradeoff in [CT21]) or Theorem 5.10 (the tradeoff in [LP23]), whichever is larger. Let \( \kappa := \lceil \max\{4c/\epsilon, 8c_1/\epsilon\} \rceil \); the meaning of this constant is that we will perform a win-win analysis over input lengths \( m \) and \( m^\kappa \). Finally, let \( a := 4cc_1 \) and \( L(t) := 2^{ct/4} \). The input instances of our Heavy Avoid algorithm will be generators \( G_t : \{0, 1\}^a \rightarrow \{0, 1\}^{L(t)} \) whose output bits are implicitly computable in \( t^a \) size.

<table>
<thead>
<tr>
<th>( c, \epsilon )</th>
<th>We want a GAP-SAT algorithm on ( nc )-size circuits in ( 2^{nc} ) time</th>
</tr>
</thead>
<tbody>
<tr>
<td>( c_1 )</td>
<td>Overheads of the tradeoffs in Theorems 5.1 and 5.10</td>
</tr>
<tr>
<td>( \kappa = O(c/\epsilon) )</td>
<td>Our win-win analysis is over input lengths ( m ) and ( m^\kappa )</td>
</tr>
<tr>
<td>( a = O(c^2/\epsilon) )</td>
<td>We need a Heavy Avoid algorithm for generators</td>
</tr>
<tr>
<td>( L(t) = 2^{ct/4} )</td>
<td>( G : {0, 1}^a \rightarrow {0, 1}^{L(t)} ), implicitly computable in ( t^a ) size</td>
</tr>
</tbody>
</table>

**Table 1:** Constants used in this proof.

For any integer \( M \), its index \( \text{idx}(M) \) is defined as the largest integer such that \( M = m^{\text{idx}(M)} \) for some integer \( m \). (Note that most integers have index 0.) We say an input length \( M \) is big if \( \text{idx}(M) \) is odd, and is small if \( \text{idx}(M) \) is even. For convenience, we will always use upper case \( M \) to denote big input lengths and lower case \( m \) to denote small input lengths. Looking ahead, each small input length \( m \) will be paired with a big input length \( m^\kappa \) and vice versa; if our Heavy Avoid algorithm succeeds on input length \( m \), then our Gap-SAT algorithm succeeds on either length \( m \) or length \( m^\kappa \).
We now define the sequence of implicit descriptions \( C_t : \{0,1\}^n \times [L(t)] \to \{0,1\} \) for each \( t \in \mathbb{N} \), which we feed into our Heavy Avoid algorithm. Each \( C_t \) also defines the generator \( G_t : \{0,1\}^n \to \{0,1\}^{L(t)} \). Let \( M \) be the \( t \)-th smallest big input length. Note that \( M \leq O(t^\kappa) \). Let \( x \) denote the input of \( G_t \). We parse \( x \) into \( \langle \langle M_f \rangle, r \rangle \), where \( \langle M_f \rangle \) is the description of a Turing machine \( M_f \) and the remaining \( M'(2^{c_1} + 1)^c_1 \) bits \( r \) are treated as randomness. As in the proof of Theorem 5.3, we encode \( x \) in such a way that every constant-length program \( M_f \) occurs with constant probability. Now, let \( d := M^{2^{c_1}} \) and \( f \) be the \( d \times 2^d \) circuit outputted by \( M_f \) within space constraint \( d \). Then

\[
C_t(x,i) := \text{CT21.Recon}^D_M(\langle D_M \rangle, i; r).
\]

Recall that \( \text{CT21.Recon} \) uses at most \( (d \cdot |\langle D_M \rangle|^2)^{c_1} \leq (dM^{2c_1})^{c_1} \) random bits, hence we have enough random bits to feed into \( \text{CT21.Recon} \). We can see that each bit of \( G_t \) can be computed in \( M^{3^{c_1}} \leq t^{4^{c_1}} \leq t^a \) size.

Let \( \text{Avoid} \) be our algorithm for \( \text{Implicit-δ-Heavy-Avoid} \) that runs in deterministic \( \text{poly}(L(t)) \) time. Let \( I \) be the set of input lengths \( t \) for which \( \text{Avoid}(\langle C_t \rangle) \) correctly outputs a \( δ \)-light element of \( G_t \). Using the same reasoning as Theorem 5.3, one can see that:

**Claim 5.12.** \( t \in I, \) \( M \) be the \( t \)-th big input length, and let \( d := M^{t/2} \). Suppose there is a constant-length Turing machine that outputs a circuit \( f' \) of dimension \( d \times 2^d \) in \( O(d) \) space such that \( f'(\langle C_t \rangle) = \text{Avoid}(\langle C_t \rangle) \). Then \( \text{CT21.HSG}_{f'}(\langle C_t \rangle) \) hits \( D_M \).

Let \( \text{Avoid}^{\text{hist}} \) denote the algorithm that outputs the computational history of \( \text{Avoid} \). Note that \( \text{Avoid}^{\text{hist}}(\langle C_t \rangle) \) runs in \( T_{\text{hist}}(t) \leq \text{poly}(L(t)) \) time. Now let \( m \in \mathbb{N} \) be the \( t \)-th small input length, \( M := m^k \) be the \( t \)-th big input length, and \( \ell(t) := (m \cdot \log T_{\text{hist}}(t))^{c_1} \). Consider the following criterion for our win-win analysis:

** Criteria:**

- \( \text{Crit}(t) \): \( \text{Avoid}^{\text{hist}}(\langle C_t \rangle) \) is \( (\ell(t)) \)-leakage resilient hard against \( \text{LP23.Recon}^D_m \).

Our algorithm \( \text{Derand} \) works as follows. On input \( (1^n, \langle D_{1\sim n^k} \rangle) \):

- Suppose \( n \) is small, \( m := n \), and \( M := n^k \). Assume that \( m \) is the \( t \)-th small input length and assume that \( \text{Crit}(t) \) holds. Then by Theorem 5.10, \( \text{LP23.PRG}(\text{Avoid}^{\text{hist}}(\langle C_t \rangle), -): \{0,1\}^n \to \{0,1\}^m \) is a PRG that \( (1/10) \)-fools \( D_m \), where \( r := O(\log^2 T_{\text{hist}}(t)/\log m) \leq O(t'/2)/\log m < m^{t'/2} \). By enumerating this PRG, we can solve the \( \text{GAP-SAT} \) (in fact, \( \text{CAPP} \)) problem on input \( D_m \) in deterministic \( 2^{m^{2^{c_1}}} \) time.

- **Note:** The definition of \( C_t \) involves \( D_M = D_{n^k} \); this is why we need our infinitely-often* algorithm to have access to inputs on a larger length.

- Suppose \( n \) is big, \( M := n \), and \( m := n^{1/k} \). Suppose that \( M \) is the \( t \)-th big input length and assume that \( \text{Crit}(t) \) does not hold. Then it follows from Claim 5.11 that there is a logspace-uniform circuit \( \tilde{\text{Avoid}} \) of dimension \( d' \times T' \) such that \( \tilde{\text{Avoid}}(\langle C_t \rangle) = \text{Avoid}(\langle C_t \rangle) \), where \( d' = O(m^{c_1} + \ell(m) + \log T_{\text{hist}}(t)) \leq O(m^{c_1} + (mt^{c_1}/4)^{c_1}) < M^{c_1/2} \) and \( T' = 2^{d'} \). By Claim 5.12, \( \text{CT21.HSG}_{\text{Avoid}}(\langle C_t \rangle) \) hits \( D_M \). Since the size of this HSG is \( 2^{O(d')} < 2M^{0.9k} \), we can solve the \( \text{GAP-SAT} \) problem on \( D_M \) in \( 2^{M^{0.9k}} \) time.

For every \( t \in I \), let \( m \) be the \( t \)-th small input length and \( M = m^k \). If \( \text{Crit}(t) \) holds, then our algorithm solves \( \text{GAP-SAT} \) on input \( D_m \); otherwise our algorithm solves \( \text{GAP-SAT} \) on input \( D_M \). Since \( |I| \) is infinite, it follows that our algorithm infinitely-often* solves \( \text{GAP-SAT} \) on \( \{D_n\} \).
5.3 On Black-Box Reductions to Heavy-Avoid

We complement the previous non-black-box reductions by showing that if there is a black-box reduction from GAP-SAT to Heavy-Avoid of a certain type, then we would have \( \text{prBPP} = \text{prP} \) unconditionally. In more detail, we consider the natural notion of Levin reductions [Lev73], i.e., “witness-mapping” reductions between search problems, and show that a Levin reduction from search-GAP-SAT to Heavy-Avoid implies \( \text{prBPP} = \text{prP} \).

Intriguingly, these results together separate the notion of (weak) non-black-box reductions and black-box (i.e., Levin) reductions between two natural problems w.r.t. current techniques! That is, improving the weak non-black-box reductions in Theorems 5.3 and 5.8 to Levin reductions (which is a stronger notion of black-box reduction) would imply breakthroughs in complexity theory.

As the notion of Levin reductions is standard in complexity theory (for a recent example, see [MP24]), we only recall its definition in the special case of reducing search-GAP-SAT to Heavy-Avoid:

**Definition 5.13.** We say there is a Levin reduction from search-GAP-SAT to Heavy-Avoid if there are functions \( f, g \) computable in deterministic polynomial time such that the following holds:

- For every circuit \( C : \{0,1\}^n \rightarrow \{0,1\} \) that is 1/2-dense (i.e., that is a valid search-GAP-SAT instance), \( f(C) = (D, 1^t) \) is a Heavy-Avoid instance where we want to find a \((1/t)\)-light string in \( D \).

- For every string \( y \) that is \((1/t)\)-light for \( D \), \( g(C, y) \) is a valid solution for search-GAP-SAT for \( C \). That is, if \( C \) is 1/2-dense, then \( C(g(C, y)) = 1 \).

**Theorem 5.14.** If there is a polynomial-time Levin reduction \((f, g)\) from search-GAP-SAT to Heavy-Avoid, then \( \text{prP} = \text{prBPP} \).

**Proof.** First, we describe the main idea. The crucial observation is that there is a trivial algorithm that “list-solves” any Heavy-Avoid instance, that is, outputs a list of solutions such that some element in the list is not heavy. In particular, let \((D, 1^t)\) be an input instance of Heavy-Avoid, and consider the trivial algorithm that outputs an arbitrary list of \( t+1 \) distinct strings. By an averaging argument, at least one string in this list will be a \((1/t)\)-light element of \( D \). This means that using the list and a witness-mapping reduction we can produce at least one satisfying assignment if the input circuit is dense.

We proceed to the formal proof. Again, by [BF99], it suffices to prove that \( \text{prP} = \text{prRP} \). Let \( C : \{0,1\}^n \rightarrow \{0,1\} \) be an input to GAP-SAT. We first reduce \( C \) to a Heavy-Avoid instance \((D, 1^t) := f(C)\). Then we compute an arbitrary list of \( t+1 \) distinct strings \( x_1, x_2, \ldots, x_{t+1} \). We are guaranteed that some \( x_i \) is a \( \delta \)-light element of \( D \), hence if \( C \) is 1/2-dense, then \( g(C, x_i) \) would be a satisfying assignment of \( C \). Consequently, if there is an index \( i \in [\ell] \) such that \( C(g(C, x_i)) = 1 \), then we output 1, otherwise we output 0. It is easy to see that if \( C \) is unsatisfiable then we always output 0, while if \( C \) is 1/2-dense then we always output 1. Hence we have \( \text{prP} = \text{prRP} \) and this concludes the proof. \( \square \)

5.4 Heavy Avoid versus Almost-All-Inputs Hardness

Finally, we show connections between **Implicit-Heavy-Avoid** and the almost-all-inputs hardness assumptions, introduced recently in [CT21].

The results in this section are motivated by two conjectures. Given the non-black-box reductions in Theorems 5.3 and 5.8, it seems natural to conjecture that **Implicit-Heavy-Avoid** is complete for \( \text{prBPP} \) under “the most natural notion of non-black-box reductions”:
Conjecture 5.15 (Informal). If Implicit-Heavy-Avoid for non-uniform samplers admits a deterministic polynomial-time algorithm, then prBPP = prP.

On the other hand, there is another intriguing conjecture implicit in the work of Chen–Tell [CT21]. Recall that Chen and Tell [CT21] showed how to derive prBPP = prP given a multi-output function $f: \{0,1\}^n \to \{0,1\}^n$ that is almost-all-inputs hard against randomized algorithms, provided that $f$ can be computed in low depth. They also showed that prBPP = prP necessitates the existence of multi-output function with almost-all-inputs hardness, but the hard function they construct might require high depth. Still, this demonstrates that almost-all-inputs hardness might be the right hardness assumption for derandomization. It is tempting to conjecture that low-depth constraints are, in fact, not necessary:

Conjecture 5.16 (Informal). If there is a multi-output function $f: \{0,1\}^n \to \{0,1\}^n$ computable in deterministic polynomial time that is almost-all-inputs hard against fixed-polynomial time randomized algorithms, then prBPP = prP.

In this section, we show that in an “implicit” setting, Conjecture 5.15 and Conjecture 5.16 are equivalent! In fact, we show that Implicit-Heavy-Avoid is the computational problem characterizing the task of “creating” almost-all-inputs hardness (against randomized algorithms). We also show that creating such hardness is equivalent to generating strings with high conditional sublinear-time probabilistic Kolmogorov complexity.

Our “implicit” setting. We consider functions with a possibly long output, i.e., $f: \{0,1\}^n \to \{0,1\}^{\ell(n)}$ where $\ell(n)$ is larger than the running time of our adversaries. Consequently, it makes sense to only require our adversaries to output each bit of $f(x)$ given $x$ and the index of that bit. It is worth noting that the hardness-randomness tradeoffs in both [CT21] and [LP23] holds for such implicit adversaries.

Formally, let $A$ be a randomized algorithm, we say that $A$ locally computes $f(x)$ if for every $i \in [\ell(n)]$, it holds that $\Pr[A(x, i) = f(x)_i] \geq 2/3$, where the probability is over the internal randomness of $A$. An equivalent way of saying this is that $f(x)$ is not 0-leakage resilient hard against $A$ in the sense of Definition 5.9.

We also consider the task of generating strings with high (conditional) sublinear-time probabilistic Kolmogorov complexity. (Our equivalence results hold for both pKpoly and rKpoly.) Roughly speaking, fix a universal Turing machine $U$, a time bound $t$, and strings $x, y$, where $|x| \ll t \ll |y|$. The conditional complexity of $y$ given $x$ is the length of the shortest program $p$ such that $U(p, w, x, i)$ outputs the $i$-th bit of $y$ in $t$ steps, with $w$ being the randomness. However, there is a technical detail that is worth stressing: In our definition, we require the resources ($x$ and $r$) to have length at most $t$, hence the universal Turing machine $U$ has time to read them in their entirety. A possible alternative definition would be that $U$ has oracle access to strings $x$ and $r$ (whose lengths might be $\gg t$), but it is unclear if we can extend our equivalence result (Theorem 5.18) to these alternative definitions.

Definition 5.17 (Sublinear-time probabilistic Kolmogorov complexity). Let $U$ be a universal Turing machine, $x, y \in \{0,1\}^*$, and $t \in \mathbb{N}$. (Think of $|x| \ll t$ and $|y| \gg t$.) We artificially define $y_{|y|+1} = *$.

- **Sublinear-time pK^t complexity:**

  $$pK^t_{\ell}(y | x) := \min \left\{ k \in \mathbb{N} \mid \Pr_{w \sim \{0,1\}^k} \left[ \exists p \in \{0,1\}^k, \forall i \in [|y| + 1], U(1^t, p, w, x, i) = y_i \right] \geq \frac{2}{3} \right\}.$$
In other words, if \( k = pK'_{r'}(y \mid x) \), then with probability at least \( 2/3 \) over the choice of the length-\( t \) random string \( w \), given \( w \) and \( x \), the string \( y \) admits a \( t \)-time-bounded local encoding of length \( k \).

- **Sublinear-time \( rK' \) complexity:**
  \[
  rK'_{r'}(y \mid x) = \min_{p \in \{0,1\}^*} \left\{ \left| \mathbb{P} \left[ \forall i \in [|y|+1], \left( \frac{1}{2} \mathbb{P}_{w \sim \{0,1\}^t} \left[ U(1^t, p, w, x, i) = y_i \right] \geq \frac{2}{3} \right) \right] \right\}.
  \]

Now we are ready to present our equivalence result.

**Theorem 5.18.** The following are equivalent.

1. **(Almost-all-inputs hardness.)** For every polynomial \( p(\cdot) \), there exists a polynomial \( q(\cdot) \) and a function \( f: \{0,1\}^n \to \{0,1\}^{q(n)} \) computable by a deterministic polynomial-time algorithm, such that every algorithm running in randomized \( p(n) \) time only locally computes \( f(x) \) on finitely many inputs \( x \in \{0,1\}^n \).

2. **(Deterministic algorithms for Heavy-Avoid.)** For every polynomial \( p(\cdot) \), there exists a polynomial \( q(\cdot) \) such that the following holds. Let \( C \) denote the class of circuits with \( n \) input bits and \( q(n) \) output bits where each output bit is implicitly computed by a size-\( p(n) \) circuit, then \( C \)-Implicit-(1/p(\( n \))-Heavy-Avoid can be solved in deterministic polynomial time.

3. **(Finding strings with large conditional \( pK^{poly}\)-complexity.)** For every polynomial \( p(\cdot) \), there exists a polynomial \( q(\cdot) \) and a deterministic polynomial-time algorithm that given an input \( x \in \{0,1\}^n \), finds a string \( y \in \{0,1\}^{q(n)} \) such that \( pK^{poly}(y \mid x) \geq \log p(n) \).

4. **(Finding strings with large conditional \( rK^{poly}\)-complexity.)** For every polynomial \( p(\cdot) \), there exists a polynomial \( q(\cdot) \) and a deterministic polynomial-time algorithm that given an input \( x \in \{0,1\}^n \), finds a string \( y \in \{0,1\}^{q(n)} \) such that \( rK^{poly}(y \mid x) \geq \log p(n) \).

Arguably, the most interesting implication above is (1) \( \Rightarrow \) (2), which can be interpreted as *instance-wise hardness vs. randomness* for solving Implicit-Heavy-Avoid without depth constraints.

**Lemma 5.19.** In Theorem 5.18, (1) \( \Rightarrow \) (2) holds. That is, almost-all-inputs hardness (of any kind, without depth restrictions) can be used to solve Implicit-Heavy-Avoid.

**Proof.** Let \( T(n) \) and \( q'(n) \) be two polynomials such that there is a multi-output function \( f: \{0,1\}^n \to \{0,1\}^{q'(n)} \) computable in deterministic \( T(n) \) time that is almost-all-inputs hard against randomized algorithms running in time \( p(n) \). Let \( q(n) := \text{poly}(T(n), q'(n)) \), and \( f_{PCP}: \{0,1\}^n \to \{0,1\}^{q(n)} \) denote the PCP of \( f \): On input \( x \in \{0,1\}^n \), \( f_{PCP}(x) \) outputs the concatenation of strings \( z, pc_{p_1}, pc_{p_2}, \ldots, pc_{p_{q(n)}} \), where \( z = f(x) \) and each \( pc_{p_i} \) is a length-poly \( T(n) \) PCP proof for the assertion that the \( i \)-th output bit of \( f(x) \) is equal to \( z_i \). (Any efficiently-computable PCP with polynomial length and constant query complexity works here, e.g., [ALM+98, BGH+06, Din07].) We claim that \( f_{PCP} \) is an algorithm that solves \( C \)-Implicit-(1/p(\( n \))-Heavy-Avoid.

Suppose, towards a contradiction, that \( f_{PCP} \) does not solve \( C \)-Implicit-(1/p(\( n \))-Heavy-Avoid on an input \( \langle C \rangle \in \{0,1\}^\ell \). The input \( \langle C \rangle \) encodes a circuit \( C \) of size \( p(n) \) (hence \( \ell = O(p(n)) \)) that implicitly represents a generator \( G: \{0,1\}^n \to \{0,1\}^{q(n)} \). Given \( \langle C \rangle \), \( r \in \{0,1\}^n \), and \( i \in [q(n)] \), the \( i \)-th output bit of \( G(r) \) can be computed in \( O(p(n)) \) time. Since \( f_{PCP} \) fails on \( \langle C \rangle \), we have

\[
\mathbb{P}_{r \sim \{0,1\}^n} \left[ f_{PCP}(\langle C \rangle) = G(r) \right] \geq 1/p(n).
\]
Now we present a randomized algorithm \( A \) running in \( p(n)^5 \) time that locally computes \( f \) on input \( (C) \). Given an integer \( i \), we want to compute the \( i \)-th bit of \( f((C)) \). We repeat the following \( O(p(n)^2) \) times:

1. Sample a random string \( r \sim \{0,1\}^n \).
2. Parse \( G(r) \) as the concatenation of \( z, \text{pcp}_1, \text{pcp}_2, \ldots, \text{pcp}_{q(n)} \).
3. Invoke the PCP verifier \( O(p(n)) \) times to verify that \( \text{pcp}_i \) is indeed a correct PCP proof that \( f((C))_i = z_i \).
4. If all invocations of the PCP verifier are successful, then we output \( z_i \) and halt.

If we have not outputted anything after these \( O(p(n)^2) \) iterations, then we output a random bit.

Let \( A \) denote the above randomized algorithm. We now analyze \( A \).

- (Running time.) Since each bit of \( \text{pcp}_i \) can be retrieved in \( \tilde{O}(p(n)) \) time, Step 3 above takes \( \tilde{O}(p(n)^2) \) time, hence the whole algorithm runs in at most \( \tilde{O}(p(n)^4) \leq p(n)^9 \) time.

- ("Soundness.") At each iteration where we parse \( G(r) \) as \( z, \text{pcp}_1, \ldots, \text{pcp}_{q(n)} \), if \( f((C))_i \neq z_i \), then no matter what \( \text{pcp}_i \) is, the PCP verifier will catch an error with probability at least \( 1 - \exp(-p(n)) \). Hence, the probability that \( A((C), i) \) halts in Step 4 above and outputs \( 1 - f((C))_i \) is at most \( \exp(-p(n)) \).

- ("Completeness.") On the other hand, by Eq. (2), with probability at least \( 1 - (1 - 1/(p(n))^{p(n)^2} \geq 1 - \exp(-p(n)) \), there is some iteration of the above algorithm in which \( G(r) = f_{\text{PCP}}((C)) \). During this iteration, it will be the case that \( z_i = f((C))_i \) and \( \text{pcp}_i \) is a valid PCP proof for this, therefore we will output \( f((C))_i \) and halt. It follows that the probability that none of the \( p(n)^2 \) iterations succeed and we output a random bit at the end is also upper bounded by \( \exp(-p(n)) \).

The "Soundness" and "Completeness" above imply that \( A \) locally computes \( f((C)) \). To conclude, if \( f \) is indeed almost-all-inputs hard against randomized algorithms running in time \( p(n)^6 \), then \( f_{\text{PCP}} \) solves the \( \mathcal{C} \text{-Implicit}-(1/p(n))-\text{Heavy-Avoid} \) problem on all but finitely many inputs.

Now we present the complete proof for Theorem 5.18.

**Proof of Theorem 5.18.** We consider each implication below.

(1) \( \Rightarrow \) (2): This follows from Lemma 5.19.

(2) \( \Rightarrow \) (3): Let \( p(n) \) be a polynomial and \( p'(n) := p(n)^2 \). By (2), for some polynomial \( q(n) \), there is a polynomial-time algorithm \( \text{Avoid} \) solving the \( \mathcal{C} \text{-Implicit}-(1/p'(n))-\text{Heavy-Avoid} \) problem, where the generators have output length \( q(n) \) and each bit can be computed in size \( p'(n) \). Let \( U \) be a universal Turing machine and suppose that given input \( x \in \{0,1\}^n \), we want to find a string \( y \in \{0,1\}^{q(n)} \) such that \( \mathcal{K}_U^{p(n)}(y | x) \geq \log p(n) \).

Consider the generator \( G_x : \{0,1\}^{p(n)+\log p(n)} \rightarrow \{0,1\}^{q(n)} \) that is implicitly computed by the following circuit \( C_x \): given \( (z, i) \) as input, where \( z \in \{0,1\}^{p(n)+\log p(n)} \) and \( i \in [q(n)] \), \( C_x(z, i) \) outputs the \( i \)-th bit of \( G(z) \). We parse \( z \) into \( (s, r) \), where \( s \) is a Turing machine of description length \( \log p(n) \), and \( r \in \{0,1\}^{p(n)} \) is treated as randomness. Then, \( C_x(z, i) \) outputs \( U(1^{\log p(n)}, (M), r, x, i) \). Clearly, \( C_x \) can be implemented by a circuit of size \( \tilde{O}(p(n)) < p'(n) \).
Let $y \in \{0, 1\}^{q(n)}$ be any string that is $\frac{1}{p(n)}$-light for $G_x$, we claim that $pK_U^{p(n)}(y \mid x) \geq \log p(n)$. This is easily shown by contradiction. Suppose that $pK_U^{p(n)}(y \mid x) < \log p(n)$, then w.p. at least $2/3$ over $r \sim \{0, 1\}^{p(n)}$, there is a program $M$ of description length $\log p(n)$ such that for every $i \in [n+1]$, $U(1^{p(n)}, \langle M \rangle, r, x, i) = y_i$; in other words, $G_x(\langle M \rangle, r) = y$. This contradicts our assumption that $y$ is $\frac{1}{p(n)}$-light for $G_x$.

Hence, solving the $C$:Implicit-(1/p(n))-Heavy-Avoid problem on $G_x$ will give us a string $y \in \{0, 1\}^{q(n)}$ such that $pK_U^{p(n)}(y \mid x) \geq \log p(n)$.

(3) $\Rightarrow$ (4): This follows from the fact ([LO22, Fact 2]) that for every universal Turing machine $U$, every $x, y \in \{0, 1\}^*$ and every time bound $t$, we have $pK_U(y \mid x) \leq rK_U(y \mid x)$. It is easy to verify that this is still true with respect to our notions of sublinear-time-bounded Kolmogorov complexity.

(4) $\Rightarrow$ (1): Let $q(n)$ be any polynomial, $f: \{0, 1\}^n \rightarrow \{0, 1\}^{q(n)}$ be any function, and $A$ be a randomized algorithm running in $p(n)$ time. We claim that for every input $x \in \{0, 1\}^n$, if $rK_U^{p(n)}(f(x) \mid x) > |A| + o(1)$, then $A$ fails to locally compute $f(x)$. Indeed, if $A$ locally computes $f(x)$, then

$$\forall i \in |x| + 1, \quad \Pr_{r \sim \{0, 1\}^{p(n)}}[A(x, i; r) = U(1^{p(n)}, \langle A \rangle, r, x, i) = f(x)_i] \geq 2/3.$$ 

Clearly, this means that $rK_U^{p(n)}(f(x) \mid x) \leq |A| + O(1)$.

Suppose that $f$ is a deterministic polynomial-time algorithm that given an input $x \in \{0, 1\}^n$, outputs a string $y \in \{0, 1\}^{q(n)}$ such that $rK_U^{p(n)}(y \mid x) \geq 2 \log p(n)$. It follows directly that every randomized algorithm running in $p(n)$ time only locally computes $f(x)$ for finitely many inputs $x \in \{0, 1\}^*$ (as long as the algorithm admits a constant-size description). Hence $f$ is almost-all-inputs hard against $p(n)$-time randomized algorithms. 

Given the above equivalence, it is easy to see that Conjectures 5.15 and 5.16 are equivalent, since Conjecture 5.15 asserts the equivalence between (2) and $\text{prBPP} = \text{prP}$, while Conjecture 5.16 asserts the equivalence between (1) and $\text{prBPP} = \text{prP}$.

References


A Reduction to Range Avoidance

Recall that in the range avoidance problem (referred to as Avoid), we are given an input Boolean circuit $F : \{0, 1\}^m \to \{0, 1\}^n$, with $m < n$, and the goal is to output a string $y \in \{0, 1\}^n$ such that $y \not\in \text{Range}(F) = \{F(x) \mid x \in \{0, 1\}^m\}$ (see, e.g., [Kor21, RSW22]27).

Let $C : \{0, 1\}^m \to \{0, 1\}^n$ be a sampler, where $m$ and $n$ are arbitrary. Note that non-uniform heavy avoid for $0 < \delta < 2^{-m}$ is precisely the range avoidance problem, since a string is not in the range of the map $C$ if and only if its probability under $D_C$ is strictly less than $2^{-m}$. In this work, we are mostly concerned with the regime where $\delta = 1/poly(n)$, i.e., when the goal is to avoid heavy elements. If $n > m$, then it is trivial to reduce Heavy-Avoid to Avoid; however, the goal of this section is to show that even in the regime that $n \leq m$, Heavy-Avoid and Heavy-Find are not harder than range avoidance.

Theorem A.1. For any constant $u \geq 1$, there is a deterministic polynomial-time algorithm that, given oracle access to Avoid, an input circuit $C : \{0, 1\}^m \to \{0, 1\}^n$ of size at most $n^u$, and parameters $\delta > \varepsilon > 1/n^u$, outputs a pair $(\alpha, \beta)$ of $n$-bit strings such that the following hold:

- If there is a string $z \in \{0, 1\}^n$ that is $\delta$-heavy under $D_C$, then $\alpha$ is $(\delta - \varepsilon)$-heavy under $D_C$.
- If there is a string $z \in \{0, 1\}^n$ that is not $(\delta - \varepsilon)$-heavy under $D_C$, then $\beta$ is not $\delta$-heavy under $D_C$.

Proof. In order to prove Theorem A.1, we rely on the fact that a query to Avoid allows us to obtain the description of a Boolean function $h : \{0, 1\}^{\log n} \to \{0, 1\}$ of circuit complexity $O(n / \log n)$ (see, e.g., [Kor21]). In turn, this implies that, for every constant $k \geq 1$ and $\varepsilon \geq 1/poly(n)$, we can compute in time $\text{poly}(n, 1/\varepsilon)$ a pseudorandom generator $G : \{0, 1\}^{O(\log n)} \to \{0, 1\}^n$ that $\varepsilon$-fools circuits of size $n^k$ [NW94, IW97]. Next, we observe that this is sufficient to solve in polynomial time the non-uniform variants of both Heavy-Avoid and Heavy-Find.

We will need the following lemma.

Lemma A.2. There is a constant $b \geq 1$ such that the following holds. Let $C : \{0, 1\}^m \to \{0, 1\}^n$ be a map computed by a circuit of size $s \geq n$. Let $G : \{0, 1\}^\ell \to \{0, 1\}^m$ be a pseudorandom generator that $\varepsilon$-fools any circuit $E : \{0, 1\}^m \to \{0, 1\}$ of size $b \cdot s$. Consider any string $z \in \{0, 1\}^n$, and let $p_z$ be its probability under $D_C$. Then

$$p_z - \varepsilon \leq \Pr_{w \sim \{0, 1\}^\ell}[C(G(w)) = z] \leq p_z + \varepsilon.$$ 

27This problem was called “EMPTY” in [Kor21].
Proof. For any fixed string \( z \in \{0,1\}^n \), we consider the non-uniform circuit \( E_z : \{0,1\}^m \rightarrow \{0,1\} \) that on an input \( x \) outputs 1 if and only if \( C(x) = z \). Note that \( \Pr_x[E_z(x) = 1] = p \). Since \( E_z \) has size at most \( O(s + n) = O(s) \), this circuit is \( \varepsilon \)-fooled by \( G \), assuming that \( b \) is large enough. The pseudorandomness property of \( G \) against the test computed by \( E_z \) yields the desired bound. \( \diamond \)

Consider a generator \( G \) as above. The previous lemma and discussion imply that given a circuit \( C : \{0,1\}^m \rightarrow \{0,1\}^n \) of size polynomial in \( n \) and a parameter \( \varepsilon \geq 1/\text{poly}(n) \), and assuming access to an oracle that solves \( \text{Avoid} \), we can compute in polynomial time (by iterating over all seeds of \( G \)) a set \( S = \{z^1, \ldots, z^a\} \), where \( a = \text{poly}(n) \) and each \( z^i \in \{0,1\}^n \), and probability estimates \( \bar{p}_1, \ldots, \bar{p}_a \), such that the following hold:

1. For every \( i \in [a] \), \( \bar{p}_i - \varepsilon \leq D_C(z^i) \leq \bar{p}_i + \varepsilon \).
2. If a string \( z \in \{0,1\}^n \) satisfies \( D_C(z) > \varepsilon \), then \( z \in S \).

In particular, given parameters \( \delta > \varepsilon \geq 1/\text{poly}(n) \), by picking appropriate parameters in the discussion above we can output in polynomial time a set \( B_{\delta,\varepsilon} \subseteq \{0,1\}^n \) that contains every string \( z \) that is \( \delta \)-heavy in \( D_C \) and no string \( z \) that is not \( (\delta - \varepsilon) \)-heavy in \( D_C \). Given \( B_{\delta,\varepsilon} \), we can easily solve \( \text{Heavy-Avoid} \) (by selecting any string not in \( B_{\delta,\varepsilon} \)) and \( \text{Heavy-Find} \) (by selecting a string in \( B_{\delta,\varepsilon} \)), as in the statement of Theorem A.1. \( \square \)

B Properties of the PSPACE-Complete Language

In this section, we discuss the proof of the following result.

**Theorem 3.1 (A PSPACE-Complete Language with Useful Properties).** There is a language \( L^* \subseteq \{0,1\}^* \) with the following properties:

1. (Complexity Upper Bound) \( L^* \in \text{PSPACE} \).
2. (Completeness) \( L^* \) is PSPACE-hard under DLOGTIME-uniform projection reductions.
3. (Instance Checkability) There is a DLOGTIME-uniform family of BP-AC\(^0[\oplus]\) oracle circuits \( \{\text{IC}_n\}_{n \geq 1} \) making projection queries such that, on every input string \( x \in \{0,1\}^n \) and for every oracle \( O \subseteq \{0,1\}^* \), the following holds:
   - \( \text{IC}_n(x) \) only makes queries of length \( n \) to \( O \).
   - If \( O \) agrees with \( L^* \) on inputs of length \( n \), then \( \Pr_r[\text{IC}_n(x;r) = L^*(x)] = 1 \).
   - For every oracle \( O \), \( \Pr_r[\text{IC}_n(x;r) \in \{\perp, L^*(x)\}] \geq 1 - \exp(-n) \).

To establish this theorem, we verify that the language called \( L^{\text{WH-TV}} \) described in [Che23, Section 7] satisfies the properties we need.\(^{28}\) In particular, [Che23] showed that this language is PSPACE-complete and has instance checkers in AC\(^0[\oplus]\). However, [Che23] only considered \( \mathcal{P} \)-uniformity. For both the instance checker and the PSPACE-hardness reduction, a few minor modifications of the construction are needed to achieve DLOGTIME-uniformity.

The rest of this section will verify the required uniformity conditions by inspecting the proof in [Che23, Section 7]. Note that we will assume familiarity with [Che23, Section 7].\(^{29}\)

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\(^{28}\) We work with \( L^{\text{WH-TV}} \) instead of the final language \( L^{\text{PSPACE}} \) because the only difference between \( L^{\text{WH-TV}} \) and \( L^{\text{PSPACE}} \) is paddability, which is not required for our arguments.

\(^{29}\) The full version of [Che23] can be found at ECCC Report TR22-183.
B.1 Preliminaries

We assume familiarity with notations in [Che23, Section 7] such as \( w_t, \ell_n, \mathbf{s}_n \), and \( \mathbb{F}_n \). We will use length-\( \mathbf{s}_n \) strings and elements in \( \mathbb{F}_n \) interchangeably (instead of explicitly going through the bijection \( \kappa_n \)). The following tasks can be computed in DLOGTIME-uniform \( \text{AC}^0[\oplus] \) [HV06]:

- (Iterated addition.) Given \( \alpha_1, \ldots, \alpha_t \in \{0, 1\}^{\mathbf{s}_n} \), compute \( \sum_{i \in [t]} a_i \in \{0, 1\}^{\mathbf{s}_n} \).
- (Iterated multiplication.) Given \( \alpha_1, \ldots, \alpha_t \in \{0, 1\}^{\mathbf{s}_n} \) where \( t \leq \log n \), compute \( \prod_{i \in [t]} a_i \in \{0, 1\}^{\mathbf{s}_n} \).

We will need a DLOGTIME-uniform \( \text{AC}^0[\oplus] \) circuit for polynomial interpolation over \( \mathbb{F}_n \), which is the following task. Let \( \alpha_1, \alpha_2, \ldots, \alpha_t \) be the lexicographically smallest \( t \) elements in \( \mathbb{F}_n \). Given \( \beta_1, \beta_2, \ldots, \beta_t \in \mathbb{F}_n \) and \( z \in \mathbb{F}_n \) as inputs, the goal is to output \( p(z) \), where \( p : \mathbb{F}_n \to \mathbb{F}_n \) is the unique degree-\((t-1)\) polynomial over \( \mathbb{F}_n \) such that \( p(\alpha_i) = \beta_i \) for every \( i \in [t] \).

It is shown in [Che23, Corollary 7.2] that the polynomial interpolation problem admits a uniform \( \text{AC}^0[\oplus] \) circuit when \( t \leq \log n \). Jumping ahead, the circuit is not DLOGTIME-uniform since (3) requires one to compute the inverse of \( \alpha_i - \alpha_j \), and it is unclear how to compute inverses over \( \mathbb{F}_n \) in DLOGTIME-uniform \( \text{AC}^0[\oplus] \). One way to work around this technical issue is to let the interpolation algorithm output two numbers \( u, v \in \mathbb{F}_n \) such that \( p(z) = u \cdot v^{-1} \).

Claim B.1. For any constant \( t \geq 3 \), there is a DLOGTIME-uniform \( \text{AC}^0[\oplus] \) circuit that given \( z, \beta_1, \ldots, \beta_t \in \mathbb{F}_n \) as inputs, outputs two elements \( u, v \in \mathbb{F}_n \) such that \( p(z) = u \cdot v^{-1} \), where \( p : \mathbb{F}_n \to \mathbb{F}_n \) is the unique degree-(\( t-1 \)) polynomial over \( \mathbb{F}_n \) such that \( p(\alpha_i) = \beta_i \) for every \( i \in [t] \).

Proof Sketch. The expression for \( p(z) \) is

\[
p(z) = \sum_{i \in [t]} \beta_i \cdot \prod_{j \in [t] \setminus \{i\}} \frac{z - \alpha_j}{\alpha_i - \alpha_j}.
\]

Hence, we have \( p(z) = u \cdot v^{-1} \) where

\[
v = \prod_{1 \leq i < j \leq t} (\alpha_i - \alpha_j), \quad \text{and}
\]

\[
u = p(z)v = \sum_{i \in [t]} \beta_i \cdot \prod_{j \in [t] \setminus \{i\}} (z - \alpha_j) \cdot \prod_{1 \leq i' < j' \leq t} (\alpha_i - \alpha_j).
\]

(Note that we omitted some \((-1)^i\) terms since our field has characteristic 2.)

The desired DLOGTIME-uniform \( \text{AC}^0[\oplus] \) circuit follows from [HV06]. \( \Box \)

B.2 The Instance Checker

We assume familiarity with the notations in [Che23, Section 7], such as \( S_Q, f_{n,i}, J_{n,j}, Q_{n,j} \). We start by showing that [Che23, Algorithm 7.1] (i.e., the instance checker for the polynomials \( \{f_{n,i}\} \) defined in [Che23, Lemma 7.3]) admits a DLOGTIME-uniform family of \( \text{BP-AC}^0[\oplus] \) oracle circuits. The instance checker receives input parameters \( n, i \in \mathbb{N} \) such that \( 1 \leq i \leq n \), input \( \bar{x} \in \mathbb{F}_n^\ell \), and oracle access to \( n-i+1 \) functions \( \bar{f}_i, \bar{f}_{i+1}, \ldots, \bar{f}_n : \mathbb{F}_n^\ell \to \mathbb{F}_n \). It draws \( z_i, z_{i+1}, \ldots, z_{n-1} \leftarrow \mathbb{F}_n \) uniformly at random and performs the following steps:

\(^{30}\)Actually, solving the polynomial interpolation problem for \( t = 3 \) suffices for our instance checker.
1. First, it computes $\bar{\alpha}_i, \bar{\alpha}_{i+1}, \ldots, \bar{\alpha}_n \in \mathbb{F}_n^\ell$ as in [Che23, Eq. (8)]. For each $i \leq j \leq n$ and $\ell \in [n]$, let $j_{\text{max}}$ be the maximum $j' < j$ such that $j' \geq i$, $J_{n,j'} = \ell$ and $Q_{n,j'} \neq \text{MUL}$ (or $\perp$ if such $j'$ does not exist). If $j_{\text{max}}$ does not exist, then $(\bar{\alpha}_j)_\ell = x_\ell$; otherwise $(\bar{\alpha}_j)_\ell = z_{j_{\text{max}}}$. We will show in Claim B.2 that given $j$ and $\ell$, we can compute $j_{\text{max}}$ in $O(\log n)$ time; hence, we can compute each $\bar{\alpha}_j$ via a $\text{DLOGTIME}$-uniform projection.

2. Then, it queries the oracles to obtain $r_j = \tilde{f}_j(\bar{\alpha}_j)$ for every $i \leq j \leq n$.

3. Let $t := \lceil \deg + 1 \rceil = 3$ (by [Che23, Lemma 7.7], the polynomials have individual degree at most 2), $w_1, \ldots, w_t$ be the first $t$ non-zero elements of $\mathbb{F}_n$. For each $i \leq j < n$ and $\ell \in [t]$, it queries the oracles to obtain $\bar{\alpha}^t_i := \tilde{f}_{j+1}(\bar{\alpha}_j, t \leftarrow w_i)$.

4. For every $i \leq j < n$ in parallel:
   
   - If $Q_{n,j} = \text{MUL}$, then it verifies that $r_j = r_{j+1} \cdot \text{Term}_{n,j}(\bar{\alpha}_j)$, where $\text{Term}_{n,j}$ is defined in [Che23, Eq. (4)] and hence computable by $\text{DLOGTIME}$-uniform $\text{AC}^0[\oplus]$ circuits.
   
   - Otherwise, let $p$ be the unique degree-$(t - 1)$ polynomial such that $p(w_\ell) = \beta^t_\ell$ for every $\ell \in [t]$, we can use Claim B.1 to obtain the values $p(0), p(1)$, and $p(z_j)$. If $r_j \neq S_{Q_{n,j}}((\bar{\alpha}_j), J_{n,j}, p(0), p(1))$ or $r_{j+1} \neq p(z_j)$, then output $\perp$ and halt.

5. Finally, if $r_n = 1$ then accept and output $r_i := \tilde{f}(\bar{x})$; otherwise output $\perp$.

We remark that the values $p(0), p(1), p(z_j)$ in Item 4 are represented as $u \cdot v^{-1}$ for some $u, v \in \mathbb{F}_n$, but it is still possible to check the equalities. For example:

- If $Q = \exists$, then $S_Q(x, y_0, y_1) = y_0 \cdot y_1$, hence $S_Q(x, y_0 z_0^{-1}, y_1 z_1^{-1}) = r$ if and only if $y_0 y_1 = r z_0 z_1$.

- If $Q = \forall$, then $S_Q(x, y_0, y_1) = 1 - (1 - y_0)(1 - y_1)$, hence $S_Q(x, y_0 z_0^{-1}, y_1 z_1^{-1}) = r$ if and only if $(z_0 - y_0)(z_1 - y_1) = z_0 z_1 (1 - r)$.

- If $Q = \text{LIN}$, then $S_Q(x, y_0, y_1) = xy_1 + (1 - x)y_0$, hence $S_Q(x, y_0 z_0^{-1}, y_1 z_1^{-1}) = r$ if and only if $x y_1 z_0 + (1 - x) y_0 z_1 = r z_0 z_1$.

The first three steps above issue queries to the oracles $\tilde{f}_1, \tilde{f}_{i+1}, \ldots, \tilde{f}_n$, and it is easy to see that these queries can be generated by a $\text{DLOGTIME}$-uniform projection over $\bar{x}$ and $\bar{z} = (z_i, \ldots, z_{n-1})$. The last two steps above perform $\text{DLOGTIME}$-uniform $\text{AC}^0[\oplus]$ computation over $\bar{z}$ and the answers returned from the oracles. This establishes the complexity of the instance checker.

Finally, we need to compute $j_{\text{max}}$ efficiently:

**Claim B.2.** There is an algorithm running in $O(\log n)$ time that given $i < j \leq n$ and $\ell \in [n]$, finds the maximum $j' < j$ such that $j' \geq i$, $J_{n,j'} = \ell$, and $Q_{n,j'} \neq \text{MUL}$.

**Proof.** We recall the definitions of $J_{n,j}$ and $Q_{n,j}$ from [Che23, Proof of Lemma 7.6]. For some integers $m < \lambda < \sqrt{n}$ computable in $O(\log n)$ time, we have:

$$ (J_{n,j}, Q_{n,j}) = \begin{cases} 
(1, \text{LIN}) & \text{if } j > \lambda^2, \\
(j \mod \lambda, \text{LIN}) & \text{otherwise, if } \lambda \nmid j, \\
(1, \text{MUL}) & \text{otherwise, if } j \geq (m + 1)\lambda, \\
(j/\lambda, Q_{j/\lambda} \in \{\exists, \forall\}) & \text{otherwise.}
\end{cases} $$

If $\ell \geq \lambda$, then we can safely return $\perp$. If $\ell = 1$ and $j > \lambda^2 + 1$, then we can simply return $j_{\text{max}} = j - 1$. Otherwise, there are only two possible candidates for $j_{\text{max}}$: 

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The case that \((J_{n,j}, Q_{n,j}) = (j \mod \lambda, \text{LIN})\): the largest \(j' < \min\{j, \lambda^2\}\) s.t. \(j \mod \lambda = \ell\);

The case that \((J_{n,j}, Q_{n,j}) = (j/\lambda, Q_{j/\lambda})\): \(j' = \ell \cdot \lambda\).

We discard any candidate not in the range \([i, j]\) and return the (larger) remaining candidate \(j'\). If both candidates are not in \([i, j]\) then we return \(\perp\). This finishes the algorithm for finding \(j_{\text{max}}\) in \(O(\log n)\) time. □

The instance checker for \(L^{\text{WH-TV}}\) reduces to the instance checker for the polynomials \(\{f_{n,i}\}\) in a straightforward way. In fact, let \(\text{input} \in \{0, 1\}^m\) be the input of \(L^{\text{WH-TV}}\) and \(k\) be the integer defined in [Che23, Algorithm 7.2] (computable in \(O(\log m)\) time), then:

- given access to (a purported oracle for) the \(m\)-th slice of \(L^{\text{WH-TV}}\), one can access the polynomials \(f_{n_i,k}, f_{n_i,k+1}, \ldots, f_{n_i,n_k}\) via \(\text{DLOGTIME}\)-uniform projections;

- given \(\text{input}\) and the answers of each \(f_{n_i,j}(\bar{x})\) \((j \geq i_k)\), where \(\bar{x} \in \mathbb{F}_n^m\) corresponds to the length-\((n \cdot sz_n)\) prefix of \(\text{input}\), one can compute \(L^{\text{WH-TV}}(\text{input})\) using [Che23, Eq. (10) and (11)] via a \(\text{DLOGTIME}\)-uniform \(\text{AC}^0[\oplus]\) circuit.

Since the instance checker for \(\{f_{n,i}\}\) is a \(\text{DLOGTIME}\)-uniform \(\text{BP-AC}^0[\oplus]\) circuit making projection queries, so is the instance checker for \(L^{\text{WH-TV}}\). Finally, the error probability of the instance checker for \(\{f_{n,i}\}\) is at most \(\text{poly}(n)/2^n\) by [Che23, Claim 7.12], hence the error probability of the instance checker for \(L^{\text{WH-TV}}\) is also at most \(\text{poly}(n)/2^n\) by a union bound.

### B.3 PSPACE-Completeness

We also need to show that \(L^{\text{WH-TV}}\) is \(\text{PSPACE}\)-complete under \(\text{DLOGTIME}\)-uniform projections. Note that \(L^{\text{WH-TV}}\) is an arithmetization of the problem \(\text{TQBF}^u\) defined in [Che23, Section 7.3], thus we first show that \(\text{TQBF}^u\) is \(\text{PSPACE}\)-complete under \(\text{DLOGTIME}\)-uniform projections, and then reduce \(\text{TQBF}^u\) to \(L^{\text{WH-TV}}\) using \(\text{DLOGTIME}\)-uniform projections. However, the \(\text{PSPACE}\)-completeness reduction presented in [Che23, Section 7.3] (from the classical \(\text{TQBF}\) to \(\text{TQBF}^u\)) is not a projection, so we need to implement the reduction more efficiently here.

#### Definition of \(\text{TQBF}^u\).

There are \(8 \cdot \binom{n}{3}\) possible width-3 clauses on \(n\) variables and we let \(\phi^{cl-idx}_n\) be a bijection between \([8 \cdot \binom{n}{3}]\) and the set of valid width-3 clauses. A 3-CNF \(\phi\) can thus be described by a string \(y \in \{0, 1\}^{8 \cdot \binom{n}{3}}\). The \(\text{TQBF}^u\) problem takes such a bit-string as an input, constructs the corresponding 3-CNF \(\Phi(x_1, x_2, \ldots, x_n)\), and outputs

\[
Q_1 x_1 Q_2 x_2 \ldots Q_n x_n \Phi(x_1, x_2, \ldots, x_n), \quad (4)
\]

where \(Q_i\) equals \(\exists\) for odd \(i\) and \(\forall\) for even \(i\).

#### \(\text{PSPACE}\)-completeness of \(\text{TQBF}^u\). First, the proof of the \(\text{PSPACE}\)-completeness of \(\text{TQBF}\) (that is, computing (4) when \(\Phi\) is a general circuit) actually shows the following stronger result: For every language \(L \in \text{PSPACE}\), there is a polynomial-time Turing machine \(M\) and a polynomial \(\ell(n)\) such that, for every \(z \in \{0, 1\}^n\), \(z \in L\) if and only if

\[
Q_1 x_1 Q_2 x_2 \ldots Q_{\ell(n)} x_{\ell(n)} M(z_{1\sim n}, x_{1\sim \ell(n)}),
\]

where, again, \(Q_i\) equals \(\exists\) for odd \(i\) and \(\forall\) for even \(i\). (See [ALR99] for an exposition. In particular, [ALR99, Section 6.2] pointed out that the above \(\text{PSPACE}\)-completeness reduction can be computed by a \(\text{DLOGTIME}\)-uniform projection.)
Since $P$ is equal to DLOGTIME-uniform $SIZE[poly]$ [BI94], there is a family of $poly(n)$-size circuits \( \{C_n : \{0, 1\}^{n+\ell(n)} \rightarrow \{0, 1\}\} \) that simulates $M$ and satisfies the following uniformity conditions. Let $s(n) \leq poly(n)$ denote the number of gates in $C_n$ (including input gates), then $1, 2, \ldots, s$ is a valid topological order of $C_n$ (the first $n + \ell$ gates are inputs and the $s$-th gate is the output gate). By adding dummy gates, we may assume that no gate has both children being $z_i$ variables (this will imply that our final is 1-local). Finally, the direct connection language of $C_n$ can be computed in $O(\log n)$ time: there is an algorithm running in deterministic $O(\log n)$ time that given $n$, indices of gates $g_1, g_2, g_3 \in [|C_n|]$ (where $g_1 > \max\{g_2, g_3\}$), and assignments $b_1, b_2, b_3 \in \{0, 1\}$, returns true if and only if the outputs of $g_2$ and $g_3$ are fed as inputs of $g_1$ and \( \{g_i = b_i\}_{i \in [3]} \) is consistent with the gate type of $g_1$ (e.g., if $g_1$ is an AND gate, then it cannot be the case that $b_1 = 1$ but $b_2 = 0$).

Now we show that this implies a DLOGTIME-uniform projection reduction from $L$ to $TQBF^u$. We will reduce an instance $\vec{z} \in \{0, 1\}^n$ of $L$ to a $TQBF^u$ instance $\Phi_2$ with $s(n)$ variables. Let $D$ be a clause expressing

\[
(g_i \neq b_i) \lor (g_j \neq b_j) \lor (g_k \neq b_k),
\]

where $g_i, g_j, g_k$ are variables corresponding to gates in $C_n$ and $b_i, b_j, b_k \in \{0, 1\}$. We may assume that $g_1 > \max\{g_2, g_3\}$. Then, $D$ appears in $\Phi$ if and only if $g_j, g_k$ are fed as inputs of $g_i$ but \( \{g_i = b_i\}_{i \in [3]} \) is inconsistent with the gate type of $g_i$. This information can be retrieved by $O(1)$ queries to the direct connection language.

Finally, the $TQBF^u$ instance we produced is

\[
Q_1x_1 Q_2x_2 \ldots Q_\ell(n)x_\ell(n) \exists g(\ell(n) + n + 1)^{\sim s(n)} \Phi(\vec{z}^{\sim n}, \vec{x}^{\ell(n)}, g(\ell(n) + n + 1)^{\sim s(n)}),
\]

and we may insert $\forall$ quantifiers among $g(\ell(n) + n + 1)^{\sim s(n)}$ to make the quantifiers alternate. It is easy to see that the reduction is computable in DLOGTIME; it is a projection because every clause (i.e., gate in $C_n$) only touches one $z_i$ variable.

**PSPACE-completeness of $L^{WH-TV}$**. The reduction from $TQBF^u$ to $L^{WH-TV}$ is straightforward. First, by [Che23, Lemma 7.10], the truth-table of $TQBF^u$ on input length $m$ coincides with the truth-table of $f_{x,1} = g_{1,1}^{(n)}$ over the Boolean cube, for some $n = poly(m)$. Hence, when $L = TQBF^u$, the algorithm $A_L^{red}$ (as defined in Item 4 of [Che23, Lemma 7.3]) is a DLOGTIME-uniform projection. Second, the reduction in [Che23, Lemma 7.19] produces the instance $(\vec{z}, \vec{y}, \vec{u})$ where $\vec{y}$ and $\vec{u}$ are constant vectors whose each bit can be computed trivially, and $\vec{z} = A_L^{red}(x)$ and $x$ is the input of $L$. Hence, when $L = TQBF^u$, this reduction is also a DLOGTIME-uniform projection over $x$.

Combining all of the above, we can see that the overall reduction from any language $L \in \text{PSPACE}$ to $L^{WH-TV}$ is a DLOGTIME-uniform projection.