

From Nondeterministic to Modular Branching Programs, Uniformly and in Logspace

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Dedicated to Avi Wigderson's 70th birthday.

Abstract

A branching program is a labeled directed graph that, on each input x , induces an s - t connectivity instance. Nondeterministic branching programs (NBPs) accept an input if there exists an accepting path, whereas parity branching programs ($\oplus$ BPs) accept if the number of accepting paths is odd. Wigderson (Structure in Complexity Theory Conference, 1994) showed that NBPs can be simulated by $\oplus$ BPs with the aid of nonuniform advice, establishing

$$\text{NL}/\text{poly} \subseteq \oplus\text{L}/\text{poly}.$$

In this short note, we observe that the recent breakthrough of Chatterjee, Ghosh, Gurjar, Raj, and Thierauf, placing bipartite perfect matching in deterministic NC, yields a uniform version of Wigderson's theorem. Specifically, we show that every nondeterministic branching program can be transformed, in deterministic logspace, into an equivalent polynomial-size parity branching program. More generally, for every fixed prime p , every nondeterministic branching program admits a logspace-uniform simulation by a polynomial-size mod- p branching program. In complexity-theoretic terms, this establishes $\text{NL} \subseteq \oplus\text{L}$ and, more generally, $\text{NL} \subseteq \text{Mod}_p\text{L}$ for every fixed prime p .

1 Introduction

Branching programs and main results. A nondeterministic branching program (NBP) is a finite acyclic directed multigraph with a source s , a sink t , and edges labeled by constants or literals from

$$\{0, 1, x_1, \bar{x}_1, \dots, x_m, \bar{x}_m\}.$$

For an assignment x , let B_x be the subgraph containing the edges whose labels evaluate to 1. The program accepts x if B_x contains an s - t path. A parity branching program accepts x if the number of its enabled source-sink paths is odd. For a prime p , a mod- p branching program accepts x if the number of its enabled source-sink paths is nonzero mod p .¹ The size of a branching program is the number of vertices and edges in the graph.

In this note, we observe that the recent result of Chatterjee, Ghosh, Gurjar, Raj, and Thierauf [7] yields a deterministic logspace reduction from bipartite perfect matching to full-row-rank testing over any finite field of sufficiently large (polynomial) size. Combined with a standard reduction

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¹Some papers additionally require that whenever the number of accepting paths is nonzero modulo p , it is in fact equal to 1 mod p . The two definitions are equivalent up to a multiplicative factor of $p-1$ in size: one may sequentially compose $p-1$ copies of the branching program, thereby mapping a path count k to k^{p-1} mod p . By Fermat's little theorem, this maps 0 to 0 and every nonzero residue modulo p to 1.

from directed reachability to bipartite matching and a standard representation of extension-field linear maps over the base field, which preserves full row rank, this gives a *uniform* simulation of nondeterministic branching programs by parity branching programs. The argument is independent of the characteristic and therefore extends to counting modulo any fixed prime. In particular, the argument places bipartite perfect matching in Mod_pL for every fixed prime p ; composing this containment with the standard logspace reduction from directed reachability to bipartite perfect matching yields the claimed simulation. Formally, we prove the following theorem.

Theorem 1. *For every fixed prime p , there is a deterministic logspace transducer that maps every NBP B to a mod- p branching program C_p of size $|B|^{O(1)}$ such that, for every assignment x ,*

$$B(x) = 1 \iff |\text{Paths}((C_p)_x)| \not\equiv 0 \pmod{p}.$$

Consequently, $\text{NL} \subseteq \text{Mod}_p\text{L}$ for every fixed prime p .

A cryptographic application. The class $\oplus\text{L}$ plays a special role in cryptography. Applebaum, Ishai, and Kushilevitz [3] showed that every function in $\oplus\text{L}$ admits a perfect randomized encoding by an NC^0 function; to the best of our knowledge, this is the largest known standard complexity class for which such a general result is available. Roughly speaking, given a function $f \in \oplus\text{L}$, one can efficiently construct an NC^0 function $g(x; r)$, each of whose output bits depends on at most four input bits, such that, for every input x , the distribution of $g(x; r)$ for uniformly random r perfectly encodes the value $f(x)$. More precisely, an efficient decoder recovers $f(x)$ from $g(x; r)$, while an efficient simulator, given only $f(x)$, samples exactly from the distribution of $g(x; r)$. Here, *perfect* means that decoding is error-free, simulation is exact, and the encoding satisfies several additional regularity properties; see [3] for details.²

Previously known constant-degree randomized encodings for NL functions (that is, functions each of whose output bits is computable in NL) incurred a small correctness or privacy error. Indeed, Ishai and Kushilevitz [10] asked whether NL functions admit perfect constant-degree randomized encodings.³ Combining Theorem 1 with [3, Theorem 4.18 and Lemmas 4.15 and 4.17] yields the following corollary.

Corollary 2. *Every Boolean function computable in NL admits a uniform perfect randomized encoding in NC^0 of degree at most 3 and locality at most 4.*

Remark 3 (Multi-output functions). *The same argument applies more generally to polynomial-time-uniform families of multi-output nondeterministic branching programs. Let $f : \{0, 1\}^* \rightarrow \{0, 1\}^*$ be such that, for every n , the output length $|f(x)| = m(n)$ depends only on $n = |x|$, and let $f_n : \{0, 1\}^n \rightarrow \{0, 1\}^{m(n)}$ denote the restriction of f to n -bit inputs. We say that f is computable by a polynomial-time-uniform family of nondeterministic branching programs if there is a deterministic polynomial-time algorithm that, on input 1^n , outputs descriptions of branching programs $B_{n,1}, \dots, B_{n,m(n)}$, where $B_{n,i}$ computes the i th output bit of f_n . Under this convention, Theorem 1 and [3, Theorem 4.18] imply that every such function admits a uniform perfect randomized encoding in NC^0 . Following the complexity-theoretic notation of [3], the above can be stated as*

$$\text{poly-time uniform NL/poly} \subseteq \text{PREN}.$$

²These additional properties are, in fact, crucial for some applications; see, e.g., [3, Section 6] and [13] for a more recent example.

³We thank Yuval Ishai for pointing us to this open question.

Related work. The relationship between nondeterministic and modular logspace has a long history. Buntrock, Damm, Hertrampf, and Meinel introduced and systematically studied the classes $\text{Mod}_k \text{L}$ and established the connection with linear algebra over finite fields [6]. Wigderson subsequently proved the nonuniform containment

$$\text{NL}/\text{poly} \subseteq \oplus \text{L}/\text{poly},$$

using isolation to reduce reachability to odd reachability [14]. Wigderson’s proof is field-independent and, when interpreted over $\mathbb{F}_p$, yields the analogous nonuniform containment for $\text{Mod}_p \text{L}$. This observation is made explicitly by Gál and Wigderson, who also extended the method to LogCFL and semi-unbounded fan-in circuits [9]. Beimel and Gál [5] gave another simulation of NBPs by counting branching programs with improved size parameters. These simulations rely on fixing suitable randomness as *nonuniform* advice and therefore do not yield uniform transformations. Allender, Reinhardt, and Zhou further investigated the uniform and nonuniform consequences of isolation for matching and logspace counting classes [2]. The linear-algebraic landscape was developed further by Allender, Beals, and Ogihara, who characterized the complexity of matrix rank and linear feasibility over the integers under logspace reductions [1].

2 Proof of the main theorem

Proof of Theorem 1. The proof composes four transformations; Section 2.5 verifies that their composition is computable by a deterministic logspace transducer.

2.1 Directed reachability to bipartite perfect matching

First normalize B by deleting 0-labeled edges, edges entering s , and edges leaving t . Parallel edges may be removed by subdividing each edge $e = (u, v)$ into

$$u \longrightarrow w_e \longrightarrow v,$$

where the first edge retains the label of e and the second is labeled 1. Let $G = (V, E)$ be the resulting directed graph, with distinguished vertices $s, t \in V$; thus (G, s, t) is an instance of directed reachability. For an assignment x , let $G_x = (V, E_x)$ be the subgraph consisting of the enabled edges.

Construct a balanced bipartite graph H with sides

$$L = \{u_L : u \in V \setminus \{t\}\}, \quad R = \{v_R : v \in V \setminus \{s\}\}.$$

For every internal vertex $v \in V \setminus \{s, t\}$ add the constant edge $v_L v_R$, and for every directed edge $u \rightarrow v$ of G add the bipartite edge $u_L v_R$ with the same label. This folklore reduction is recorded explicitly, for example, by Assadi and Raz [4, Section 6.2]; it is also closely related to the classical reductions used by Karp, Upfal, and Wigderson [11]. For every assignment x ,

$$s \rightsquigarrow t \text{ in } G_x \iff H_x \text{ has a perfect matching.} \quad (1)$$

Indeed, an s - t path is completed to a perfect matching using the identity edges $v_L v_R$ for vertices off the path. Conversely, a perfect matching defines, for every $u \neq t$, a unique successor v through the matched edge $u_L v_R$, and every $v \neq s$ has a unique predecessor. Starting at s , this successor relation cannot enter a directed cycle: the first vertex of such a cycle would then have both a predecessor on the cycle and a predecessor from outside it. Hence the resulting directed walk is a simple path, and since t is the only vertex without a left copy, it must terminate at t .

Because parallel edges were removed, every entry of the symbolic bi-adjacency matrix $A_H(x)$ is one of $0, 1, x_i, \bar{x}_i$.

2.2 Perfect matching to rank over an extension of $\mathbb{F}_p$

Let $n = |L| = |R|$. Following Section 3 of [7], extend $A_H(x)$ to an $n \times n^2$ matrix

$$\widehat{A}(x) = (A_H(x) \mid A_0),$$

where A_0 is an $n \times n(n-1)$ binary matrix whose i th row is $\mathbf{0}^{i-1}\mathbf{1}\mathbf{0}^{n-i}$ where $\mathbf{0} = 0^{n-1}$ and $\mathbf{1} = 1^{n-1}$. That is, each row consists of n blocks of size $n-1$ each, where the i th block of the i th row is the all-one block, and all other blocks are zero blocks. Set

$$r = n^3 + 1, \quad D = n(r - n), \quad d = \lceil \log_p(n^2r + 1) \rceil.$$

Let $K = \mathbb{F}_{p^d}$ and choose a primitive element $\gamma \in K$. Then $\text{ord}(\gamma) = p^d - 1 \geq n^2r$. Put

$$\alpha_j = \gamma^{(j-1)r}, \quad 1 \leq j \leq n^2.$$

The elements $\alpha_j \gamma^\ell$, for $1 \leq j \leq n^2$ and $0 \leq \ell < r$, are pairwise distinct. Define the folded Vandermonde matrix

$$V(\alpha_j)_{q,\ell} = (\alpha_j \gamma^\ell)^q, \quad 0 \leq q < D, \quad 0 \leq \ell < r,$$

and form the block matrix

$$M(x)_{i,j} = \widehat{A}_{i,j}(x)V(\alpha_j), \quad M(x) \in K^{nD \times n^2r}. \quad (2)$$

The matching-matrix theorem of Chatterjee, Ghosh, Gurjar, Raj, and Thierauf [7, Theorem 3.1], whose algebraic ingredient is the folded-Wronskian/subspace-design method of Guruswami and Kopparty [8], states that

$$H_x \text{ has a perfect matching} \iff \text{rank}_K M(x) = nD. \quad (3)$$

The dependence on x is local: every block of $M(x)$ is either zero or a fixed folded-Vandermonde matrix multiplied by a single literal or constant.

2.3 Simulating extension-field linear maps over $\mathbb{F}_p$

Fix an $\mathbb{F}_p$ -basis of K . For $a \in K$, let

$$\rho(a) \in \mathbb{F}_p^{d \times d}$$

be the matrix of the $\mathbb{F}_p$ -linear map $z \mapsto az$. Applying ρ entrywise in block form to $M(x)$ gives

$$C(x) = \rho(M(x)) \in \mathbb{F}_p^{nDd \times n^2rd}.$$

The matrices $M(x)$ and $C(x)$ represent the same linear transformation, viewed respectively as a K -linear map and as an $\mathbb{F}_p$ -linear map. Therefore,

$$\text{rank}_{\mathbb{F}_p} C(x) = d \text{rank}_K M(x). \quad (4)$$

Moreover, if an entry of $M(x)$ is $a\ell(x)$, where $a \in K$ is constant and ℓ is a literal or a constant, then the corresponding $d \times d$ block over $\mathbb{F}_p$ is $\ell(x)\rho(a)$. Hence every bit in a fixed binary encoding of $C(x)$ is either a constant or a literal. In particular,

$$\text{rank}_K M(x) = nD \iff \text{rank}_{\mathbb{F}_p} C(x) = nDd. \quad (5)$$

2.4 $\mathbb{F}_p$ full rank to a mod- p branching program

For every fixed prime p , testing whether a matrix over $\mathbb{F}_p$ has full row rank belongs to Mod_pL [6, 12]; in particular, full-row-rank testing over $\mathbb{F}_2$ belongs to $\oplus\text{L}$. Fix a polynomial-time-bounded nondeterministic logspace machine R satisfying

$$\# \text{acc}_R(C) \not\equiv 0 \pmod{p} \iff C \text{ has full row rank over } \mathbb{F}_p. \quad (6)$$

Take the time-layered configuration graph of R . We do not write $C(x)$ explicitly. Whenever R reads a bit of the encoding of $C(x)$, that bit is recomputed from its indices. If it is a constant, the corresponding transition is retained or deleted. If it is a literal $\ell(x)$, a transition requiring the bit to be 1 is labeled by ℓ , while a transition requiring it to be 0 is labeled by $\bar{\ell}$. After padding all computations to a common polynomial length and joining all accepting configurations to a single sink, the resulting acyclic graph is a mod- p branching program P . Its enabled source–sink paths are in bijection with the accepting computations of R on $C(x)$.

Combining (1), (3), (5), and (6) establishes the equivalence asserted in Theorem 1.

2.5 Logspace uniformity

The purely graph-theoretic parts of the construction are computable by deterministic logspace transducers. The normalization of B and the reduction of Section 2.1 can be generated by enumerating the possible output vertices and edges using logarithmic counters and determining, by a logspace scan of B , whether each edge is present and how it is labeled. Likewise, the time-layered configuration graph in Section 2.4 can be generated in deterministic logspace, provided that whenever R queries a bit of the encoding of $C(x)$, that bit can be recomputed from its position using logarithmic space. It therefore remains to verify this property for the algebraic constructions of Sections 2.2 and 2.3.

Since $d = O(\log n)$, the field K has polynomial size. A degree- d irreducible polynomial over $\mathbb{F}_p$ can be found deterministically in $O(d \log p)$ workspace by enumerating all monic degree- d polynomials and rejecting any candidate divisible by a nonconstant monic polynomial of degree at most $d/2$. Candidate polynomials, possible divisors, and remainders require $O(d \log p) = O(\log n)$ bits. Once a representation of K is fixed, a primitive element can be found by enumerating K^* and computing successive powers; the field has polynomial size, so this takes polynomial time and logarithmic workspace.

Given their indices, the entries of the folded Vandermonde matrices and the bits of the multiplication matrices $\rho(a)$ are therefore computable in $O(\log n)$ space. Configurations and transitions of the fixed machine R can likewise be enumerated in logarithmic space. Closure of logspace transductions under composition shows that $B \mapsto P$ is a deterministic logspace transduction, and all intermediate dimensions are polynomial in $|B|$. Finally, the standard logspace-uniform correspondence between polynomial-size NBPs and nondeterministic logspace machines yields $\text{NL} \subseteq \text{Mod}_p\text{L}$ and, for $p = 2$, $\text{NL} \subseteq \oplus\text{L}$. $\square$

Remark 4 (DLOGTIME-uniformity). *The construction also preserves DLOGTIME-uniformity. Let $\{B_m\}_{m \geq 1}$ be a DLOGTIME-uniform polynomial-size family of NBPs. Rather than having the transducer evaluate the field-dependent entries of $C(x)$, we incorporate their deterministic logspace computation into the machine simulating R . Whenever this computation needs to inspect an edge or a label of B_m , it simulates the direct-connection procedure for the family. For the fixed family $\{B_m\}_{m \geq 1}$, the resulting machine $\hat{R}$ is fixed, polynomial-time bounded, and uses $O(\log m)$ space. Under the standard encoding, each time-layered configuration of $\hat{R}$ has length $O(\log m)$, and whether*

one configuration follows another in a single step can be decided in $O(\log m)$ time. Transitions that query the i th input bit are labeled by x_i or $\bar{x}_i$, as appropriate. Hence the resulting symbolic configuration graphs have a DLOGTIME-decidable direct-connection relation. Consequently, the transformation maps every DLOGTIME-uniform family of nondeterministic branching programs to a DLOGTIME-uniform family of equivalent mod- p branching programs.

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